

Status: **Active**

- Specifications
- Product Options
- Documentation & Tools
- Technical Support
- Contact Sales

Programmable Timing Control Hub™ for Next Gen P4™ processor

Features

- 3 - 0.7V current-mode differential CPU pairs
- 10 - PCI, 3 free running, 33MHz
- 2 - REF, 14.318MHz
- 3 - 3V66, 66.66MHz
- 1 - 48MHz
- 1 - 24/48MHz
- 2 - 25MHz @ 2.5V

Product Specification

CHIPSET MANUFACTURER	CHIPSET NAME	CLOCK SPEC.	CORE VOLTAGE (V)	INPUT FREQ (MHZ)	OUTPUTS (#)	PLATFORM NAME	POWER TYP (MW)	SPREAD SPECTRUM	XTAL FREQ (KHZ)
VIA	PM880, PT880		3.3	14.318000 - 400.000000	22			Yes	14.318180

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
952909AKLF	Active	NLG56	VFQFPN	56	C	Y	Check Inventory
952909AKLFT	Active	NLG56	VFQFPN	56	C	Y	Check Inventory
952909BKLF	Active	NLG56	VFQFPN	56	C	Y	Check Inventory
952909BKLFT	Active	NLG56	VFQFPN	56	C	Y	Check Inventory

Documents

TITLE	TYPE ▲	FORMAT	FILE SIZE	DATE
PC Clocks Contact Info	Misc	PDF	62 KB	05/29/2007
PCN#: A1104-02 ADD CHIPMOS TAIWAN AS ALTERNATE ASSEMBLY FOR COPPER WIRE BOND PROCESS ON VFQFN PACKAGES	Product Change Notice	PDF	101 KB	08/06/2011
PCN#: A0910-01 ALTERNATE ASSEMBLY FACILITY FOR COPPER WIRE PROCESS ON VFQFPN-20, 32, 40, 48, 56	Product Change Notice	PDF	116 KB	11/05/2009
PDN#: K-07-06 TO DISCONTINUE 952909BKLF AND 952909BKLF AND REPLACED WITH 952909CKLF AND 952909CKLF	Product Delete Notice	PDF	46 KB	12/17/2007

CONTACT SALES OFFICE ▶