

August 1991 Revised February 2002

74AC05

Hex Inverter with Open Drain Outputs

General Description

The AC05 contains six inverters.

Features

- Outputs sink 24 mA
- Open drain for wired NOR function
- Radiation tolerant FACT[™] process

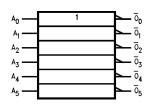
Ordering Code:

Order Number	Package Number	Package Description				
74AC05SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow				

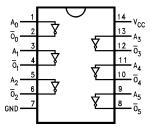
Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering code.

Logic Symbol

IEEE/IEC



Connection Diagram



Pin Descriptions

Pin Names	Description			
A _n	Inputs			
\overline{O}_n	Outputs			

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Absolute Maximum Ratings(Note 1)

 $\begin{aligned} & V_{I} = -0.5V & -20 \text{ mA} \\ & V_{I} = V_{CC} + 0.5V & +20 \text{ mA} \end{aligned}$

DC Input Voltage (V_I) $-0.5V \text{ to V}_{CC} + 0.5V$

DC Output Diode Current (I_{OK})

DC Output Voltage (V_O) -0.5V to +7.0V

DC Output Sink Current (I_O) + 50 mA

DC V_{CC} or Ground Current

per Output Pin (I_{CC} or I_{GND}) \pm 50 mA Storage Temperature (T_{STG}) -65° C to +150 $^{\circ}$ C

Recommended Operating Conditions

Minimum Input Edge Rate (ΔV/Δt)

 $V_{\mbox{\footnotesize{IN}}}$ from 30% to 70% of $V_{\mbox{\footnotesize{CC}}}$

 $V_{CC} @ 3.3V, 4.5V, 5.5V$ 125 mV/ns

Note 1: Absolute maximum ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation of FACT™ circuits outside databook specifications.

DC Electrical Characteristics

Symbol	Parameter	V _{CC}	T _A = +25°C		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	Units	Conditions	
- Cyllibol	i arameter	(V)	Тур	Guaranteed Limits		Onito		
V _{IH}	Minimum HIGH Level	3.0	1.5	2.1	2.1		V _{OUT} = 0.1V	
	Input Voltage	4.5	2.25	3.15	3.15	V	or V _{CC} – 0.1V	
		5.5	2.75	3.85	3.85			
V _{IL}	Maximum LOW Level	3.0	1.5	0.9	0.9		V _{OUT} = 0.1V	
	Input Voltage	4.5	2.25	1.35	1.35	V	or V _{CC} – 0.1V	
		5.5	2.75	1.65	1.65			
V _{OL}	Maximum LOW Level	3.0	0.002	0.1	0.1			
	Output Voltage	4.5	0.001	0.1	0.1	V	$I_{OUT} = 50 \mu A$	
		5.5	0.001	0.1	0.1			
							$V_{IN} = V_{IL}$ or V_{IH}	
		3.0		0.32	0.44		I _{OL} = 12 mA	
		4.5		0.36	0.44	V	$I_{OL} = 24 \text{ mA}$	
		5.5		0.36	0.44		I _{OL} = 24 mA (Note 2)	
I _{IN} (Note 4)	Maximum Input Leakage Current	5.5		±0.1	±1.0	μΑ	$V_I = V_{CC}$, GND	
I _{OHZ}	Off-State Current	6		+0.5	+10.0	μΑ	$V_{IN} = GND, V_O = 6V$	
I _{OLD}	Minimum Dynamic	5.5		50	75	mA	V _{OLD} = 1.65V Max	
	Output Current (Note 3)			30	7.5		VOLD = 1.05 V IVIAX	
I _{CC} (Note 4)	Maximum Quiescent Supply Current	5.5		4.0	20.0	μΑ	$V_{IN} = V_{CC}$ or GND	

Note 2: All outputs loaded; thresholds on input associated with output under test.

Note 3: Maximum test duration 2.0 ms. one output loaded at a time.

Note 4: I_{IN} and $I_{\text{CC}} @ 3.0 \text{V}$ are guaranteed to be less than or equal to the respective limit @ 5.5 V V_{CC} .

AC Electrical Characteristics

Symbol	Parameter	V _{CC} (V)		T _A = +25°C C _L = 50 pF		T _A = -40°C to +85°C	
		(Note 5)	Min	Max	Min	Min	
t _{PLZ}	Propagation Delay	3.3	2.0	14.5	2.0	14.5	ns
	(Note 6)	5.0	2.0	14.0	2.0	14.0	115
t _{PZL}	Propagation Delay	3.3	2.0	6.5	2.0	6.5	ns
		5.0	2.0	5.0	2.0	5.0	115

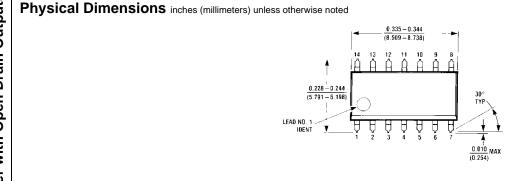
Note 5: Voltage Range 3.3 is $3.3V \pm 0.3V$ Voltage Range 5.0 is $5.0V \pm 0.5V$

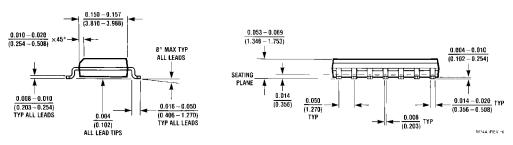
Note 6: AC Load is $V_{CC} x 2$, $R_L = 1 k\Omega$

 $C_{L} = 50 \text{ pF}$

Capacitance

Symbol	Symbol Parameter		Units	Conditions	
C _{IN}	Input Capacitance	4.5	pF	V _{CC} = 5.0V	
C _{PD}	Power Dissipation Capacitance	30.0	pF	V _{CC} = 5.0V	





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