

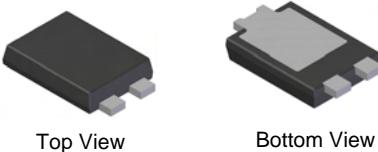
Features

- Guard Ring Die Construction for Transient Protection
- High Surge Current Capability
- Low Leakage Current
- Low Forward Voltage Drop
- High Forward Surge Current Capability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

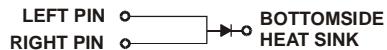
- Case: POWERDI5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Polarity: See Diagram
- Weight: 0.093 grams (approximate)

POWERDI5



Top View

Bottom View



Note: Pins Left & Right must
be electrically connected
at the printed circuit board.

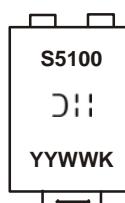
Ordering Information (Note 4)

Part Number	Case	Packaging
PDS5100-13	POWERDI5	5000/Tape & Reel
PDS5100Q-13D (Note 5)	POWERDI5	5000/Tape & Reel
PDS5100-7	POWERDI5	1500/Tape & Reel

Notes:

- EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <http://www.diodes.com>.
- "D" suffix designate for the 12mm Tape and Reel option.

Marking Information



S5100 = Product type marking code
 DII = Manufacturers' code marking
 YYWW = Date code marking
 YY = Last digit of year (ex: 04 for 2004)
 WW = Week code (01 - 53)
 K = Factory Designator

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}		
Working Peak Reverse Voltage	V_{RWM}	100	V
DC Blocking Voltage	V_R		
RMS Reverse Voltage	$V_{R(\text{RMS})}$	71	V
Average Rectified Output Current (See also figure 5)	I_O	5	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load	I_{FSM}	120	A

Thermal Characteristics

Characteristic	Symbol	Typ	Max	Unit
Thermal Resistance Junction to Soldering Point	$R_{\theta JS}$	—	2.6	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction to Ambient Air (Note 6) $T_A = +25^\circ\text{C}$	$R_{\theta JA}$	90	—	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction to Ambient Air (Note 7) $T_A = +25^\circ\text{C}$	$R_{\theta JA}$	70	—	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction to Ambient Air (Note 8) $T_A = +25^\circ\text{C}$	$R_{\theta JA}$	50	—	$^\circ\text{C}/\text{W}$
Operating Temperature Range	T_J	-65 to +150		$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-65 to +175		$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 9)	$V_{(BR)R}$	100	—	—	V	$I_R = 200\mu\text{A}$
Forward Voltage	V_F	—	0.74	0.79	V	$I_F = 5\text{A}, T_S = +25^\circ\text{C}$
		—	0.64	0.68		$I_F = 5\text{A}, T_S = +100^\circ\text{C}$
		—	0.60	0.64		$I_F = 5\text{A}, T_S = +125^\circ\text{C}$
		—	0.81	0.89		$I_F = 10\text{A}, T_S = +25^\circ\text{C}$
		—	0.68	0.73		$I_F = 10\text{A}, T_S = +125^\circ\text{C}$
Reverse Leakage Current (Note 9)	I_R	—	0.002	0.2	mA	$T_S = +25^\circ\text{C}, V_R = 100\text{V}$
		—	0.5	5		$T_S = +100^\circ\text{C}, V_R = 100\text{V}$
		—	2	20		$T_S = +125^\circ\text{C}, V_R = 100\text{V}$

Notes:

6. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per <http://www.diodes.com>
7. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per <http://www.diodes.com>.
8. Polymide PCB, 2 oz. Copper. Cathode pad dimensions 9.4mm x 7.2mm. Anode pad dimensions 2.7mm x 1.6mm.
9. Short duration pulse test used to minimize self-heating effect.

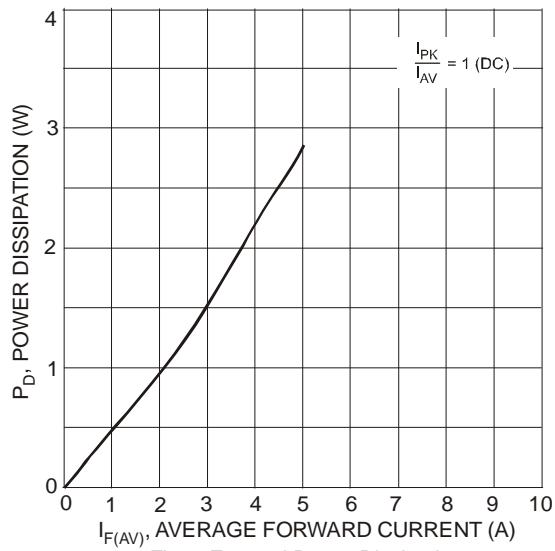


Fig. 1 Forward Power Dissipation

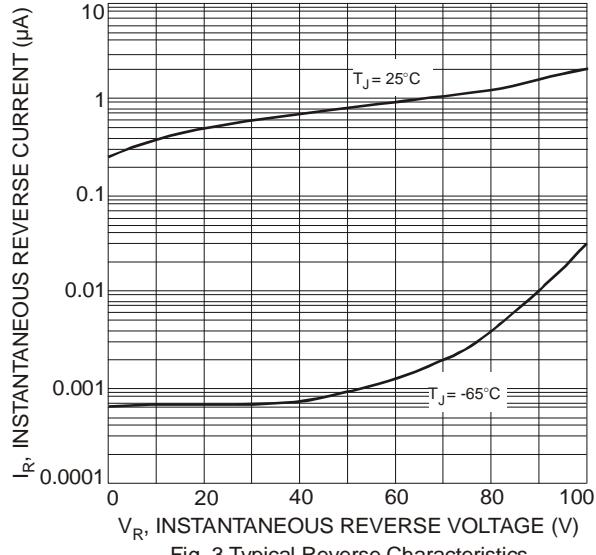


Fig. 3 Typical Reverse Characteristics

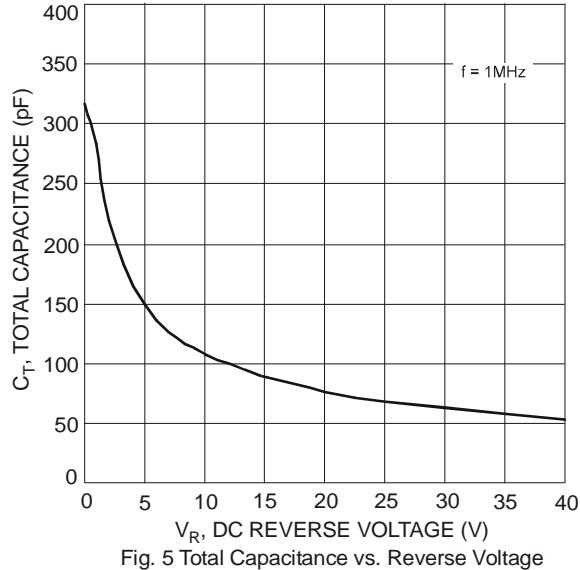


Fig. 5 Total Capacitance vs. Reverse Voltage

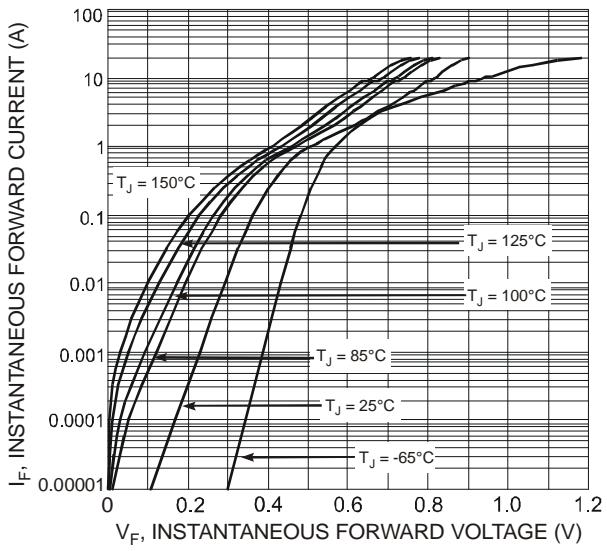


Fig. 2 Typical Forward Characteristics

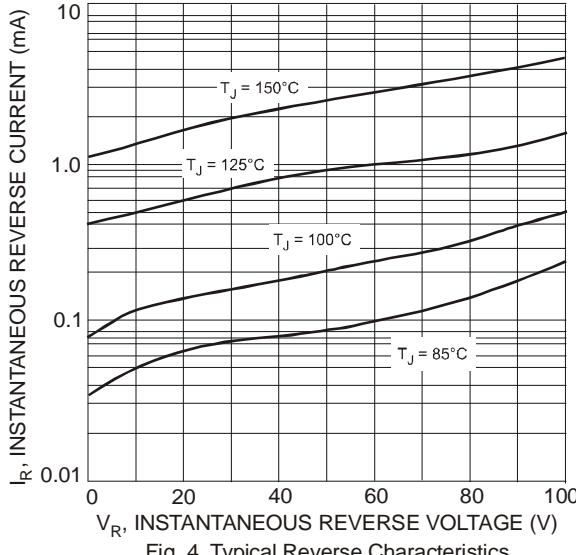


Fig. 4 Typical Reverse Characteristics

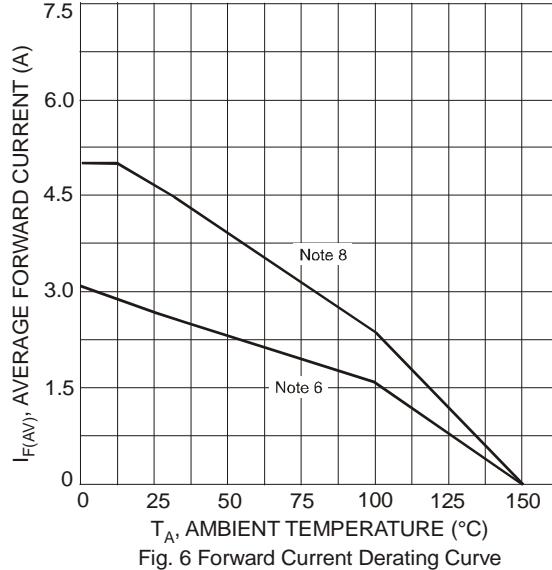


Fig. 6 Forward Current Derating Curve

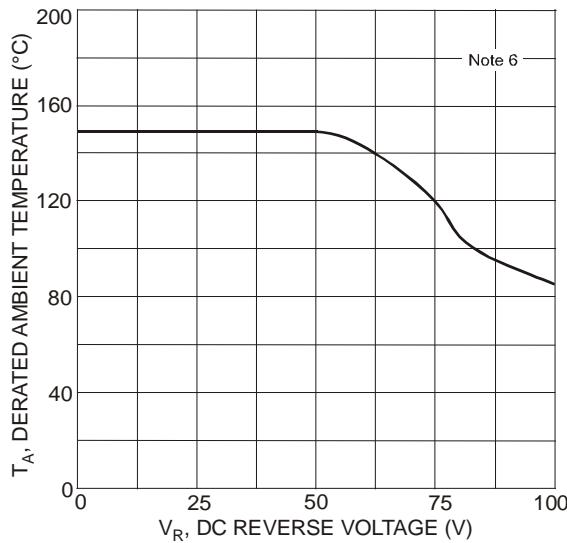
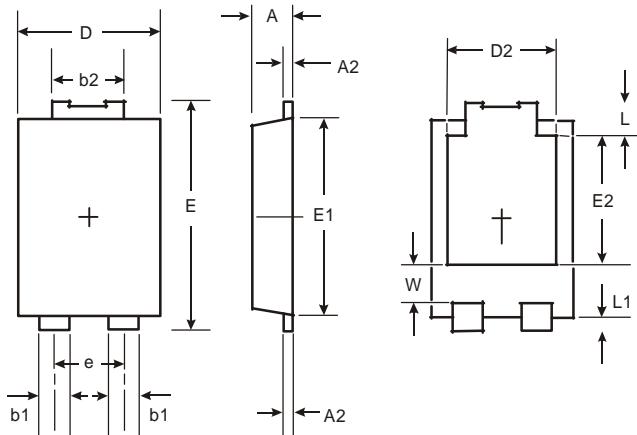


Fig. 7 Operating Temperature Derating

Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.

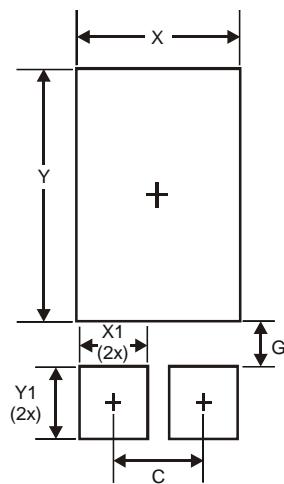


POWERDI5		
Dim	Min	Max
A	1.05	1.15
A2	0.33	0.43
b1	0.80	0.99
b2	1.70	1.88
D	3.90	4.05
D2	3.054 Typ	
E	6.40	6.60
e	1.84 Typ	
E1	5.30	5.45
E2	3.549 Typ	
L	0.75	0.95
L1	0.50	0.65
W	1.10	1.41

All Dimensions in mm

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	1.840
G	0.852
X	3.360
X1	1.390
Y	4.860
Y1	1.400

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