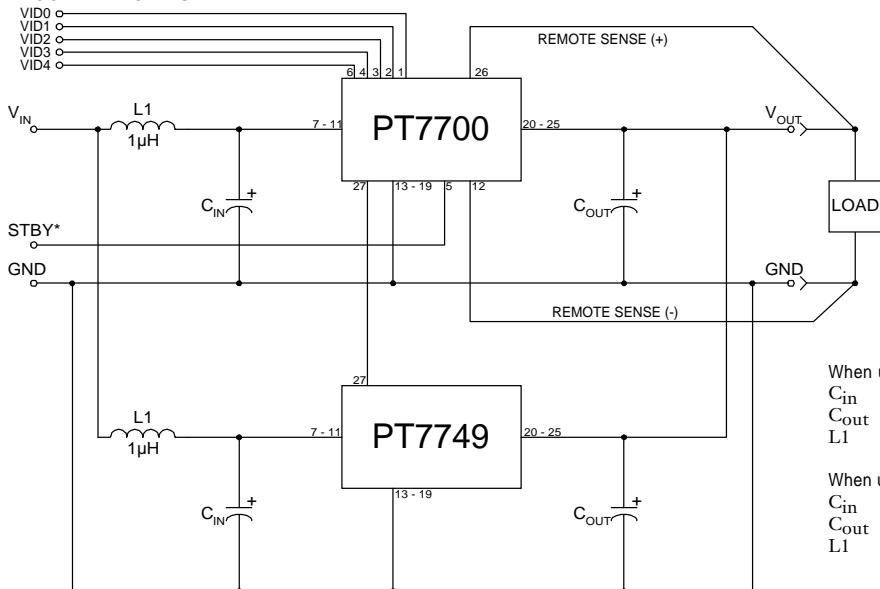


### Features

- Current Boost
- Tracks Vout of PT7705/06/07
- High Efficiency
- Input Voltage Range: 3V to 5.5V
- Synchronized with Regulator
- 27-pin SIP Package
- Run up to 4 in Parallel - 90 Amps

### Standard Application

#### PROGRAMMING PINS



### Pin-Out Information

Pin	Function	Pin	Function
1	Do not connect	15	GND
2	Do not connect	16	GND
3	Do not connect	17	GND
4	Do not connect	18	GND
5	Do not connect	19	GND
6	Do not connect	20	V <sub>out</sub>
7	V <sub>in</sub>	21	V <sub>out</sub>
8	V <sub>in</sub>	22	V <sub>out</sub>
9	V <sub>in</sub>	23	V <sub>out</sub>
10	V <sub>in</sub>	24	V <sub>out</sub>
11	V <sub>in</sub>	25	V <sub>out</sub>
12	Do not connect	26	Do not connect
13	GND	27	Master Sync In
14	GND		

### Ordering Information

#### PT7749

(For dimensions and PC Board layout,  
see Package Styles 800 and 810.)

### PT Series Suffix (PT1234X)

#### Case/Pin Configuration

Vertical Through-Hole	N
Horizontal Through-Hole	A
Horizontal Surface Mount	C

**Output Capacitors:** When used with a PT7705 or PT7706, the PT7749 requires a minimum output capacitance of 1200 $\mu$ F. When used with a PT7707, the PT7749 requires a minimum output capacitance of 330 $\mu$ F for proper operation. Do not use Oscon type capacitors. The maximum allowable output capacitance is 15,000 $\mu$ F.

**Input Filter:** An input filter is optional for most applications. The input inductor must be sized to handle 18ADC with a typical value of 1 $\mu$ H. The input capacitance must be rated for a minimum of 1.3Arms of ripple current. For transient or dynamic load applications, additional capacitance may be required.

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