

GaAs SPST Switch DC - 2.5 GHz

Rev. V4

Features

- Very Low Power Consumption: 50 μ W
- Low Insertion Loss: 1.0 dB
- High Isolation: 35 dB up to 2 GHz
- Very High Intercept Point: 46 dBm IP3
- Nanosecond Switching Speed
- Temperature Range: -40°C to $+85^{\circ}\text{C}$
- Lead-Free SOIC-8 Plastic Package
- 100% Matte Tin Plating over Copper
- Halogen-Free “Green” Mold Compound
- 260 $^{\circ}\text{C}$ Reflow Compatible
- RoHS* Compliant Version of SW-259

Description

The MASWSS0162 is a GaAs MMIC SPST switch in a lead-free SOIC-8 lead surface mount plastic package.

The MASWSS0162 is ideally suited for use where low power consumption is required. Typical applications include transmit/receive switching, switch matrices and switched filter banks in systems such as radio and cellular equipment, PCM, GPS, fiber optic modules, and other battery powered radio equipment.

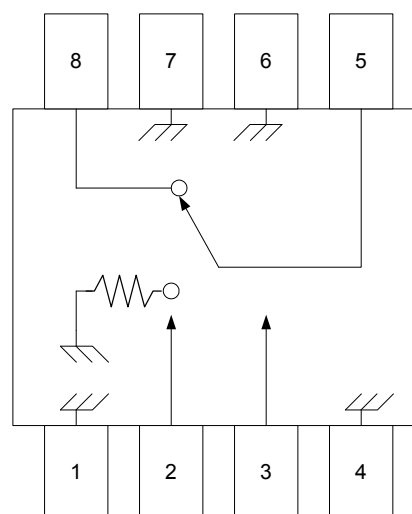
The MASWSS0162 is fabricated using a monolithic GaAs MMIC using a mature 1 micron process. The process features full chip passivation for increased performance and reliability.

Ordering Information¹

Part Number	Package
MASWSS0162	Bulk Packaging
MASWSS0162TR	1000 piece reel

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration

Pin#	Description
1	Ground
2	A
3	B
4	Ground
5	RF Port 2
6	Ground
7	Ground
8	RF Port 1

* Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

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Electrical Specifications²: $T_A = +25^\circ\text{C}$, $V_C = -5\text{ V} / 0\text{ V}$, $P_{IN} = 0\text{ dBm}$

Parameter	Test Conditions	Units	Min.	Typ. ³	Max.
Insertion Loss ³	DC - 0.5 GHz 0.5 - 1.0 GHz 1.0 - 2.0 GHz	dB	—	0.8 0.9 1.1	— 1.2 —
Isolation ³	DC - 0.5 GHz 0.5 - 1.0 GHz 1.0 - 2.0 GHz	dB	— 45 —	65 53 40	—
VSWR On VSWR Off	DC - 2.0 GHz	Ratio	—	1.2:1	—
1 dB Compression	P_{IN} @ 0.05 GHz P_{IN} @ 0.5 - 2.0 GHz	dBm	—	18 23	—
T_{RISE} , T_{FALL}	10% to 90% RF, 90% to 10% RF	ns	—	4	—
T_{ON} , T_{OFF}	50% Control to 90% RF, 50% Control to 10% RF	ns	—	8	—
Transients	In-Band	mV	—	35	—
2nd Order Intercept	Measured Relative to Input Power, two-tone up to 5 dBm 0.05 GHz 0.5 - 2.0 GHz	dBm	—	55 68	—
3rd Order Intercept	Measured Relative to Input Power, two-tone up to 5 dBm 0.05 GHz 0.5 - 2.0 GHz	dBm	—	40 46	—
Control Current	—	μA	—	—	25

2. All measurements with 0, -5 V control voltages at 1.0 GHz in a 50 Ω system, unless otherwise specified.

3. Typical values listed are based on average of frequency range noted.

Absolute Maximum Ratings^{4,5}

Parameter	Absolute Maximum
Input Power ⁶ 0.05 GHz 0.5 - 2.0 GHz	27 dBm 34 dBm
Control Voltage	+5 V, -8.5 V
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C

4. Exceeding any one or combination of these limits may cause permanent damage to this device.
 5. MACOM does not recommend sustained operation near these survivability limits.
 6. When the RF Input power is applied to a terminated port, the absolute maximum is +32 dBm.

Truth Table^{7,8}

Control Inputs		Condition of Switch
A	B	RF State
1	0	On
0	1	Off

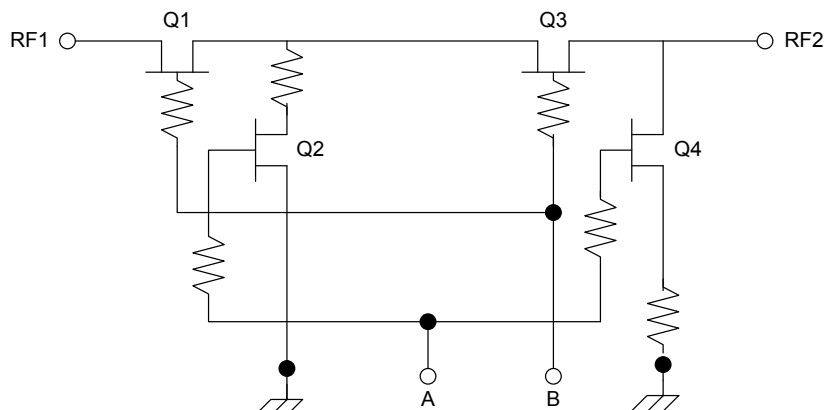
7. "0" = 0 to -0.2 V @ 20 μA maximum.

8. "1" = -5 V @ 20 μA typical to -8 V @ 600 μA maximum.

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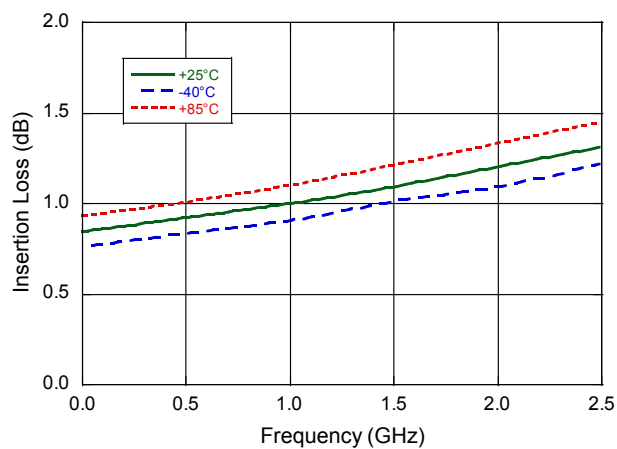
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Electrical Schematic

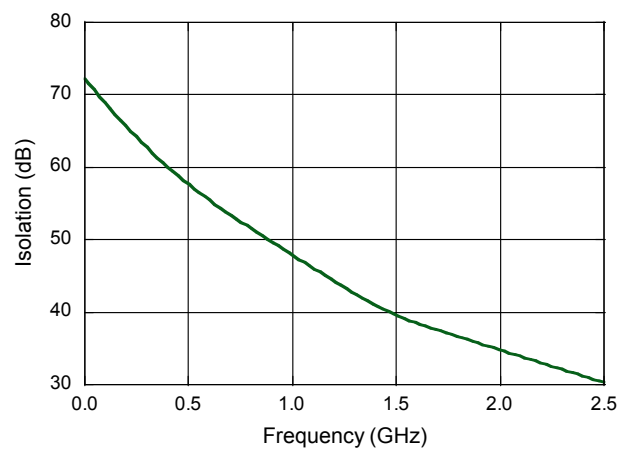


Typical Performance Curves

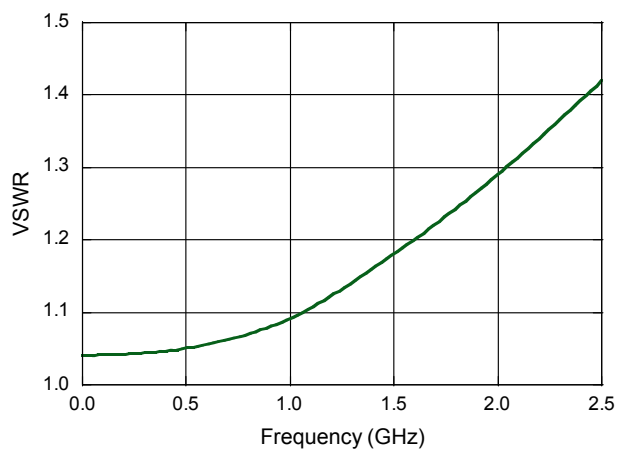
Insertion Loss



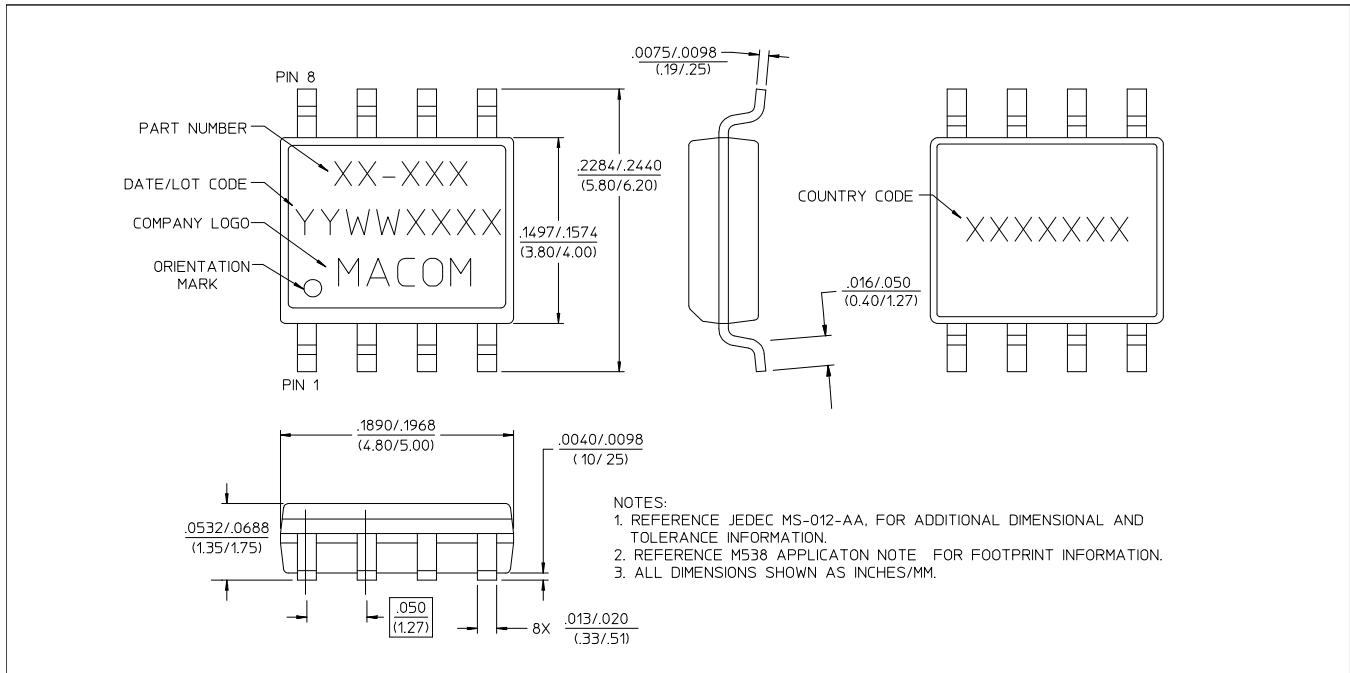
Isolation



VSWR



Lead-Free SOIC-8[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.

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