One Watt High Current Transistors

NPN Silicon

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage MPSW01 MPSW01A	V _{CEO}	30 40	Vdc
Collector – Base Voltage MPSW01 MPSW01A	V _{CBO}	40 50	Vdc
Emitter - Base Voltage	V _{EBO}	5.0	Vdc
Collector Current - Continuous	I _C	1000	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.0 8.0	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	2.5 20	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

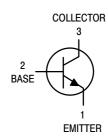
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	50	°C/W

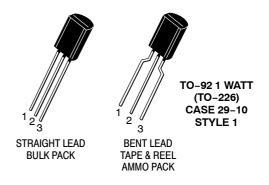
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



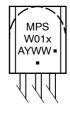
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



x = 01A Devices

A = Assembly Location

/ = Year

WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

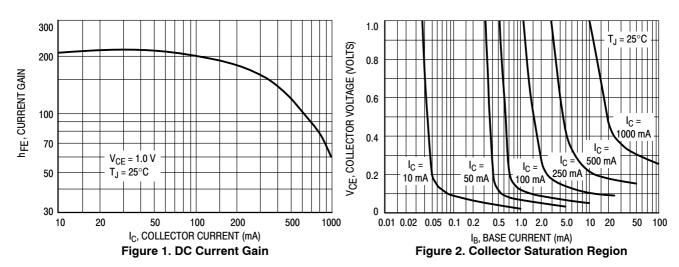
Characteristic			Min	Max	Unit
OFF CHARACTERISTICS				-	
Collector – Emitter Breakdown Voltage (Note 1) ($I_C = 10 \text{ mAdc}, I_B = 0$)	MPSW01 MPSW01A	V _{(BR)CEO}	30 40	_ _	Vdc
Collector – Base Breakdown Voltage ($I_C = 100 \mu Adc$, $I_E = 0$)	MPSW01 MPSW01A	V _(BR) CBO	40 50	- -	Vdc
Emitter – Base Breakdown Voltage ($I_E = 100 \mu Adc, I_C = 0$)		V _{(BR)EBO}	5.0	-	Vdc
Collector Cutoff Current $(V_{CB} = 30 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 40 \text{ Vdc}, I_E = 0)$	MPSW01 MPSW01A	I _{CBO}	- -	0.1 0.1	μAdc
Emitter Cutoff Current (V _{EB} = 3.0 Vdc, I _C = 0)		I _{EBO}	-	0.1	μAdc
ON CHARACTERISTICS (Note 1)					
DC Current Gain		h _{FE}	55 60 50	- - -	_
Collector – Emitter Saturation Voltage ($I_C = 1000 \text{ mAdc}, I_B = 100 \text{ mAdc}$	c)	V _{CE(sat)}	-	0.5	Vdc
Base-Emitter On Voltage (I _C = 1000 mAdc, V _{CE} = 1.0 Vdc)		V _{BE(on)}	ı	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current – Gain — Bandwidth Product (I _C = 50 mAdc, V _{CE} = 10 Vdc, f = 20 MHz)		f _T	50	-	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)		C _{obo}	-	20	pF

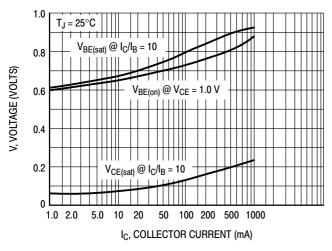
^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

ORDERING INFORMATION

Device	Package	Shipping [†]
MPSW01	TO-92	5000 Units / Bulk
MPSW01G	TO-92 (Pb-Free)	5000 Units / Bulk
MPSW01AG	TO-92 (Pb-Free)	5000 Units / Bulk
MPSW01ARLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSW01ARLRPG	TO-92 (Pb-Free)	2000 / Tape & Ammo Box

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.





-0.8

O_o/_M)
-1.2

-1.6

θV_B FOR V_{BE}
-2.4

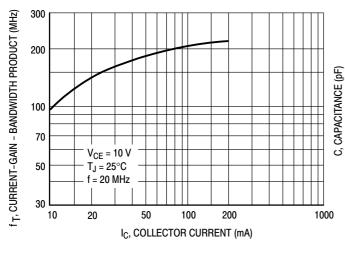
-2.4

1.0 2.0 5.0 10 20 50 100 200 500 1000

I_C, COLLECTOR CURRENT (mA)

Figure 3. "ON" Voltages

Figure 4. Temperature Coefficient



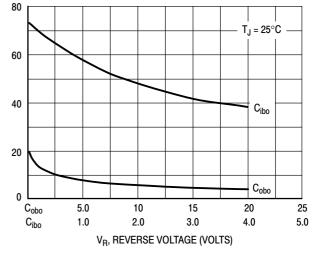


Figure 5. Current Gain — Bandwidth Product

Figure 6. Capacitance

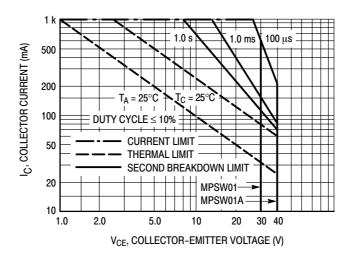
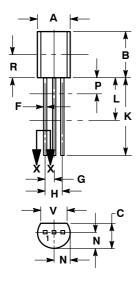


Figure 7. Active Region — Safe Operating Area

PACKAGE DIMENSIONS

TO-92 (TO-226) 1 WATT CASE 29-10 **ISSUE O**



STRAIGHT LEAD **BULK PACK**



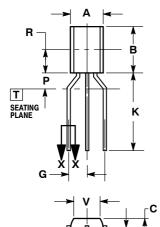
NOTES

- DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1994.
 CONTROLLING DIMENSION: INCHES.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN DIMENSIONS P AND L. DIMENSIONS D AND J APPLY BETWEEN DI-MENSIONS L AND K MINIMUM. THE LEAD DIMENSIONS ARE UNCONTROLLED IN DIMENSION P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.021	0.46	0.53
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
ſ	0.018	0.024	0.46	0.61
K	0.500		12.70	
L	0.250	-	6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.135		3.43	
٧	0.135		3.43	

STYLE 1: PIN 1. EMITTER

BASE COLLECTOR



BENT LEAD TAPE & REEL AMMO PACK



NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: INCHES.
 CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN DIMENSIONS P AND L. DIMENSIONS D AND J APPLY BETWEEN
 DIMENSIONS L AND K MINIMUM. THE LEAD
 DIMENSIONS ARE UNCONTROLLED IN DIMENSION P AND BEYOND DIMENSION K MINIMUM.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.44	5.21	
В	0.290	0.310	7.37	7.87	
С	0.125	0.165	3.18	4.19	
D	0.018	0.021	0.46	0.53	
G	0.094	0.102	2.40	2.80	
J	0.018	0.024	0.46	0.61	
K	0.500		12.70		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.135		3.43		
٧	0.135		3.43		

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice on semiconductor and are registered readerlands of semiconductor Components industries, Ite (SCILLC) as Solitude services are injected in the chargest without further holice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative