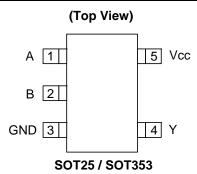


#### Description

The 74AHC1G08 is a single 2-input positive AND gate with a standard push-pull output. The device is designed for operation with a power supply range of 2.0V to 5.5V. The gate performs the positive Boolean function:

$$Y = A \bullet B$$
 or  $Y = \overline{\overline{A} + \overline{B}}$ 

#### **Pin Assignments**



#### **Features**

- Supply Voltage Range from 2.0V to 5.5V
- ± 8 mA Output Drive at 5.0V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time.
- ESD Protection per JESD 22
  - o Exceeds 200-V Machine Model (A115-A)
  - o Exceeds 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 100mA per JESD 78, Class II
- SOT25 and SOT353: Assembled with "Green" Molding Compound (no Br, Sb)
- Lead Free Finish / RoHS Compliant (Note 1)

### **Applications**

- General Purpose Logic
- Wide array of products such as:
  - o PCs, networking, notebooks, netbooks, PDAs
  - o Computer peripherals, hard drives, CD/DVD ROM
  - o TV, DVD, DVR, set top box
  - o Personal Navigation / GPS
  - o MP3 players ,Cameras, Video Recorders

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead\_free.html.



# **Pin Descriptions**

Pin Name	Pin NO.	Description	
Α	1	Data Input	
В	2	Data Input	
GND	3	Ground	
Y	4	Data Output	
Vcc	5	Supply Voltage	

# **Logic Diagram**



# **Function Table**

Inp	Output	
Α	В	Y
Н	Н	Н
L	Х	L
Х	L	L



### **Absolute Maximum Ratings (Note 2)**

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage applied to output in high or low state	-0.5 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input Clamp Current V <sub>I</sub> <0	-20	mA
l <sub>OK</sub>	Output Clamp Current (V <sub>O</sub> < 0 or V <sub>O</sub> > V <sub>CC</sub> )	±20	mA
I <sub>O</sub>	Continuous output current (V <sub>O</sub> = 0 to V <sub>CC</sub> )	±25	mA
I <sub>CC</sub>	Continuous current through V <sub>CC</sub>	50	mA
I <sub>GND</sub>	Continuous current through GND	-50	mA
TJ	Operating Junction Temperature	-40 to 150	°C
T <sub>STG</sub>	Storage Temperature	-65 to 150	°C

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

# **Recommended Operating Conditions (Note 3)**

Symbol	Р	arameter	Min	Max	Unit
V <sub>CC</sub>	Operating Voltage		2	5.5	V
		$V_{CC} = 2V$	1.5		
$V_{IH}$	High-level Input Voltage	$V_{CC} = 3V$	2.1		V
		$V_{CC} = 5.5V$	3.85		
		$V_{CC} = 2V$		0.5	
$V_{IL}$	Low-level input voltage	$V_{CC} = 3V$		0.9	V
		$V_{CC} = 5.5V$		1.65	
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	V <sub>CC</sub>	V
		V <sub>CC</sub> = 2V		-50	uA
I <sub>OH</sub>	High-level output current	$V_{CC} = 3.3V \pm 0.3V$		-4	A
		$V_{CC} = 5V \pm 0.5V$		-8	mA
		$V_{CC} = 2V$		50	uA
I <sub>OL</sub>	Low-level output current	$V_{CC} = 5V \pm 0.5V$		4	A
		$V_{CC} = 3V$		8	mA
A4/A)/	Input transition rise or fall	$V_{CC} = 3.3V \pm 0.3V$		100	A /
Δt/ΔV	rate	$V_{CC} = 5V \pm 0.5V$		20	ns/V
T <sub>A</sub>	Operating free-air temperature		-40	125	°C

Notes: 3. Unused inputs should be held at  $V_{\mbox{CC}}$  or Ground.



### **Electrical Characteristics**

0		T			25°C		-40°C t	o 85°C	-40°C to	o 125ºC	11.24
Symbol	Parameter	Test Conditions	V <sub>CC</sub>	Min	Тур.	Max	Min	Max	Min	Max	Unit
			2V	1.9	2		1.9		1.9		
		$I_{OH} = -50\mu A$	3V	2.9	3		2.9		2.9		
V <sub>OH</sub>	High Level		4.5V	4.4	4.5		4.4		4.4		V
	Output Voltage	$I_{OH} = -4mA$	3V	2.58			2.48		2.40		
		$I_{OH} = -8mA$	4.5V	3.94			3.8		3.70		
			2V			0.1		0.1		0.1	
		$I_{OL} = 50\mu A$	3V			0.1		0.1		0.1	
V <sub>OL</sub>	V <sub>OL</sub> High-level Input Voltage		4.5V			0.1		0.1		0.1	V
		$I_{OL} = 4mA$	3V			0.36		0.44		0.55	
		$I_{OL} = 8mA$	4.5V			0.36		0.44		0.55	
l <sub>l</sub>	Input Current	$V_I = 5.5 \text{ V or GND}$	0 to 5.5V			± 0.1		± 1		± 2	μΑ
I <sub>CC</sub>	Supply Current	$V_I = 5.5V$ or GND $I_O=0$	5.5V			1		10		40	μΑ
Cı	Input Capacitance	$V_I = V_{CC} - \text{or GND}$	5.5V		2.0	10		10		10	pF
Δ	Thermal Resistance	SOT25	(Note 4)		195						°C/W
θ <sub>JA</sub>	Junction-to- Ambient	SOT353	(Note 4)		430						C/VV
Δ	Thermal Resistance	SOT25	(Note 4)		58						°C/W
θ <sub>JC</sub>	Junction-to- Case	SOT353	(Note 4)		155						C/VV

Note: 4. Test conditions for SOT25, and SOT353: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout

# **Switching Characteristics**

#### $V_{CC} = 3.3V \pm 0.3$ (see Figure 1)

Doromotor	From	TO			25ºC		-40°C t	o 85ºC	-40°C to	o 125ºC	Unit
Parameter	(Input)	(OUTPUT)		Min	Тур.	Max	Min	Max	Min	Max	Unit
	Λ or D	.,	C <sub>L</sub> =15pF	0.6	4.6	8.8	0.6	10.5	0.6	12.0	ns
lpd	A or B	Ť	C <sub>L</sub> =50pF	0.6	6.5	12.3	0.6	14.0	0.6	16.0	ns

#### $V_{CC} = 5V \pm 0.5V$ (see Figure 1)

Doromotor	Parameter From TO			25ºC		-40°C t	o 85ºC	-40°C to	125ºC	Unit	
Parameter	(Input)	(OUTPUT)		Min	Тур.	Max	Min	Max	Min	Max	Unit
4	A or B	V	C <sub>L</sub> =15pF	0.6	3.2	5.9	0.6	7.0	0.6	8.0	ns
t <sub>pd</sub>	AUID	Ť	C <sub>L</sub> =50pF	0.6	4.6	7.9	0.6	9.0	0.6	10.5	ns

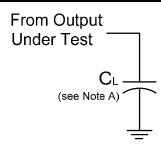


# **Operating Characteristics**

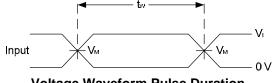
 $T_A = 25 \, {}^{\circ}C$ 

Parameter		Test Conditions	V <sub>CC</sub> = 5V Typ.	Unit
C <sub>pd</sub>	Power dissipation capacitance	f = 1 MHz No Load	12	pF

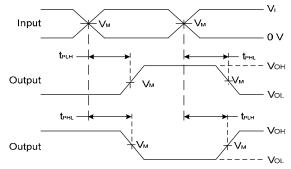
### **Parameter Measurement Information**



V	In	puts	V	6
V <sub>CC</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	CL
3.3V±0.3V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	15pF
5V±0.5V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	15pF
3.3V±0.3V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	50pF
5V±0.5V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	50pF



**Voltage Waveform Pulse Duration** 



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. Inputs are measured separately one transition per measurement.
- D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD.}$



### **Ordering Information**

T4AHC1G 08 XX - 7

Logic Device Function Package Packing

74 : Logic Prefix 08 : 2-Input W5 : SOT25 7 : Tape & Reel

SE: SOT353

AHC: 2 to 5.5V Family

1G : One gate

7" Tape and Reel **Package Packaging Device** Code (Note 5) Quantity **Part Number Suffix** 74AHC1G08W5-7 W5 SOT25 3000/Tape & Reel 74AHC1G08SE-7 -7 SE **SOT353** 3000/Tape & Reel

Notes: 5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

AND -Gate

# **Marking Information**

#### (Top View)

|5| |4| | XX Y W X

3

XX : Identification code Y : Year 0~9

<u>Y</u>: Year 0 9 <u>W</u>: Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents 52 and 53 week

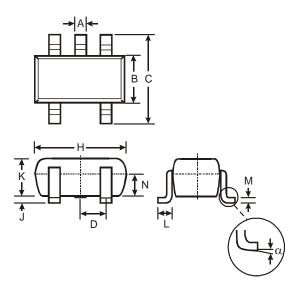
52 and 53 week X : A~Z : Internal code

Part Number	Package	Identification Code
74AHC1G08W5	SOT25	YU
74AHC1G08SE	SOT353	YU



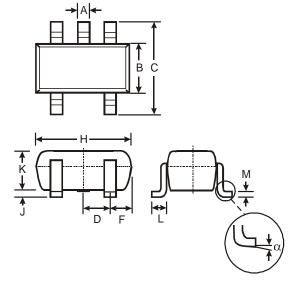
# Package Outline Dimensions (All Dimensions in mm)

### (1) Package Type: SOT25



	SOT	725	
Dim	Min	Max	Тур
Α	0.35	0.50	0.38
В	1.50	1.70	1.60
С	2.70	3.00	2.80
D	_	_	0.95
Н	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	_
All D	imensi	ons in	mm

#### (2) Package Type: SOT353



	SOT353							
Dim	Min	Max						
Α	0.10	0.30						
В	1.15	1.35						
С	2.00	2.20						
D	0.65	Тур						
F	0.40	0.45						
Н	1.80	2.20						
J	0	0.10						
K	0.90	1.00						
L	0.25	0.40						
М	0.10	0.22						
α	0°	8°						
All Di	mensions	in mm						



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