

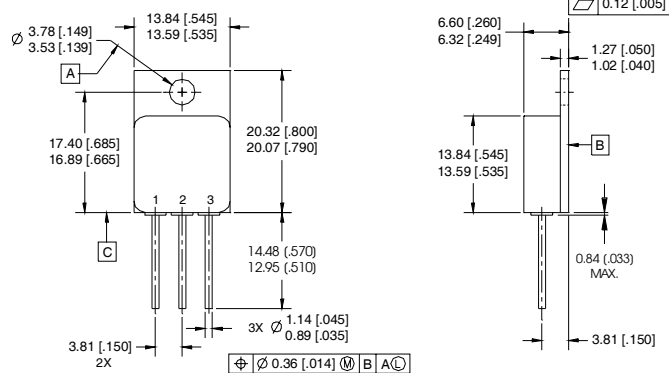
Description/Features

Characteristics	1N7043CCT1	Units
$I_F(AV)$	35	A
V_{RRM} (Per Leg)	100	V
I_{FSM} @ $t_p=8.3ms$ half-sine (Per Leg)	175	A
V_F @ 35Apk, $T_J=125^\circ C$ (Per Leg)	1.2	V
T_J, T_{stg} Operating and storage	-65 to 150	$^\circ C$

The 1N7043CCT1 center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of high reliability environments. It is packaged in the hermetic isolated TO-254AA package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

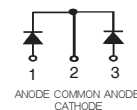
- Hermetically Sealed
- Center Tap
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long Term Reliability
- Lightweight
- ESD Rating: Class 3A per MIL-STD-750, Method 1020

CASE STYLE



NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
3. CONTROLLING DIMENSION: INCH.
4. CONFORMS TO JEDEC OUTLINE TO-254AA.



Case Outline and Dimensions - TO-254AA

Voltage Ratings

Part number	1N7043CCT1
V _R Max. DC Reverse Voltage (V) (Per Leg)	100
V _{RWM} Max. Working Peak Reverse Voltage (V) (Per Leg)	

Absolute Maximum Ratings

Parameters	Limits	Units	Conditions
I _{F(AV)} Max. Average Forward Current See Fig. 5	35	A	50% duty cycle @ T _C = 84°C, square waveform
I _{FSM} Max. Peak One Cycle Non - Repetitive Surge Current (Per Leg)	175	A	@ t _p = 8.3 ms half-sine

Electrical Specifications

Parameters		Limits	Units	Condition	
V _{FM}	Max. Forward Voltage Drop (Per Leg) See Fig. 1 ①	1.84	V	@ 35A	T _J = -55°C
		0.95	V	@ 15A	T _J = 25°C
		1.30	V	@ 35A	
		1.2	V	@ 35A	T _J = 125°C
I _{RM}	Max. Reverse Leakage Current (Per Leg) See Fig. 2 ①	0.5	mA	T _J = 25°C	V _R = rated V _R
		15	mA	T _J = 125°C	
C _T	Max. Junction Capacitance (Per Leg)	600	pF	V _R = 5V _{DC} (1MHz, 25°C)	
L _S	Typical Series Inductance (Per Leg)	6.7	nH	Measured from anode lead to cathode lead 6mm (0.025 in.) from package	

Thermal-Mechanical Specifications

Parameters	Limits	Units	Conditions
T _J Max. Junction Temperature Range	-65 to 150	°C	
T _{stg} Max. Storage Temperature Range	-65 to 150	°C	
R _{thJC} Max. Thermal Resistance, Junction to Case (Per Leg)	2.30	°C/W	DC operation See Fig. 4
R _{thJC} Max. Thermal Resistance, Junction to Case (Per Package)	1.15	°C/W	DC operation
wt Weight (Typical)	9.3	g	
Die Size (Typical)	125X125	mils	
Case Style	TO-254AA		

① Pulse Width < 300μs, Duty Cycle < 2%

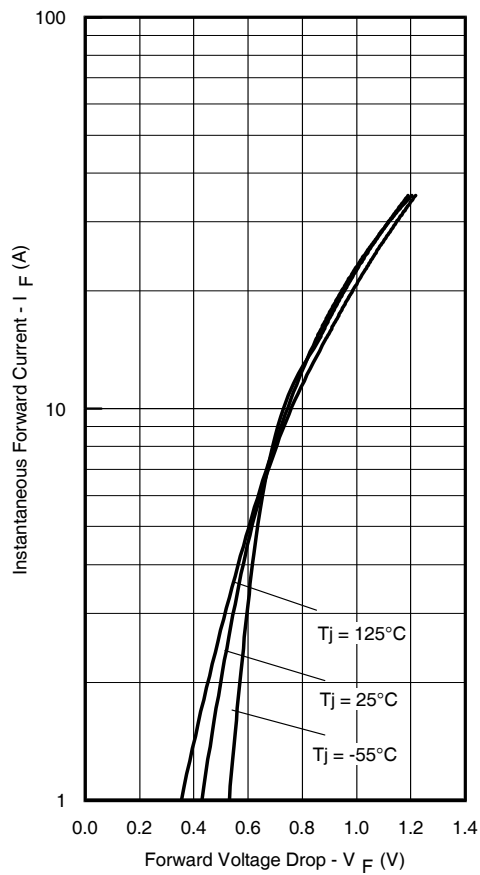


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

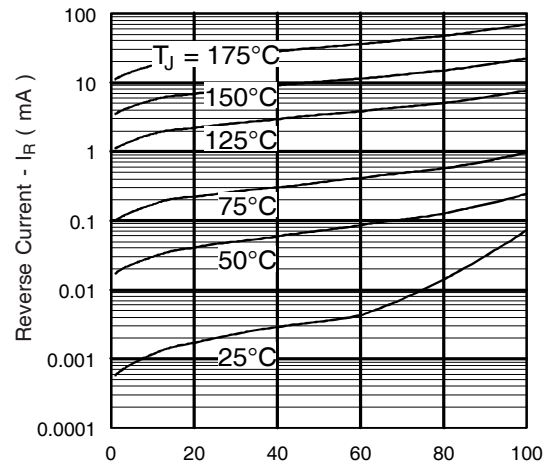


Fig. 2 - Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

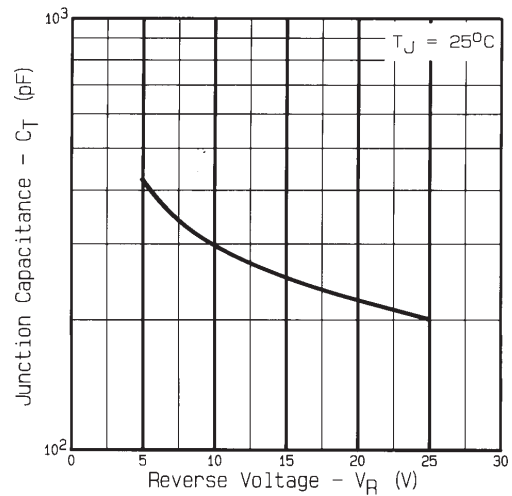
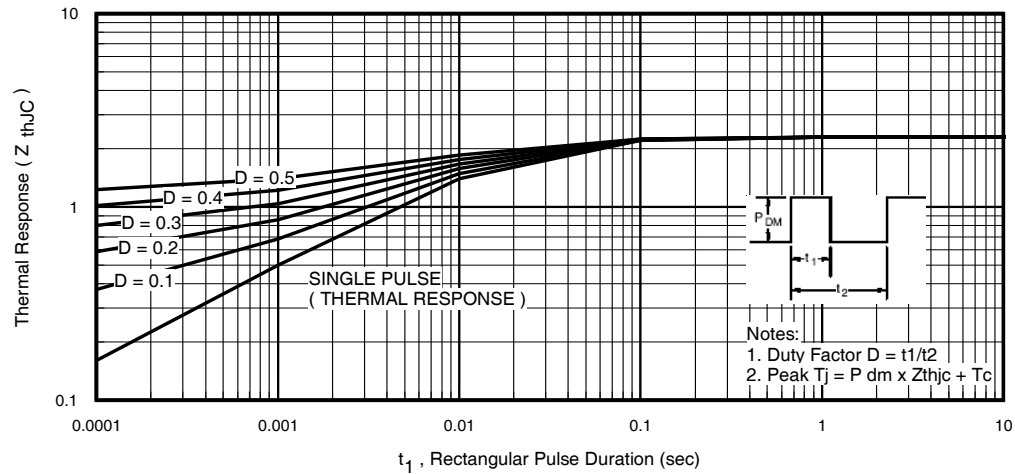
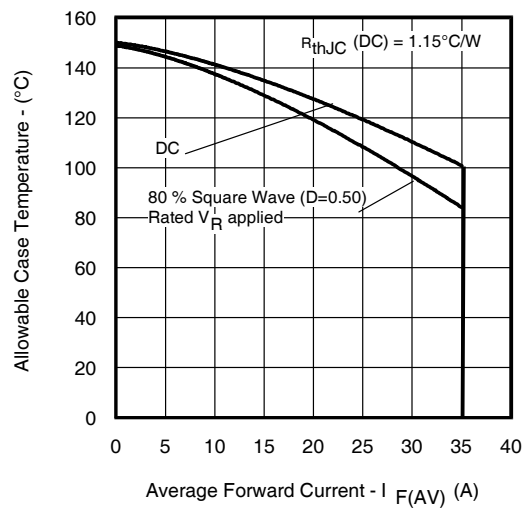


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)Fig. 5 - Max. Allowable Case Temperature Vs.
Average Forward Current (Per Package)