TOSHIBA Photocoupler Photo Relay

TLP227GA, TLP227GA-2

Modem

Telecommunications

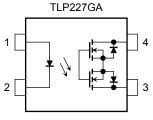
PBXs

The Toshiba TLP227GA series consist of a gallium arsenide infrared-emitting diode optically coupled to a photo-MOSFET in a 4-pin DIP or a 8-pin DIP package, and has a peak off-State voltage of 400 V.

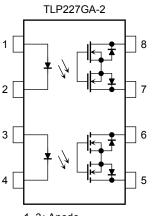
• Normally off function

TLP227GA : DIP4 (1 form A)
 TLP227GA-2 : DIP8 (2 form A)
 Peak off-state voltage : 400 V (min)
 Trigger LED current : 3 mA (max)
 On-state current : 120 mA (max)
 On-state resistance : 35Ω (max)
 Isolation voltage : 2500 Vrms (min)

Pin Configuration (top view)



- 1: Anode
- 2: Cathode
- 3: Drain
- 4: Drain



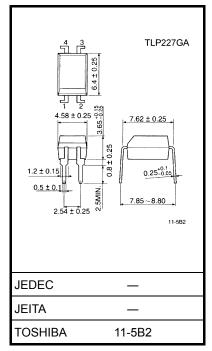
1, 3: Anode 2, 4: Cathode

5 : Drain D1

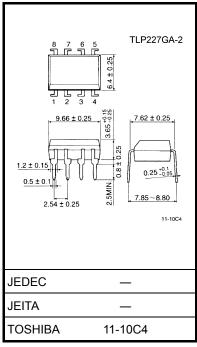
6 : Drain D2 7 : Drain D3

8 : Drain D4

Unit: mm



Weight: 0.26 g (typ.)



Weight: 0.54 g (typ.)



Absolute Maximum Ratings (Ta = 25°C)

	C	Characteristic		Symbol	Rating	Unit
	Forward curr	ent		Ιϝ	50	mA
	Forward curr	ent derating (Ta ≧ 25°C)	ΔI _F /°C	-0.5	mA/°C
Led	Peak forward (100 μs pulse			I _{FP}	1	А
	Reverse volt	age		V _R	5	V
	Junction tem	perature		Tj	125	°C
	Off-state out	put terminal vo	Itage	V _{OFF}	400	٧
		TLP227GA				
	On-state current	TLP227GA-2	One channel	I _{ON}	120	Ма
Detector		1L1 227 GA-2	Both channel			
Dete		TLP227GA				mA/°C
	On-state current rating (Ta ≧ 25°C)	TLP227GA-2	One channel	Δl _{ON} /°C	-1.2	
		Both channel				
	Junction tem	perature		Tj	125	°C
Storage temperature range				T _{stg}	T _{stg} –55~125	
Ор	Operating temperature range			T _{opr}	−40~85	°C
Lea	ad soldering te	emperature (10	s)	T _{sol}	260	°C
Iso	lation voltage	(AC, 1 min., R.	H. ≦ 60%) (Note 1)	BVS	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: LED pins are shorted together. Detector pins are also shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V_{DD}			320	V
Forward current	IF	5	7.5	25	mA
On-state current	I _{ON}	_	_	100	mA
Operating temperature	T _{opr}	-20	_	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V_{F}	I _F = 10 mA	1.0	1.15	1.3	V
Led	Reverse current	I _R	V _R = 5 V	_	_	10	μА
	Capacitance	C _T	V = 0, $f = 1$ MHz		30		pF
Detector	Off-state current	l _{OFF}	V _{OFF} = 400 V		_	1	μА
	Capacitance	C _{OFF}	V = 0, f = 1 MHz	_	_	_	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	I _{ON} = 120 mA	_	1	3	mA
On-state resistance	R _{ON}	$I_{ON} = 120 \text{ mA}, I_F = 5 \text{ mA}$		18	35	Ω

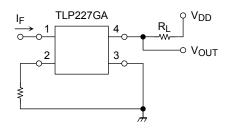
Isolation Characteristics (Ta = 25°C)

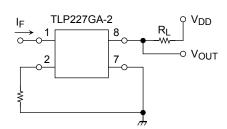
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	CS	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5 × 10 ¹⁰	10 ¹⁴	_	Ω
	BVS	AC, 1 min	2500	_	_	Vrms
Isolation voltage		AC, 1 s (in oil)	_	5000	_	VIIIIS
		DC, 1 min (in oil)	_	5000	_	Vdc

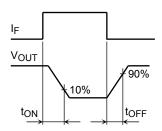
Switching Characteristics (Ta = 25°C)

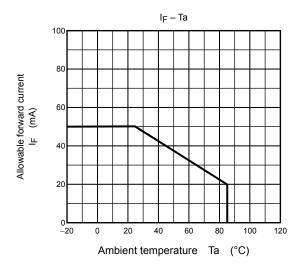
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	t _{ON}	$R_L = 200 \Omega$	_	_	1	ms
Turn-off time	toff	$V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$ (Note 2)	_	_	1	1113

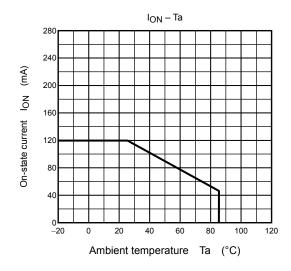
Note 2: Switching time test circuit

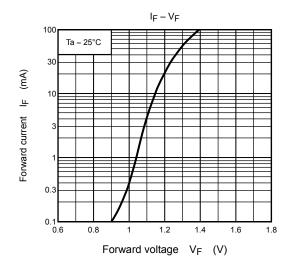


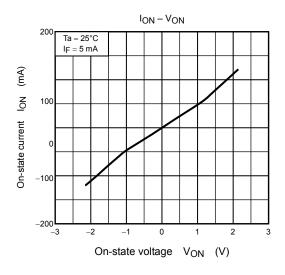


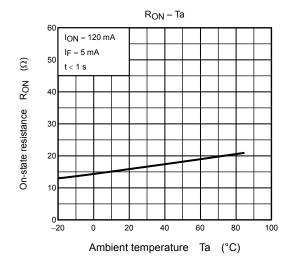


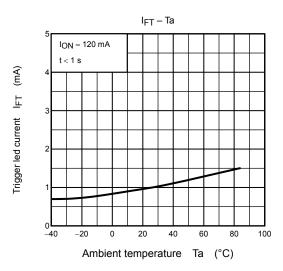


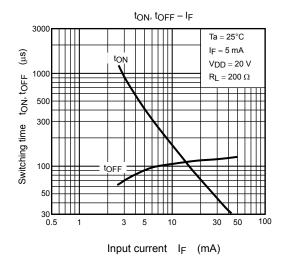


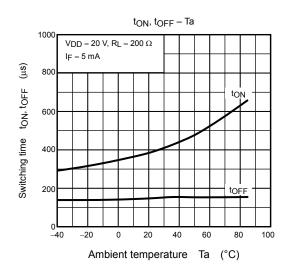


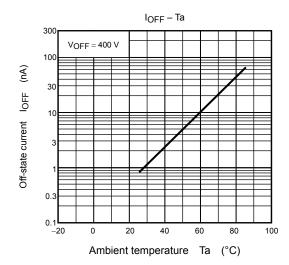














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