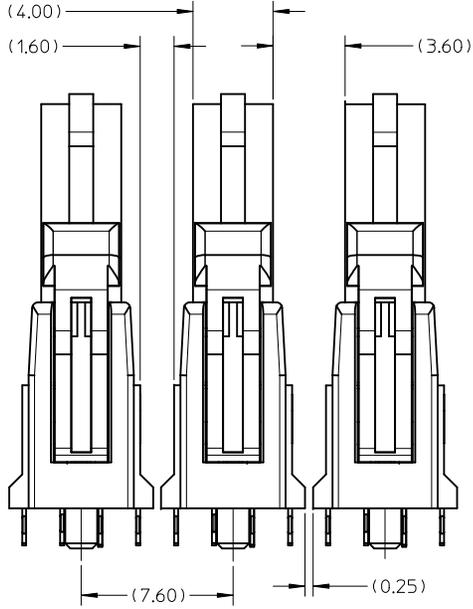
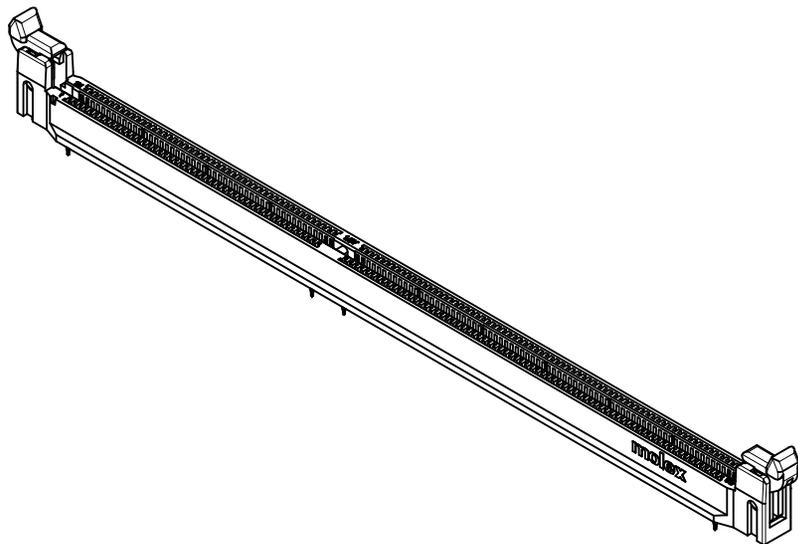
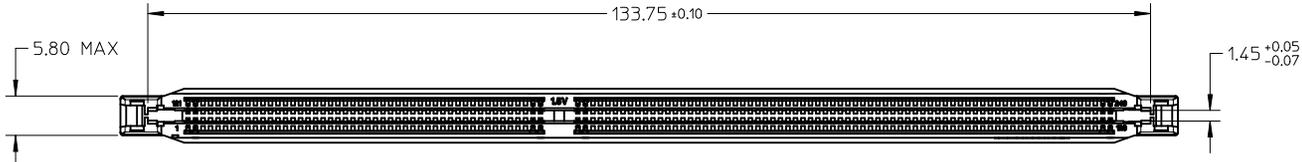


- NOTES:
- MATERIAL:
HOUSING - POLYAMIDE (PA) GLASS FILLED, UL 94V-0, COLOR - BLACK
LATCH - POLYAMIDE (PA) GLASS FILLED, UL 94V-0, COLOR - BLACK
TERMINAL - COPPER ALLOY
 - FINISHES: TERMINAL PLATING SEE TABLE ON SHEET 5
 - PRODUCT SPECIFICATION: PS-78315-001
 - PACKAGING SPECIFICATION: PRODUCT SHALL BE PACKED IN TRAYS
 - CARD SLOT ACCEPTS 1.27 +/- 0.10MM MODULE THICKNESS (MEASURED FROM PC PADS)

- 6 MOLEX LOGO, DATE & PART NUMBERS INDICATED ON HOUSING
- 7 MODULE SEATING PLANE FROM TOP OF PCB
- 8 KEEP OUT ZONE RESERVED FOR LATCH

ADDED OPTION EC NO: S2011-0814 DRW: CTEH CHKD: CGIAN APPR: SHLENI	2011/02/08 2011/04/04 2011/04/04	DESCRIPTION REV	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION										
			$F_A=0$ $F_C=0$ $F_B=0$	<table border="1"> <tr><th>mm</th><th>INCH</th></tr> <tr><td>4 PLACES ± ---</td><td>± ---</td></tr> <tr><td>3 PLACES ± ---</td><td>± ---</td></tr> <tr><td>2 PLACES ± 0.20</td><td>± ---</td></tr> <tr><td>1 PLACE ± ---</td><td>± ---</td></tr> </table>	mm	INCH	4 PLACES ± ---	± ---	3 PLACES ± ---	± ---	2 PLACES ± 0.20	± ---	1 PLACE ± ---	± ---	MM ONLY	NTS	METRIC	
			mm	INCH														
			4 PLACES ± ---	± ---														
3 PLACES ± ---	± ---																	
2 PLACES ± 0.20	± ---																	
1 PLACE ± ---	± ---																	
	ANGULAR ± 1 °	DRAWN BY CMTEO	DATE 2008/07/17	TITLE DDR3 DIMM (LSP, VLP) 100MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR														
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	CHECKED BY CCTEH	DATE 2008/07/30	APPROVED BY SHLENI	DATE 2010/12/15	MOLEX MOLEX INCORPORATED	DOCUMENT NO. SD-78315-001	SHEET NO. 1 OF 6										

10 9 8 7 6 5 4 3 2 1



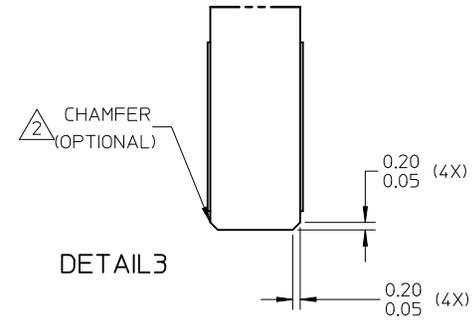
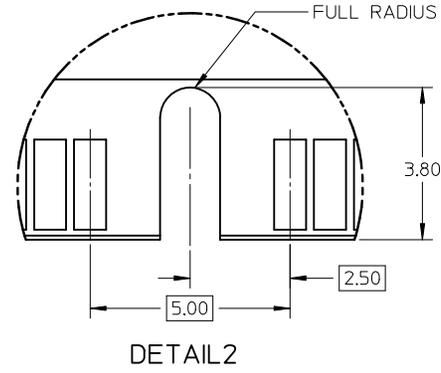
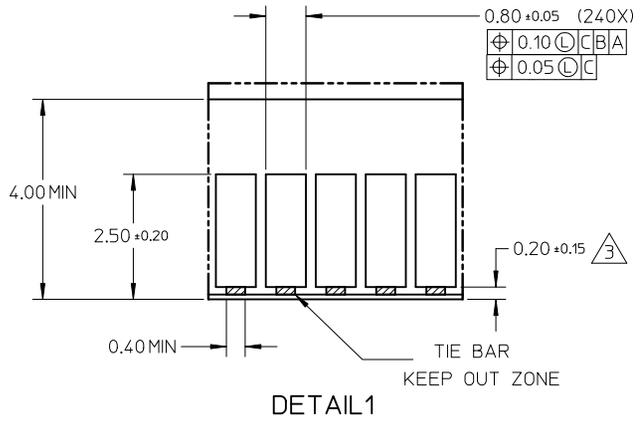
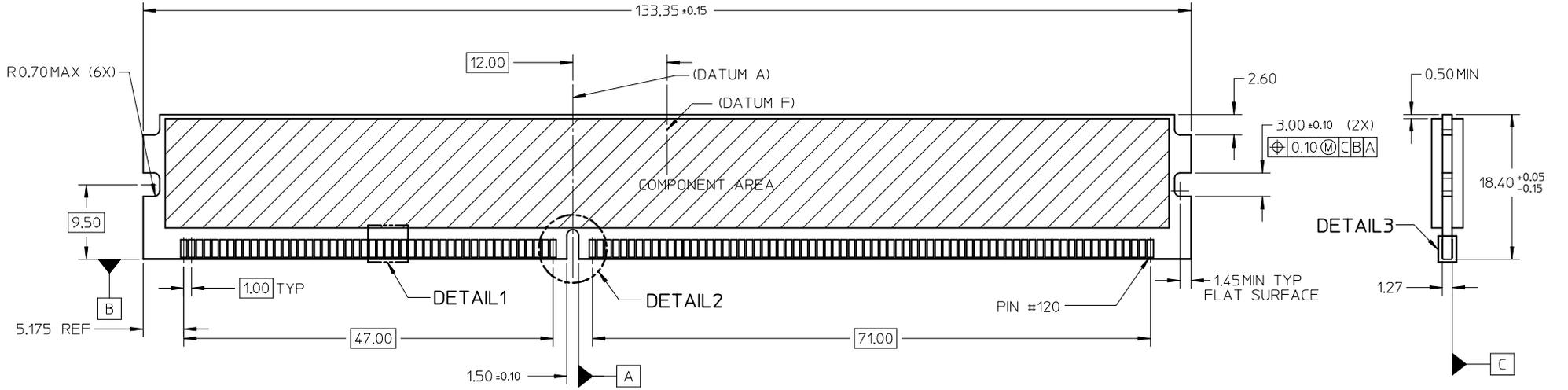
MINIMUM ROW TO ROW SPACING RECOMMENDATION
(USING 4.00MM MODULE CARD)

ADDED OPTION EC NO: S2011-0814 DRWN: CCTEH CH'KD: GGTAN APPR: SHLENI	2011/02/08 2011/04/04 2011/04/04	QUALITY SYMBOLS $F_{\Delta} = 0$ $F_{\square} = 0$ $F_{\nabla} = 0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
				mm	INCH	DRAWN BY CMTEO	DATE 2008/07/17	TITLE DDR3 DIMM (LSP, VLP) 1.00MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR			
			4 PLACES	± ---	± ---	CHECKED BY CCTEH	DATE 2008/07/30	MOLEX INCORPORATED			
			3 PLACES	± ---	± ---	APPROVED BY SHLENI	DATE 2010/12/15	MATERIAL NO.	DOCUMENT NO.	SHEET NO.	
2 PLACES	± 0.20	± ---	ANGULAR ± 1 °		SEE TABLE		SD-78315-001		2 OF 6		
1 PLACE	± ---	± ---	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						
A1	REV	DESCRIPTION									

9 8 7 6 5 4 3 2 1

10 9 8 7 6 5 4 3 2 1

009MODULE CARD
 DDR3 DUAL INLINE MEMORY MODULE FAMILY 1.00 CONTACT CENTERS
 (JEDEC MO-269, ISSUE A, 12/05)



NOTES:

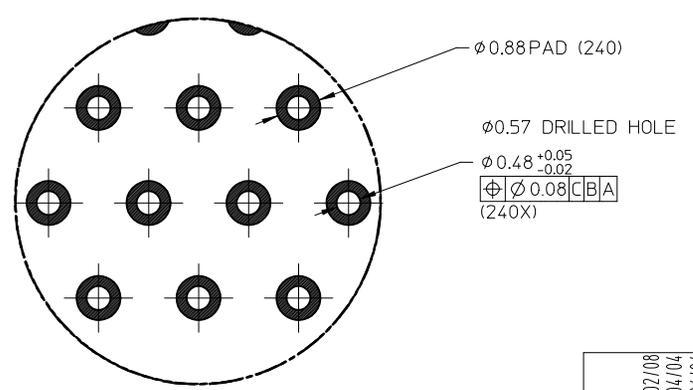
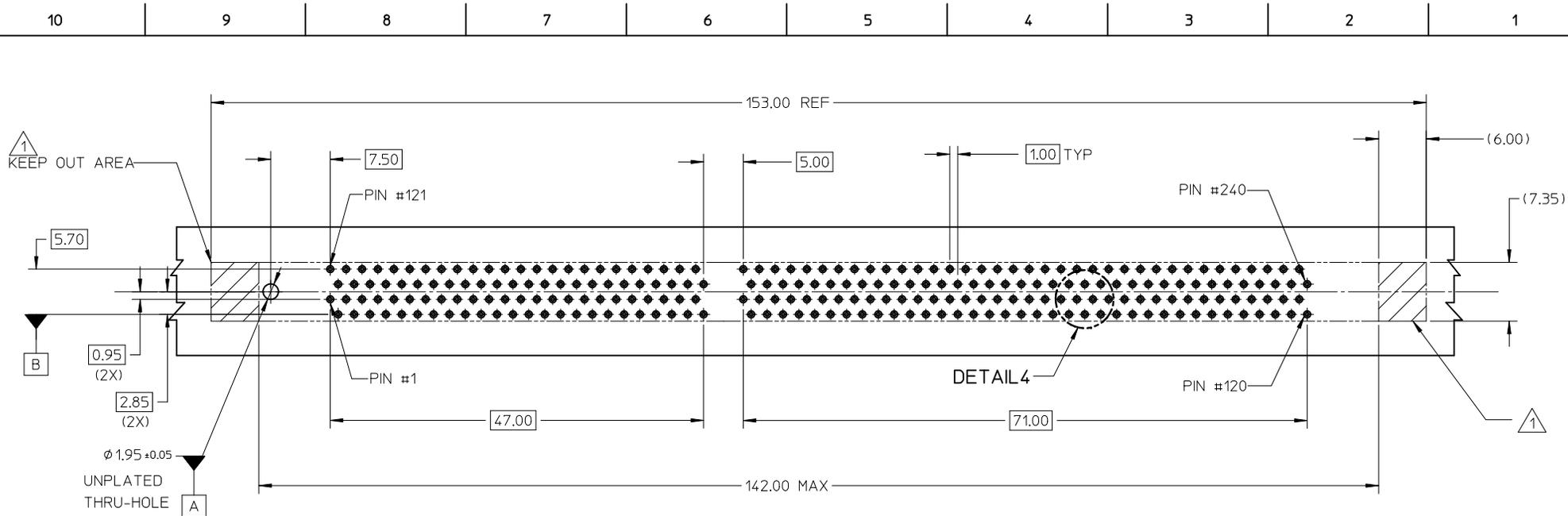
1. RECOMMENDED PLATING FOR CONTACT PADS:
 PREFERRED: 0.76 MICROMETERS GOLD PLATING MIN OVER 2.00 MICROMETERS NICKEL
 ALTERNATIVE: 0.05-0.75 MICROMETERS GOLD PLATING OVER 2.00 MICROMETERS NICKEL MUST USE AN ELECTRONIC CONTACT GRADE CORROSIVE BARRIER LUBRICANT

2. CHAMFER IS OPTIONAL AND IS NOT TO HIT THE GOLD PADS

3. LEADING EDGE OF GOLD PADS SPECIFIED BY KEEPOUT ZONE SHALL BE FREE OF BURRS AND EXTERNAL TIE BARS

ADDED OPTION EC NO: S2011-0814 DRW: CCTEH 2011/02/08 CHKD: CGTAN 2011/04/04 APPR: SHLENI 2011/04/04	QUALITY SYMBOLS $F_A=0$ $F_G=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY CMTEO	DATE 2008/07/17	TITLE DDR3 DIMM (LSP, VLP) 1.00MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR			
		4 PLACES	± ---	± ---	CHECKED BY CCTEH	DATE 2008/07/30	MOLEX INCORPORATED			
		3 PLACES	± ---	± ---	APPROVED BY SHLENI	DATE 2010/12/15	DOCUMENT NO. SD-78315-001			
			2 PLACES	± 0.20	± ---	SHEET NO. 3 OF 6				
			1 PLACE	± ---	± ---	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				
			ANGULAR ± 1 °							
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS							
A1	REV		SIZE A3							

9 8 7 6 5 4 3 2 1

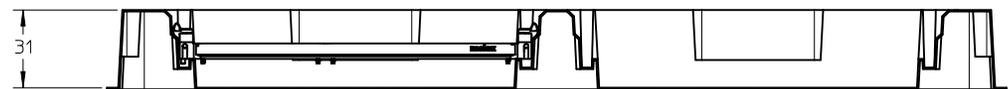
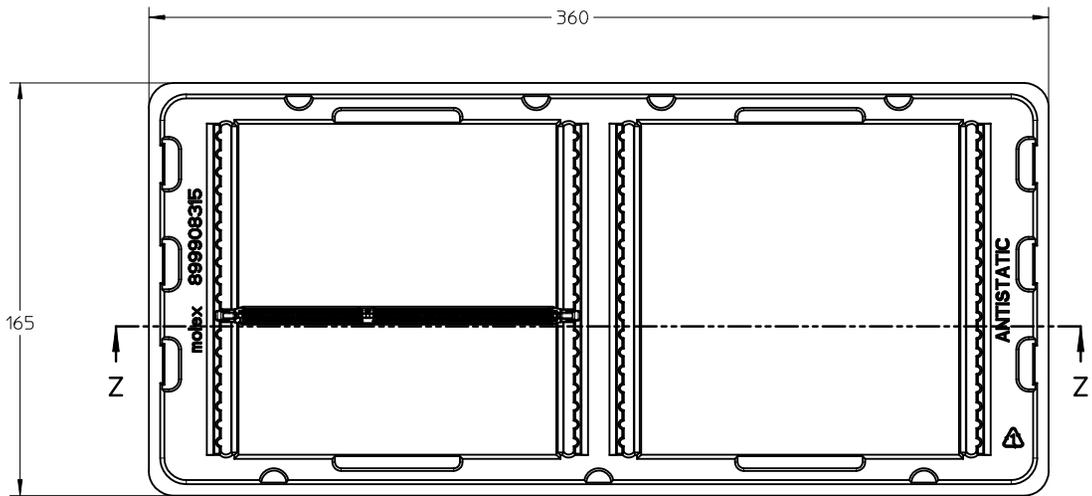


NOTES:
 1. KEEP OUT AREA IS THE AREA WHERE THE CONNECTOR (INCLUDING LATCH) IS MOUNTED ON THE PCB

ADDED OPTION EC NO: S2011-0814 DRWN: CCTEH 2011/02/08 CHKD: CGTAN 2011/04/04 APPR: SILENI 2011/04/04	QUALITY SYMBOLS $F_A=0$ $F_G=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY	SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
		4 PLACES ± --- ± ---	DRAWN BY CMTEO	DATE 2008/07/17	TITLE DDR3 DIMM (LSP, VLP) 1.00MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR			
		3 PLACES ± --- ± ---	CHECKED BY CCTEH	DATE 2008/07/30	molex MOLEX INCORPORATED			
		2 PLACES ± 0.20 ± --- 1 PLACE ± --- ± ---	APPROVED BY SILENI	DATE 2010/12/15	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-78315-001	SHEET NO. 4 OF 6	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	SIZE A3	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						

10 9 8 7 6 5 4 3 2 1

VIEW FOR PACKAGING TRAY



SECTION Z-Z

NOTE:
1. NO. OF CAVITY - 18 X 2 =36

ADDED OPTION EC NO: S2011-0814 DRWN: CCTEH 2011/02/08 CHKD: CGTAN 2011/04/04 APPR: SHLENI 2011/04/04 REV A1	QUALITY SYMBOLS $\nabla_{\sqrt{A}}=0$ $\nabla_{\sqrt{C}}=0$ $\nabla_{\sqrt{F}}=0$	GENERAL TOLERANCES (UNLESS SPECIFIED) <table border="1"> <thead> <tr> <th></th> <th>mm</th> <th>INCH</th> </tr> </thead> <tbody> <tr> <td>4 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>3 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>2 PLACES</td> <td>± 0.20</td> <td>± ---</td> </tr> <tr> <td>1 PLACE</td> <td>± ---</td> <td>± ---</td> </tr> </tbody> </table>		mm	INCH	4 PLACES	± ---	± ---	3 PLACES	± ---	± ---	2 PLACES	± 0.20	± ---	1 PLACE	± ---	± ---	DIMENSION STYLE MM ONLY	SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION
		mm	INCH																		
	4 PLACES	± ---	± ---																		
	3 PLACES	± ---	± ---																		
2 PLACES	± 0.20	± ---																			
1 PLACE	± ---	± ---																			
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	ANGULAR ± 1 °	DRAWN BY CMTEO	DATE 2008/07/17	TITLE DDR3 DIMM (LSP, VLP) 1.00MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR	MOLEX INCORPORATED	SHEET NO. 5 OF 6															
SEE TABLE	MATERIAL NO. SHLENI	DATE 2010/12/15	APPROVED BY SHLENI	DOCUMENT NO. SD-78315-001	MOLEX INCORPORATED	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION															
SIZE A3	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION																				

9 8 7 6 5 4 3 2 1

PART NO.	KEY POS.	DIM 'P'	RECOMMENDED PCB THICKNESS	PLATING OPTION		LATCH COLOUR	HOUSING COLOUR
				CONTACT AREA	TAIL AREA		
78315-0001	CENTER (1.5V)	1.85	2.40	0.76 MICROMETER/ 30 MICROINCH MIN. GOLD OVER 1.27 MICROMETER/ 50 MICROINCH MIN. NICKEL UNDERPLATE	0.38 TO 1.52 MICROMETER/15 TO 60 MICROINCH MIN TIN (MATT) OVER 1.27 MICROMETER/ 50 MICROINCH MIN. NICKEL UNDERPLATE	BLACK	BLACK
78315-0051						BLUE	BLUE
78315-0201				BLACK	BLACK		
78315-0011		2.85	2.60	0.76 MICROMETER/ 30 MICROINCH MIN. GOLD OVER 1.27 MICROMETER/ 50 MICROINCH MIN. NICKEL UNDERPLATE	0.38 TO 1.52 MICROMETER/15 TO 60 MICROINCH MIN TIN (MATT) OVER 1.27 MICROMETER/ 50 MICROINCH MIN. NICKEL UNDERPLATE	BLACK	BLACK

ADDED OPTION EC NO: S2011-0814 DRWN: CCTEH 2011/02/08 CHKD: CGTAN 2011/04/04 APPR: SHLENI 2011/04/04	DESCRIPTION REV	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION	
		$\nabla = 0$ $\nabla = 0$ $\nabla = 0$	mm INCH 4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.20 ± --- 1 PLACE ± --- ± --- ANGULAR ± 1 °	MM ONLY	NTS	METRIC	DRAWN BY: CMTEO DATE: 2008/07/17 CHECKED BY: CCTEH DATE: 2008/07/30 APPROVED BY: SHLENI DATE: 2010/12/15	TITLE: DDR3 DIMM (LSP, VLP) 1.00MM PITCH, 240 CKTS VERTICAL P/F, LOWLLCR
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	SEE TABLE	MATERIAL NO. DOCUMENT NO.	SHEET NO.			
		SIZE: A3	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					