

## Important notice

Dear Customer,

On 7 February 2017 the former NXP Standard Product business became a new company with the tradename **Nexperia**. Nexperia is an industry leading supplier of Discrete, Logic and PowerMOS semiconductors with its focus on the automotive, industrial, computing, consumer and wearable application markets

In data sheets and application notes which still contain NXP or Philips Semiconductors references, use the references to Nexperia, as shown below.

Instead of <http://www.nxp.com>, <http://www.philips.com/> or <http://www.semiconductors.philips.com/>, use **<http://www.nexperia.com>**

Instead of [sales.addresses@www.nxp.com](mailto:sales.addresses@www.nxp.com) or [sales.addresses@www.semiconductors.philips.com](mailto:sales.addresses@www.semiconductors.philips.com), use **[salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com)** (email)

Replace the copyright notice at the bottom of each page or elsewhere in the document, depending on the version, as shown below:

- © NXP N.V. (year). All rights reserved or © Koninklijke Philips Electronics N.V. (year). All rights reserved

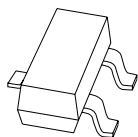
Should be replaced with:

- © **Nexperia B.V. (year). All rights reserved.**

If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **[salesaddresses@nexperia.com](mailto:salesaddresses@nexperia.com)**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia



# PMBD7000

## Double high-speed switching diode

Rev. 4 — 16 September 2010

Product data sheet

## 1. Product profile

### 1.1 General description

The PMBD7000 consists of two high-speed switching diodes connected in series, fabricated in planar technology, and encapsulated in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

### 1.2 Features and benefits

- High switching speed:  $t_{rr} \leq 4$  ns
- Repetitive peak forward current:  $I_{FRM} \leq 450$  mA
- Small SMD plastic package
- Reverse voltage:  $V_R \leq 100$  V
- Repetitive peak reverse voltage:  $V_{RRM} \leq 100$  V
- AEC-Q101 qualified

### 1.3 Applications

- High-speed switching
- General-purpose switching

### 1.4 Quick reference data

Table 1. Quick reference data

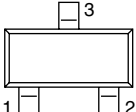
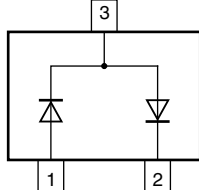
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Per diode</b>						
$I_R$	reverse current	$V_R = 100$ V	-	-	0.5	$\mu$ A
$V_R$	reverse voltage		-	-	100	V
$t_{rr}$	reverse recovery time		[1]	-	4	ns

[1] When switched from  $I_F = 10$  mA to  $I_R = 10$  mA;  $R_L = 100$   $\Omega$ ; measured at  $I_R = 1$  mA.



## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	anode (diode 1)		
2	cathode (diode 2)		
3	cathode (diode 1), anode (diode 2)		

006aaa763

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMBD7000	-	plastic surface-mounted package; 3 leads	SOT23

## 4. Marking

Table 4. Marking codes

Type number	Marking code <sup>[1]</sup>
PMBD7000	*5C

- [1] \* = -: made in Hong Kong  
 \* = p: made in Hong Kong  
 \* = t: made in Malaysia  
 \* = W: made in China

## 5. Limiting values

**Table 5. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

Symbol	Parameter	Conditions	Min	Max	Unit
<b>Per diode</b>					
$V_{RRM}$	repetitive peak reverse voltage		-	100	V
$V_R$	reverse voltage		-	100	V
$I_F$	forward current		[1] -	215	mA
			[2] -	125	mA
$I_{FRM}$	repetitive peak forward current		-	450	mA
$I_{FSM}$	non-repetitive peak forward current	square wave	[3]		
		$t_p = 1 \mu s$	-	4	A
		$t_p = 1 ms$	-	1	A
		$t_p = 1 s$	-	0.5	A
$P_{tot}$	total power dissipation	$T_{amb} \leq 25^\circ C$	[1][4] -	250	mW
<b>Per device</b>					
$T_j$	junction temperature		-	150	$^\circ C$
$T_{amb}$	ambient temperature		-55	+150	$^\circ C$
$T_{stg}$	storage temperature		-65	+150	$^\circ C$

[1] Single diode loaded.

[2] Double diode loaded.

[3]  $T_j = 25^\circ C$  prior to surge.

[4] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 6. Thermal characteristics

**Table 6. Thermal characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1][2] -	-	500	K/W
$R_{th(j-t)}$	thermal resistance from junction to tie-point		-	-	360	K/W

[1] Single diode loaded.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

7. Characteristics

Table 7. Characteristics  
*T<sub>j</sub> = 25 °C unless otherwise specified.*

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per diode						
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 mA	-	550	700	mV
		I <sub>F</sub> = 10 mA	-	670	820	mV
		I <sub>F</sub> = 50 mA	-	-	1	V
		I <sub>F</sub> = 100 mA	-	0.75	1.1	V
		I <sub>F</sub> = 150 mA	-	-	1.25	V
I <sub>R</sub>	reverse current	V <sub>R</sub> = 50 V	-	-	300	nA
		V <sub>R</sub> = 100 V	-	-	500	nA
		V <sub>R</sub> = 50 V; T <sub>j</sub> = 150 °C	-	-	100	μA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V	-	-	1.5	pF
t <sub>rr</sub>	reverse recovery time	[1]	-	-	4	ns
V <sub>FR</sub>	forward recovery voltage	[2]	-	-	1.75	V

[1] When switched from I<sub>F</sub> = 10 mA to I<sub>R</sub> = 10 mA; R<sub>L</sub> = 100 Ω; measured at I<sub>R</sub> = 1 mA.  
[2] When switched from I<sub>F</sub> = 10 mA; t<sub>r</sub> = 20 ns.

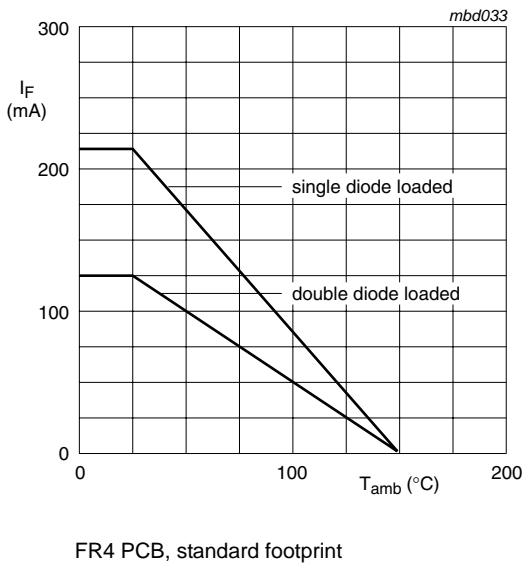


Fig 1. Forward current as a function of ambient temperature; derating curve

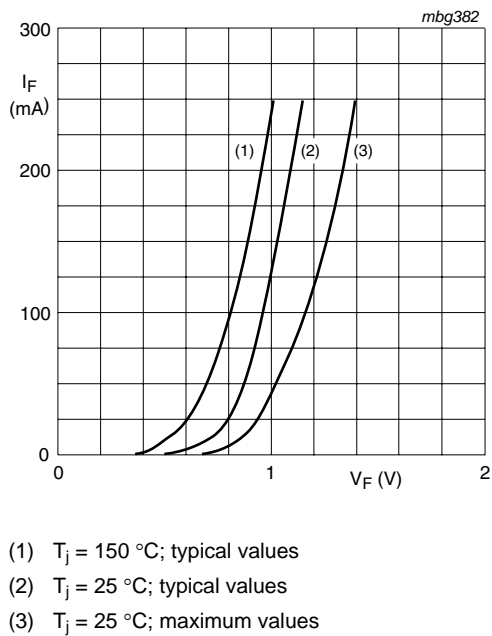
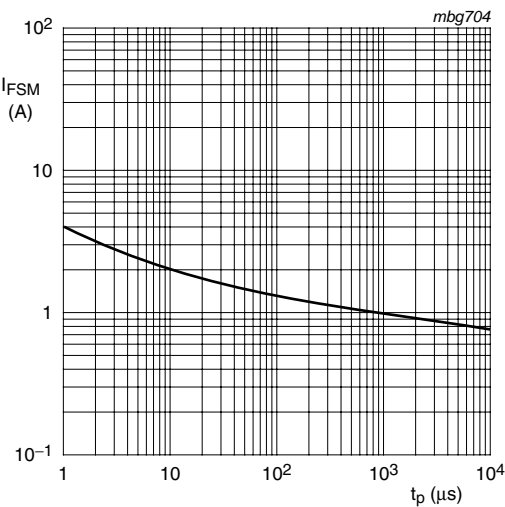
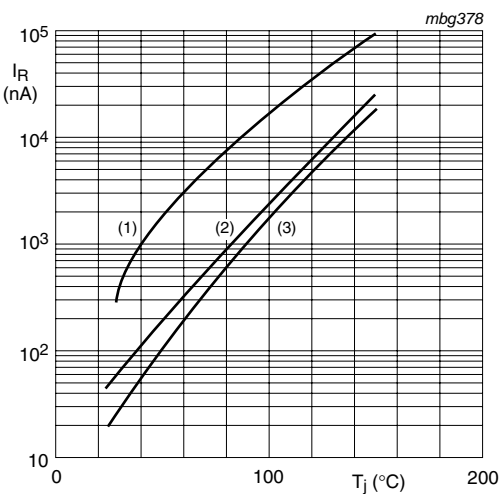


Fig 2. Forward current as a function of forward voltage



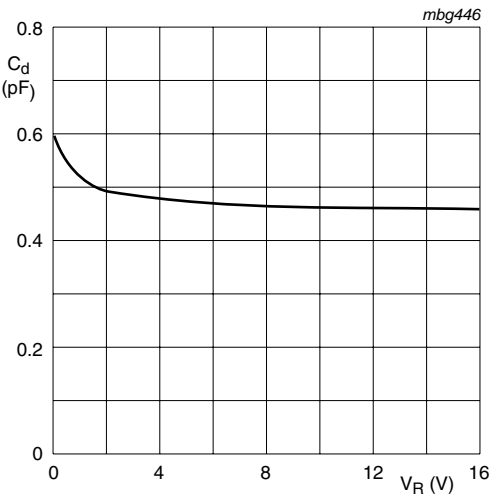
Based on square wave currents.  
 $T_j = 25\text{ }^{\circ}\text{C}$ ; prior to surge

Fig 3. Non-repetitive peak forward current as a function of pulse duration; maximum values



- (1)  $V_R = 50\text{ V}$ ; maximum values
- (2)  $V_R = 30\text{ V}$ ; typical values
- (3)  $V_R = 50\text{ V}$ ; typical values

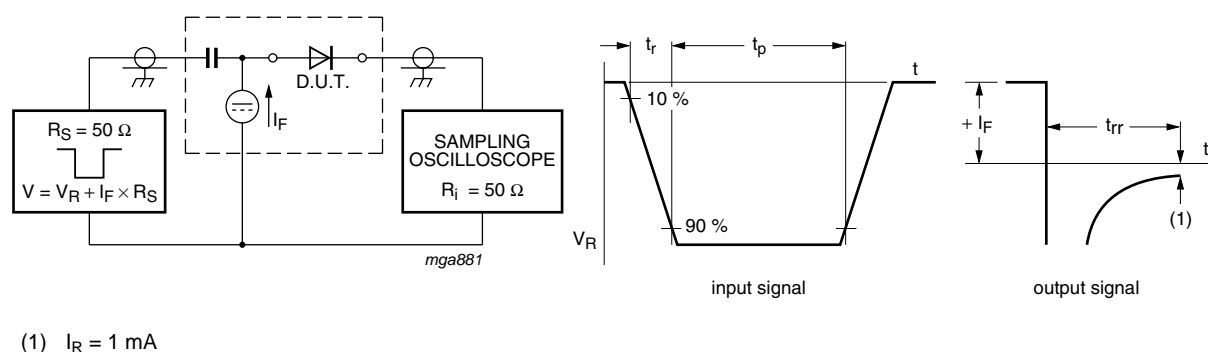
Fig 4. Reverse current as a function of junction temperature



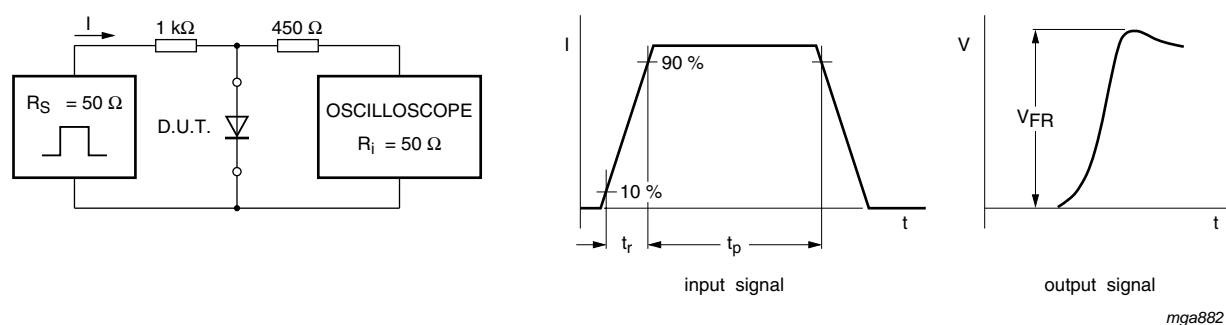
$f = 1\text{ MHz}$ ;  $T_{amb} = 25\text{ }^{\circ}\text{C}$

Fig 5. Diode capacitance as a function of reverse voltage; typical values

## 8. Test information



**Fig 6. Reverse recovery time test circuit and waveforms**

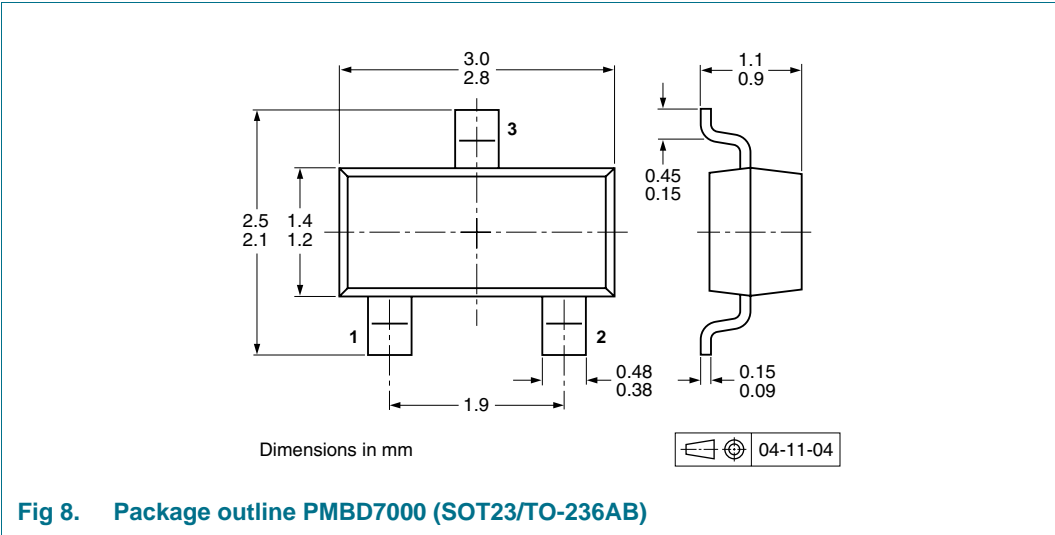


**Fig 7. Forward recovery voltage test circuit and waveforms**

### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



10. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.<sup>[1]</sup>

Type number	Package	Description	Packing quantity	
			3000	10000
PMBD7000	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235

[1] For further information and the availability of packing methods, see [Section 14](#).





## 12. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMBD7000 v.4	20100916	Product data sheet	-	PMBD7000_3
Modifications:	<ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li><li>• <a href="#">Table 4 "Marking codes"</a>: updated</li><li>• <a href="#">Table 7 "Characteristics"</a>: corrected <math>V_F</math> unit for condition <math>I_F = 150</math> mA</li><li>• <a href="#">Figure 2</a>: updated</li><li>• <a href="#">Section 8 "Test information"</a>: figure title of <a href="#">Figure 6</a> amended</li><li>• <a href="#">Section 8.1 "Quality information"</a>: added</li><li>• <a href="#">Section 13 "Legal information"</a>: updated</li></ul>			
PMBD7000_3	19990511	Product specification	-	PMBD7000_2
PMBD7000_2	19960918	Product specification	-	PMBD7000_1
PMBD7000_1	19960419	Product specification	-	-

## 13. Legal information

### 13.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 13.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### 13.3 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or

malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

## 13.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## 14. Contact information

---

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

## 15. Contents

---

<b>1</b>	<b>Product profile</b> . . . . .	<b>1</b>
1.1	General description . . . . .	1
1.2	Features and benefits . . . . .	1
1.3	Applications . . . . .	1
1.4	Quick reference data . . . . .	1
<b>2</b>	<b>Pinning information</b> . . . . .	<b>2</b>
<b>3</b>	<b>Ordering information</b> . . . . .	<b>2</b>
<b>4</b>	<b>Marking</b> . . . . .	<b>2</b>
<b>5</b>	<b>Limiting values</b> . . . . .	<b>3</b>
<b>6</b>	<b>Thermal characteristics</b> . . . . .	<b>3</b>
<b>7</b>	<b>Characteristics</b> . . . . .	<b>4</b>
<b>8</b>	<b>Test information</b> . . . . .	<b>6</b>
8.1	Quality information . . . . .	6
<b>9</b>	<b>Package outline</b> . . . . .	<b>7</b>
<b>10</b>	<b>Packing information</b> . . . . .	<b>7</b>
<b>11</b>	<b>Soldering</b> . . . . .	<b>8</b>
<b>12</b>	<b>Revision history</b> . . . . .	<b>9</b>
<b>13</b>	<b>Legal information</b> . . . . .	<b>10</b>
13.1	Data sheet status . . . . .	10
13.2	Definitions . . . . .	10
13.3	Disclaimers . . . . .	10
13.4	Trademarks . . . . .	11
<b>14</b>	<b>Contact information</b> . . . . .	<b>11</b>
<b>15</b>	<b>Contents</b> . . . . .	<b>12</b>

---

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

---

© NXP B.V. 2010.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 16 September 2010

Document identifier: PMBD7000