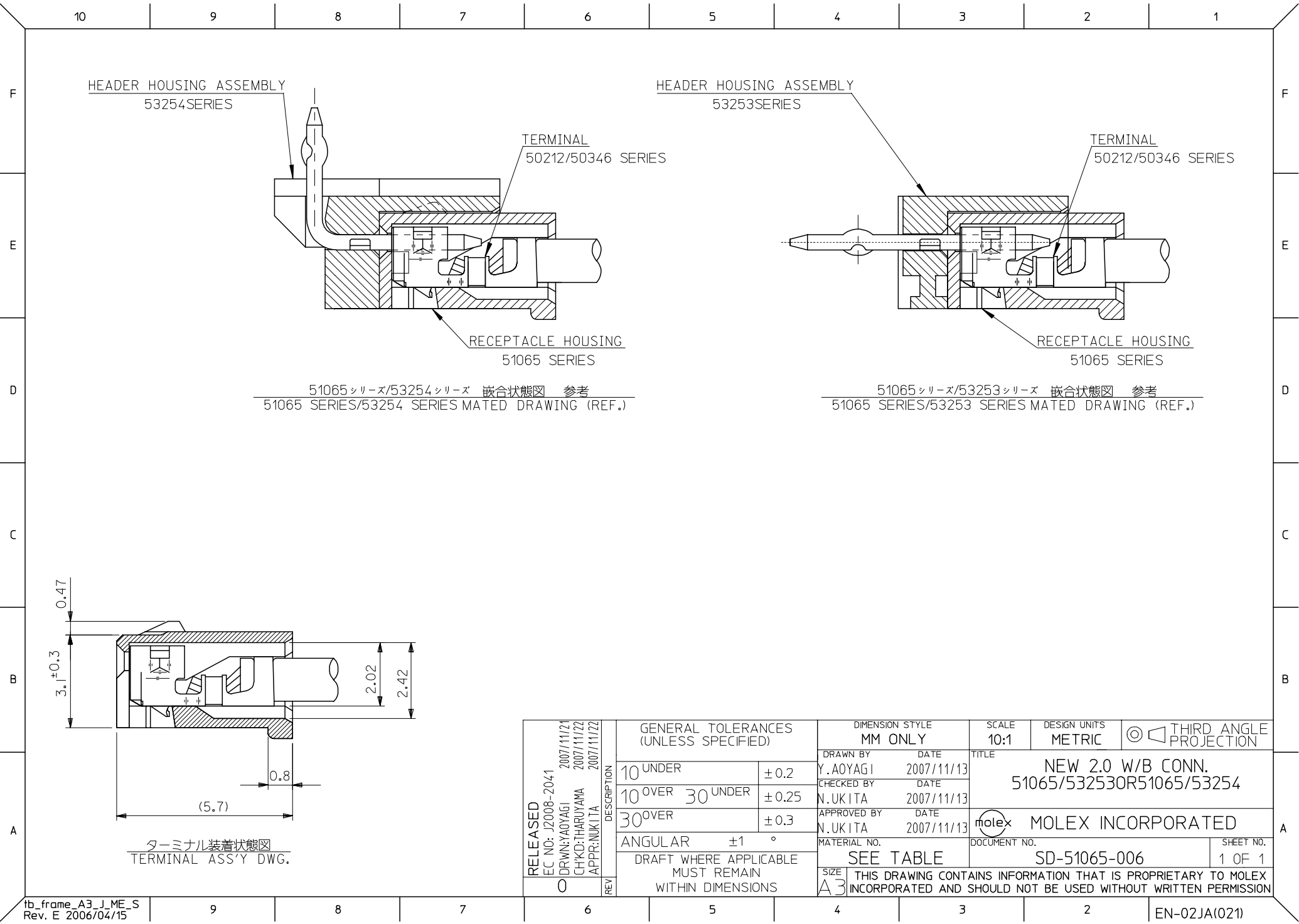




10 9 8 7 6 5 4 3 2 1

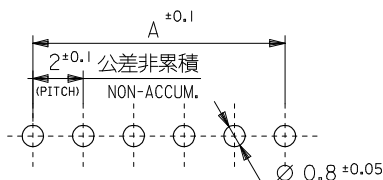
注記 NOTES

1. 嵌合相手 : 51065 シリーズ
MATE WITH : 51065 SERIES
2. 材質 MATERIAL
ウエハー : ポリアミド、UL 94V-0 色 : ナチュラル (白)
WAFER : POLYAMIDE, UL94V-0 COLOR: NATURAL (WHITE)
ピン : 黄銅、錫メッキ
PIN : BRASS, TIN(Sn) PLATING
3. キングは下記表の位置及び形状に付加のこと。2極はキング無し。
KINK TO BE APPLIED AS FOLLOWS,
2 CIRCUIT HAS NO KINK.

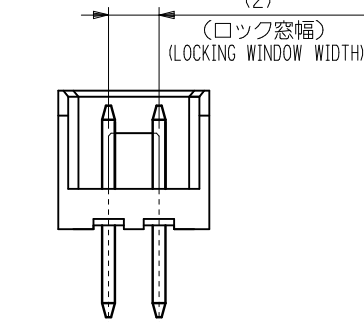
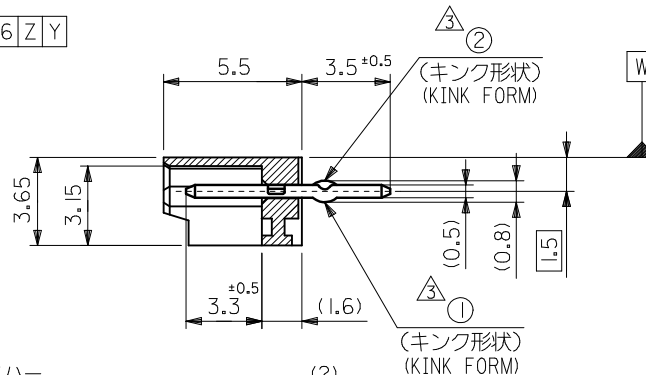
回路番号 Circuit No.		1	2	3	4	5	N-2	N-1	N
極数 Ckt	6 極以上 MORE THAN 6 Ckt	—	①	②	—	—	②	①	—
5 極	5 Ckt	①	②	—	②	①			
4 極	4 Ckt	①	②	②	①				
3 極	3 Ckt	①	②	①					

4. 本製品は、53253**10 の鉛フリー品です。
THIS PRODUCT IS LEAD FREE OF 53253**10.
5. 梱包仕様 PACKAGING
53253**70 トレー TRAY
53253**50 ポリ袋 POLYBAG

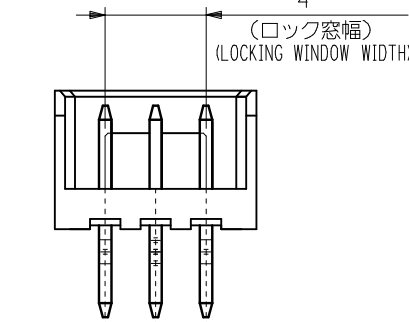
32	31.1	28	532531550	53253 1570	15
30	29.1	26	1450	1470	14
28	27.1	24	1350	1370	13
26	25.1	22	1250	1270	12
24	23.1	20	1150	1170	11
22	21.1	18	1050	1070	10
20	19.1	16	0950	0970	9
18	17.1	14	0850	0870	8
16	15.1	12	0750	0770	7
14	13.1	10	0650	0670	6
12	11.1	8	0550	0570	5
10	9.1	6	0450	0470	4
8	7.1	4	0370	0370	3
6	5.1	2	532530250	53253 0270	2
C	B	A	MATERIAL NO.	MATERIAL NO.	極数
			MODEL NO.	53253**70/**50	



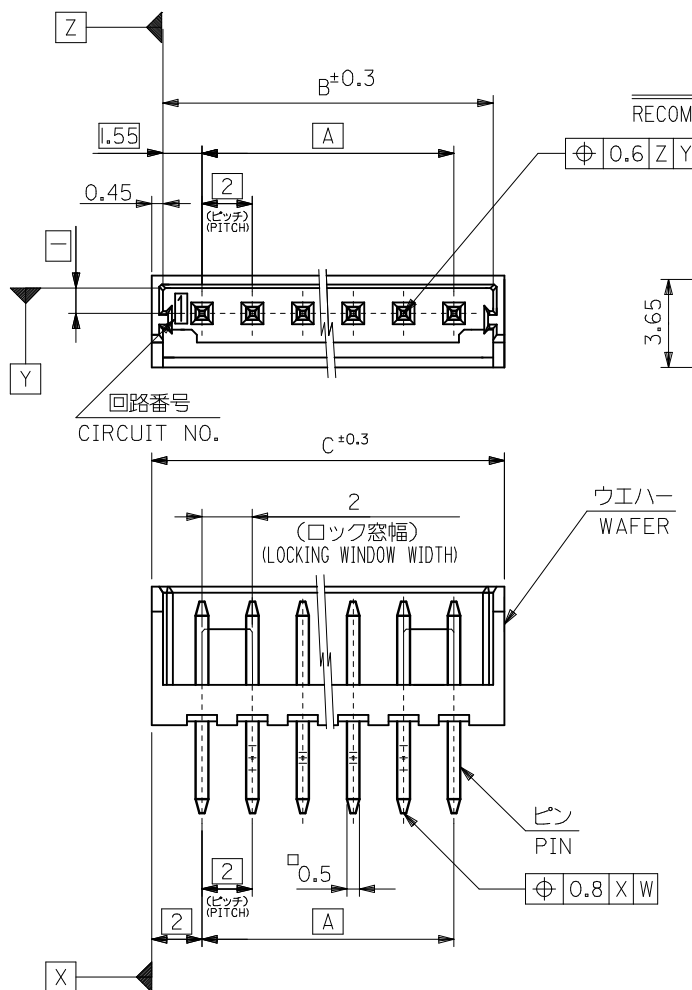
基板取付穴寸法 (参考) (t=1.6)
RECOMMENDED P.C. BOARD HOLE DIM. (REFERENCE)



2 極の場合
IN CASE OF 2 CIRCUIT WAFER



3 極の場合
IN CASE OF 3 CIRCUIT WAFER



GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 5:1		DESIGN UNITS METRIC		THIRD ANGLE PROJECTION	
10 UNDER	±0.2	DRAWN BY	DATE	TITLE		NEW 2.0 WIRE TO BOARD CONN. WAFER ASSY (ST.) -LEAD FREE-		molex	
10 OVER 30 UNDER	±0.25	T. TSUBOI	'04/01/15						
30 OVER	±0.3	CHECKED BY	DATE	SEE CHART		SD-53253-001		SHEET NO. 1 OF 1	
ANGULAR	±3 °	S. MARUYAMA	'04/01/15						
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		APPROVED BY	DATE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					
		M. SASAO	'04/01/15						
		MATERIAL NO.							
		SIZE							
		A3							