

STRUCTURE Silicon monolithic integrated circuits

PRODUCT SERIES 3-phase motor driver for VTR capstan

TYPE **BA6868FM**

FUNCTION

- Direct PWM pseudo-linear drive system
- Built-in torque ripple canceling circuit

○ Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limit	Unit
Supply voltage	VCC	7	V
	VM	22	V
Power dissipation	Pd	2.20 ^{※1}	W
Input voltage	VIN	0~VCC	V
Maximum output current	IOUT	1800 ^{※2}	mA
Operating temperature range	Topr	-25~+75	°C
Storage temperature range	Tstg	-55~+150	°C
Junction temperature	Tjmax	150	°C

※¹ 70mm × 70mm × 1.6mm glass epoxy board. Derating is done at 17.6mW/°C for operating above Ta=25°C.

※² Do not, however exceed Pd, ASO and Tjmax=150°C.

○ Recommended operating conditions (Ta= -25~+75°C)

Parameter	Symbol	Min	Typ	Max	Unit
Supply voltage	VCC	4.5	5	6.0	V
	VM	4.0	12	20.5	V
Hall amp in-phase input voltage range	VCH	1.5	-	VCC-1.7	V

This product described in this specification isn't judged whether it applies to COCOM regulations.

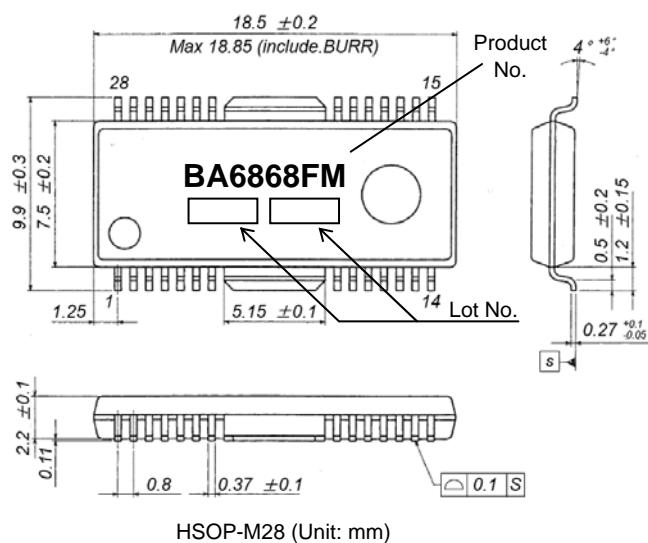
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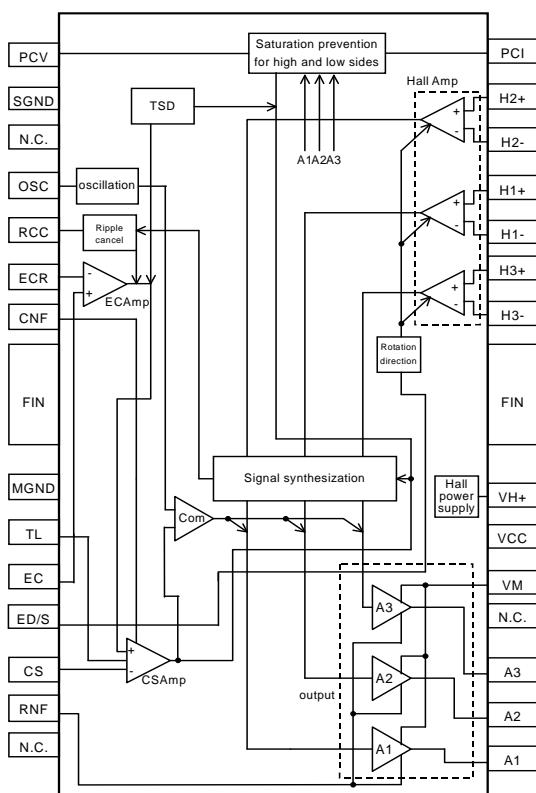
○Electrical characteristics (Unless otherwise specified, Ta=25°C, VCC=5V, VM=12V)

Parameter	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Overall						
Circuit current	Icc	-	11	17	mA	Ec=GND, input LLH
Hall input						
Hall input conversion offset	Heofs	-10	-	10	mV	
Hall element power supply voltage	VHp	2.45	2.65	2.85	V	IH+=9mA
Torque reference						
Torque reference offset voltage	ECofs	-120	-	+120	mV	
Torque reference input gain	Gio	0.95	1.07	1.18	A/V	RNF=0.5Ω
Output idling voltage	ECidle	-	-	10	mV	
ECR bias voltage	VECR	2.0	2.2	2.4	V	
Torque limit						
TL-CS offset voltage	TL-CSofs	39	56	73	mV	
Ripple canceling						
Ripple canceling rate	VRcc	6.3	9.0	11.7	%	Input LLH→LMH
Forward / reverse rotation selection						
Forward rotation reference voltage range	VEDF	-	-	2.2	V	
Reverse rotation reference voltage range	VEDR	2.8	-	-	V	
Output						
High output voltage	VOH	0.63	0.90	1.17	V	IOUT=-350mA
Low output voltage	VOL	0.42	0.60	0.78	V	IOUT=350mA, RNF=0.5Ω
oscillator						
High OSC voltage	VOSCH	17	2.1	2.5	V	
Low OSC voltage	VOSCL	1.0	1.2	1.4	V	
Oscillating frequency	FOSC	30	50	70	kHz	COSC=1000pF

○ Package outline



○ Block diagram



○ Pin No. / Pin name

Pin No.	Pin name
1	PCV
2	SGND
3	N.C.
4	OSC
5	RCC
6	ECR
7	CNF
8	MGND
9	TL
10	EC
11	ED/S
12	CS
13	RNF
14	N.C.
15	A1
16	A2
17	A3
18	N.C.
19	VM
20	Vcc
21	VH+
22	H3-
23	H3+
24	H1-
25	H1+
26	H2-
27	H2+
28	PCI

Operation Notes

(1) Absolute maximum ratings

Use of the IC in excess of absolute maximum ratings such as the applied voltage or operating temperature range (Topr) may result in IC damage. Assumptions should not be made regarding the state of the IC (short mode or open mode) when such damage is suffered. The implementation of a physical safety measure such as a fuse should be considered when use of the IC in a special mode where the absolute maximum ratings may be exceeded is anticipated.

(2) Power supply lines

Regenerated current may flow as a result of the motor's back electromotive force. Insert capacitors between the power supply and ground pins to serve as a route for regenerated current. Determine the capacitance in full consideration of all the characteristics of the electrolytic capacitor, because the electrolytic capacitor may lose some capacitance at low temperatures. If the connected power supply does not have sufficient current absorption capacity, regenerative current will cause the voltage on the power supply line to rise, which combined with the product and its peripheral circuitry may exceed the absolute maximum ratings. It is recommended to implement a physical safety measure such as the insertion of a voltage clamp diode between the power supply and GND pins.

(3) Ground potential

Ensure a minimum GND pin potential in all operating conditions.

(4) Setting of heat

Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions.

(5) Actions in strong magnetic field

Use caution when using the IC in the presence of a strong magnetic field as doing so may cause the IC to malfunction.

(6) ASO

When using the IC, set the output transistor for the motor so that it does not exceed absolute maximum ratings or ASO.

(7) Thermal shutdown circuit

This IC incorporates a TSD (thermal shutdown) circuit (TSD circuit). If the temperature of the chip reaches the following temperature, the motor coil output will be opened. The thermal shutdown circuit (TSD circuit) is designed only to shut the IC off to prevent runaway thermal operation. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operating this circuit or use the IC in an environment where the operation of this circuit is assumed.

TSD on temperature [°C] (typ.)	Hysteresis temperature [°C] (typ.)
175	65

(8) Ground Wiring Pattern

When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the application's reference point so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.

Notes

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