

TOSHIBA

MICROWAVE SEMICONDUCTOR TECHNICAL DATA

MICROWAVE POWER GaAs FET

JS8850A-AS

FEATURES:

- HIGH POWER
 $P_{1dB} = 21.5 \text{ dBm}$ at $f = 15 \text{ GHz}$
- HIGH GAIN
 $G_{1dB} = 9.0 \text{ dB}$ at $f = 15 \text{ GHz}$
- SUITABLE FOR Ku-BAND AMPLIFIER
- ION IMPLANTATION
- CHIP FORM

RF PERFORMANCE SPECIFICATIONS ($T_a = 25^\circ \text{C}$)

CHARACTERISTIC	SYMBOL	CONDITION		UNIT	MIN.	TYP.	MAX.
		f	V _{DS}				
Output Power at 1dB Compression Point	P _{1dB}	15GHz	10V	dBm	20.5	21.5	-
			8 V		-	20.5	-
		18GHz	10V		-	21.0	-
			8 V		-	20.0	-
Power Gain at 1dB Compression Point	G _{1dB}	15GHz	10V	dB	8.0	9.0	-
			8 V		-	9.0	-
		18GHz	10V		-	8.5	-
			8 V		-	8.5	-
Drain Current	I _{DS}	15GHz	10, 8 V	A	-	0.06	0.09
		18GHz	10, 8 V		-	0.06	0.09
Power Added Efficiency	η_{add}	15GHz	10V	%	-	21	-
			8 V		-	20	-
		18GHz	10V		-	19	-
			8 V		-	18	-

ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ \text{C}$)

CHARACTERISTIC	SYMBOL	CONDITION	UNIT	MIN.	TYP.	MAX.
Trans-Conductance	g_m	V _{DS} =3V I _{DS} =45mA	mS	-	30	-
Pinch-off Voltage	V _{GSoff}	V _{DS} =3V I _{DS} =1.5mA	V	-2.5	-3.5	-5
Saturated Drain Current	I _{DSS}	V _{DS} =3V V _{GS} =0V	A	-	0.1	0.125
Gate to Source Breakdown Voltage	V _{GSO}	I _{GS} =-1.5μA	V	-5	-	-
Thermal Resistance	R _{th(c-c)}	Channel to Case	°C/W	-	90	150

★ The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of TOSHIBA or others.

★ The information contained herein may be changed without prior notice. It is therefore advisable to contact TOSHIBA before proceeding with the design of equipment incorporating this product.

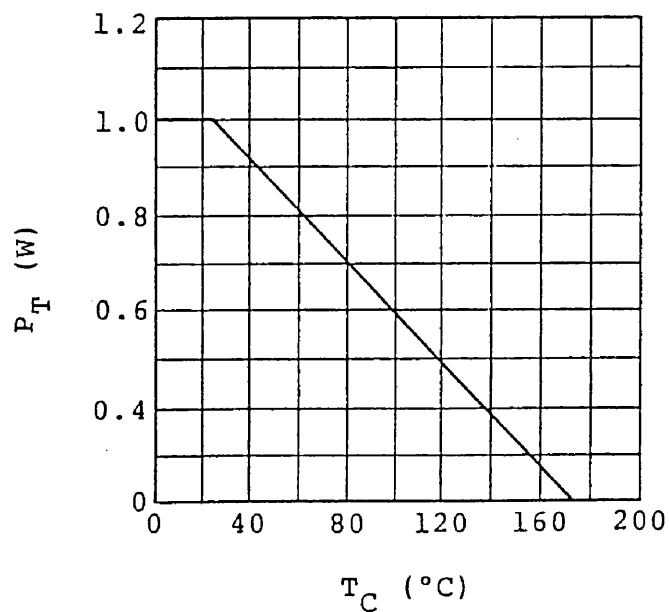


JS8850A-AS

ABSOLUTE MAXIMUM RATINGS ($T_a=25^{\circ}\text{C}$)

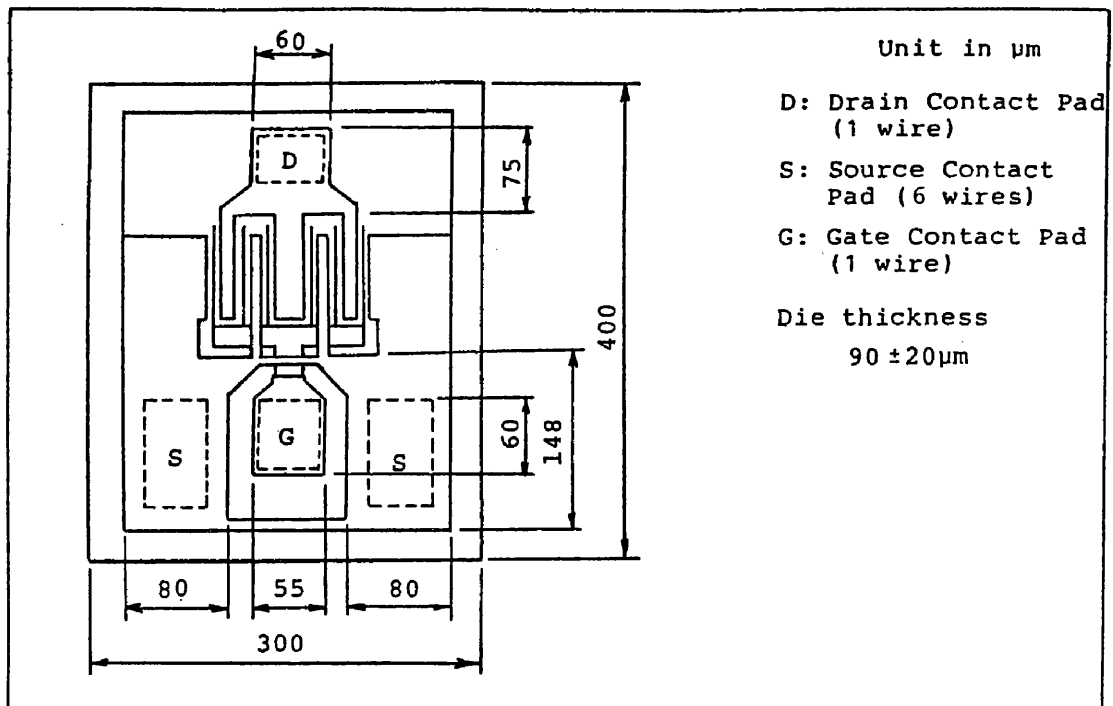
TYPE NUMBER (PACKAGE CODE)			JS8850A-AS (CHIP)
CHARACTERISTIC	SYMBOL	UNIT	RATING
Drain-Source Voltage	V_{DS}	V	15
Gate-Source Voltage	V_{GS}	V	-5
Drain Current	I_D	A	0.125
Total Power Dissipation ($T_c=25^{\circ}\text{C}$)	P_T	W	1
Channel Temperature	T_{ch}	$^{\circ}\text{C}$	175
Storage Temperature	T_{stg}	$^{\circ}\text{C}$	-65~175

POWER DISSIPATION VS. CASE TEMPERATURE



JS8850A-AS

CHIP OUTLINE



ASSEMBLY PRECAUTIONS FOR CHIP

The operations must be performed in a clean and dry environment. The chips must be kept in the same environment when they are not used. All test instruments, assembly machines, benches, tweezers and operators should be grounded to avoid damage due to electrostatic discharge. Careful attention must be paid in handling chips with tweezers because GaAs is more brittle than Si.

DIE ATTACHMENT

Die attachment can be accomplished with Au-Sn preform in nitrogen atmosphere. The operating temperature must be $290 \pm 10^\circ\text{C}$ for the preform to be well melted. The operating time should be kept within one minute.

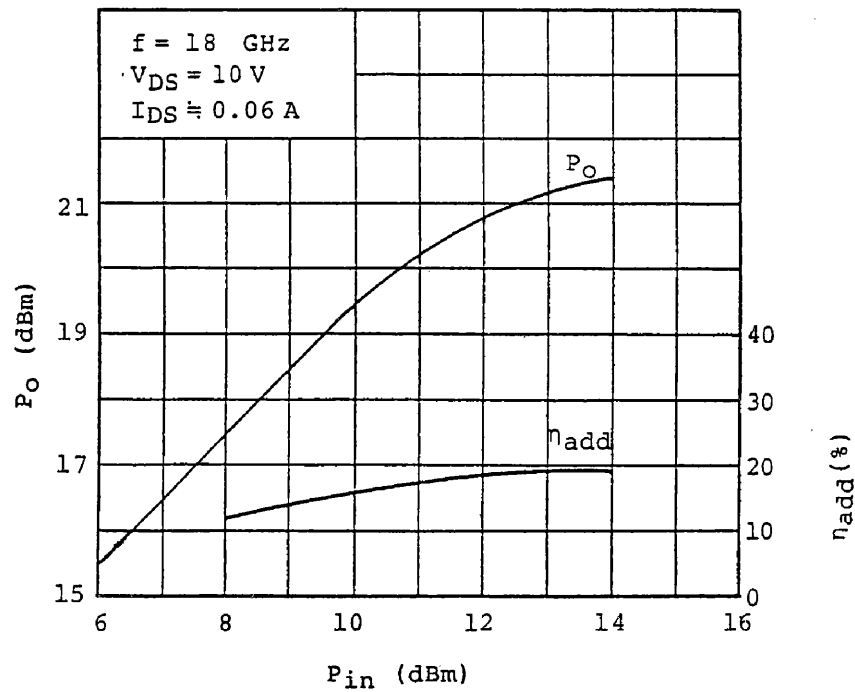
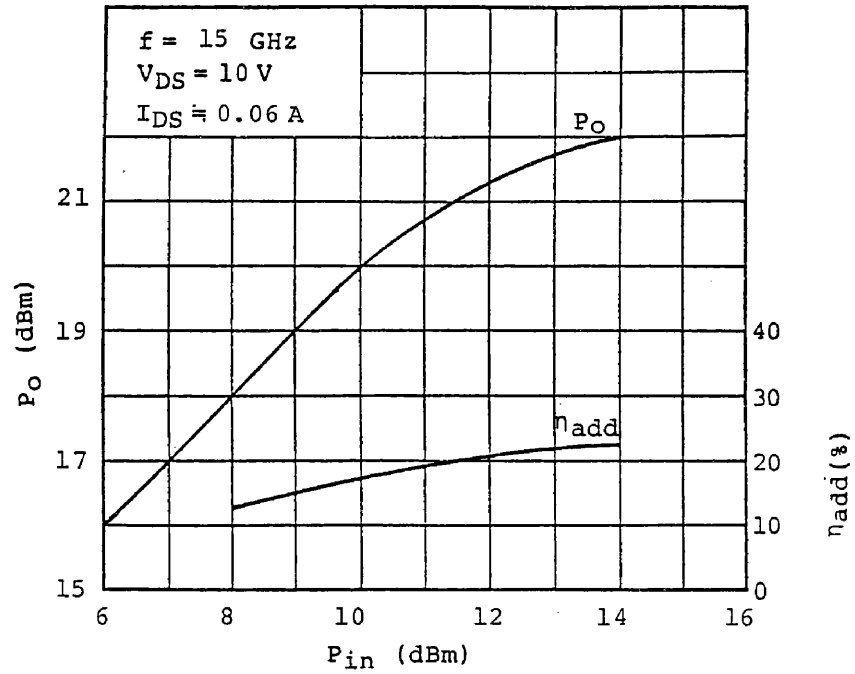
WIRE BONDING

Bonding should be performed with thermal compression wedge bonder in nitrogen atmosphere. The recommended conditions of wire bonding are as follows:

- (1) Operating temperature $200 \pm 10^\circ\text{C}$
- (2) Operating time 5 minutes max.
- (3) Bonding wire 25 μm diameter pure Au

JS8850A-AS

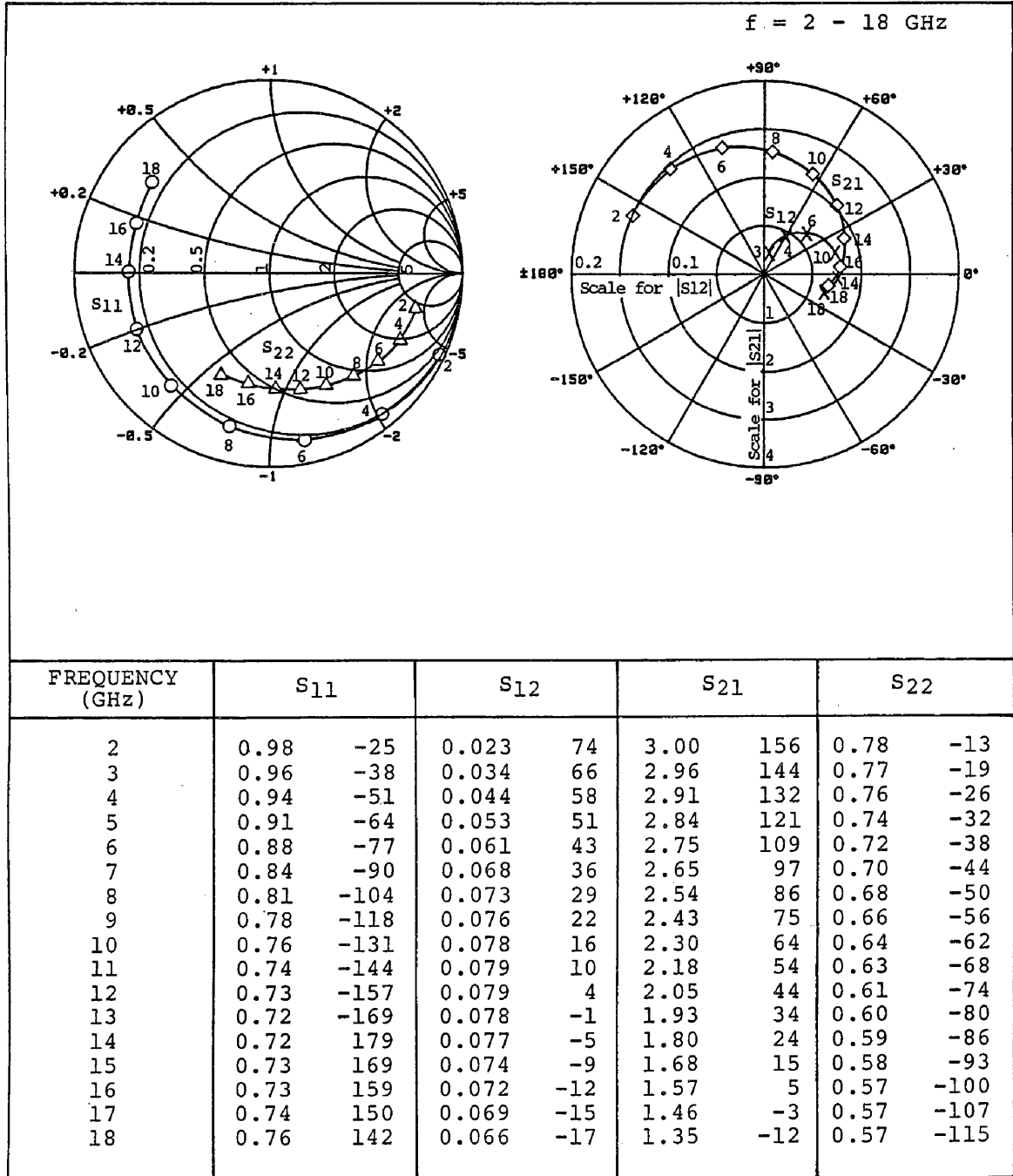
RF PERFORMANCES



JS8850A-AS

JS8850A-AS S-PARAMETERS (MAGN. and ANGLES)

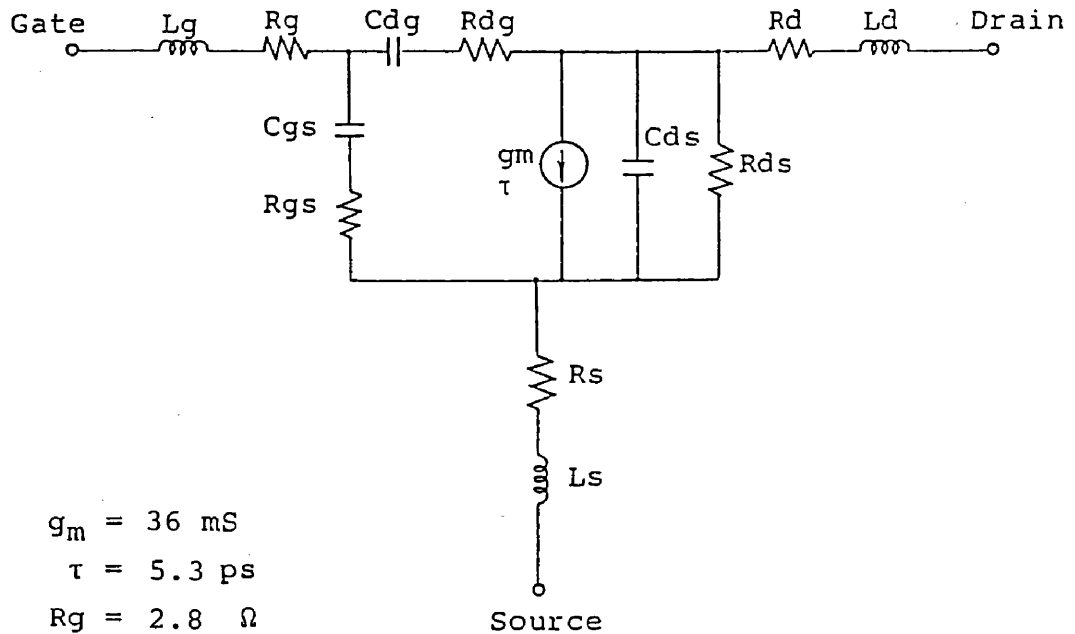
$V_{DS} = 10 \text{ V}$, $I_{DS} = 50 \text{ mA}$



JS8850A-AS

EQUIVALENT CIRCUIT (Includes normal bonding wires)

JS8850A-AS : $V_{DS} = 10$, $I_{DS} = 50$ mA



$$g_m = 36 \text{ mS}$$

$$\tau = 5.3 \text{ ps}$$

$$R_g = 2.8 \text{ } \Omega$$

$$R_s = 1.5 \text{ } \Omega$$

$$R_d = 2.6 \text{ } \Omega$$

$$R_{gs} = 2.5 \text{ } \Omega$$

$$R_{dg} = 0.01 \text{ } \Omega$$

$$R_{ds} = 405 \text{ } \Omega$$

$$C_{gs} = 0.31 \text{ pF}$$

$$C_{dg} = 0.02 \text{ pF}$$

$$C_{ds} = 0.13 \text{ pF}$$

$$L_g = 0.37 \text{ nH}$$

$$L_s = 0.02 \text{ nH}$$

$$L_d = 0.23 \text{ nH}$$

Bonding wire condition

	Number (pcs)	Length (Approx.) (mm)	Dia. (μm)
Source	6	0.3	$\phi 25$
Drain	1	0.4	$\phi 25$
Gate	1	0.4	$\phi 25$