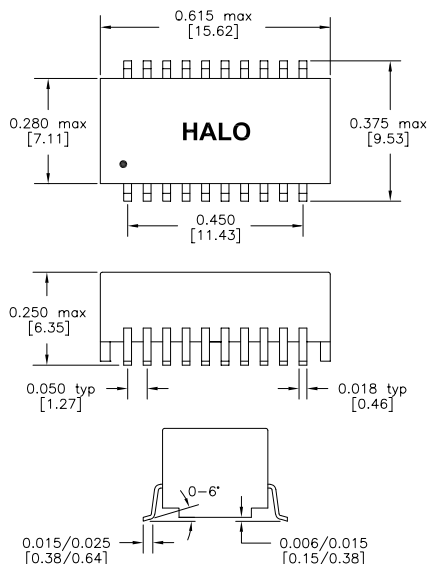


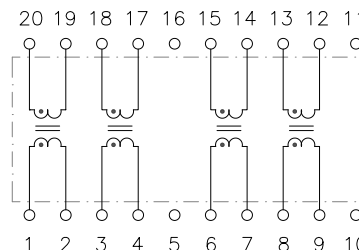
HALO Electronics has developed this standard series of high density Dual Port SMD Line Interface Modules to meet the isolation and impedance matching requirements for T1, E1, CEPT and PRI ISDN applications. UL60950 and EN60950 recognized.



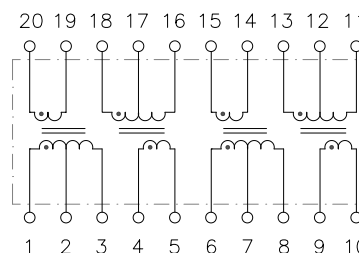
NM5 Package



Schematic A



Schematic B



Patented Construction

US Pat Nbrs: 5,656,985 6,297,721 B1
 6,297,720 B1 6,320,489 B1
 6,344,785 B1 6,662,431 B1

Electrical Specifications @ 25°C

Isolation Voltage: 1,500 Vrms
 Leakage Inductance: 0.5μH Max.
 DCR (PRI): 0.80Ω Max.
 Cw/w: 35pF Max.
 Crosstalk (0.1-10MHz): 60dB Typ.
 Operating Temp: 0 to 70°C

Part Number	Schematic	Turns Ratio PRI : SEC	PRI OCL (mH typ)	Primary Pins
TG05-1205NM5RL	A	1:2	1.2	1-2, 3-4, 6-7, 8-9
TG07-1205NM5RL	B	1:2CT & 1:2CT	1.2	20-19, 15-14 & 4-5, 9-10
TG23-1205NM5RL	A	1:2.42	1.2	1-2, 3-4, 6-7, 8-9
TG25-1205NM5RL	A	1:1.26	1.2	1-2, 3-4, 6-7, 8-9
TG27-S003NM5RL	B	1:2CT & 1:1.36CT	1.2	20-19, 15-14 & 4-5, 9-10
TG36-1205NM5RL	A	1:1.15 & 1:1	1.2	1-2, 6-7 & 18-17, 13-12
TG37-1205NM5RL	B	1:1.14CT	1.2	20-19, 4-5, 15-14, 9-10
TG39-1205NM5RL	A	1:1.15	1.2	1-2, 3-4, 6-7, 8-9
TG42-1205NM5RL	A	1:√2	1.2	1-2, 18-17, 6-7, 13-12
TG49-1205NM5RL	B	1CT:2 & 1:2CT	1.2	1-3, 6-8 & 4-5, 9-10
TG83-1205NM5RL	A	1:2 & 1:1	1.2	1-2, 6-7 & 18-17, 13-12
TGSP-S125NM5RL	A	1:2.4 & 1:1	1.2	1-2, 8-9 & 3-4, 6-7