TOSHIBA Transistor Silicon NPN Epitaxial Planar Type

MT3S41FS

VCO Oscillator Stage

UHF Low-Noise Amplifier Application

Features

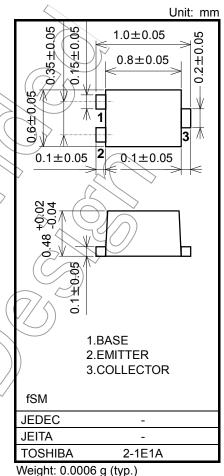
- Low-Noise Figure: NF = 1.2 dB (@f= 2 GHz)
- High Gain: $|S21e|^2 = 10.0 \text{ dB}$ (@ f = 2 GHz)

Marking



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V _{CBO})) 8	V /
Collector-emitter voltage	VCEO	4.5	\ V
Emitter-base voltage	(V _{EBO}))	1.5	//v
Collector-current	C	80	m.A
Base-current	() _B	40	mA
Collector power dissipation	PC (Note 1)	(100/)	mW
Junction temperature		150	°C
Storage temperature range	T _{stg}	-55~150	°C
*			



Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the Note: significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Rlease design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device mounted on a glass-epoxy PCB (1.0 cm² x 0.8 mm (t))

Microwave Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Transition frequency	fT	V _{CE} = 3 V, I _C = 20 mA, f = 2 GHz	11	15	-	GHz
Insertion gain	S21e ² (1)	V _{CE} = 3 V, I _C = 20 mA, f = 1 GHz	13.5	15.5	-	dB
	S21e ² (2)	$V_{CE} = 3 \text{ V, } I_{C} = 20 \text{ mA, } f = 2 \text{ GHz}$	8	10	-	dB
Noise figure	NF (1)	$V_{CE} = 3 \text{ V}, I_{C} = 5 \text{ mA}, f = 1 \text{ GHz}$)	0.8	-	dB
	NF (2)	$V_{CE} = 3 \text{ V}, I_{C} = 5 \text{ mA}, f = 2 \text{ GHz}$) M.2	1.8	dB

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	V _{CB} = 8 V, I _E = 0	-	(-)	1	μΑ
Emitter cut-off current	I _{EBO}	V _{EB} = 1 V, I _C = 0	- /	4	\(\frac{1}{2}\)	μΑ
DC current gain	hFE	V _{CE} = 3 V, I _C = 20 mA	70	-/) 140	-
Output capacitance	C _{ob}	V _{CB} = 1 V, I _E = 0, f = 1 MHz	Ž-(C	0.72) 1.10	pF
Reverse transistor capacitance	C _{re}	V _{CB} = 1 V, I _E = 0, f = 1 MHz (Note 1)	2 -	0.46	0.85	pF

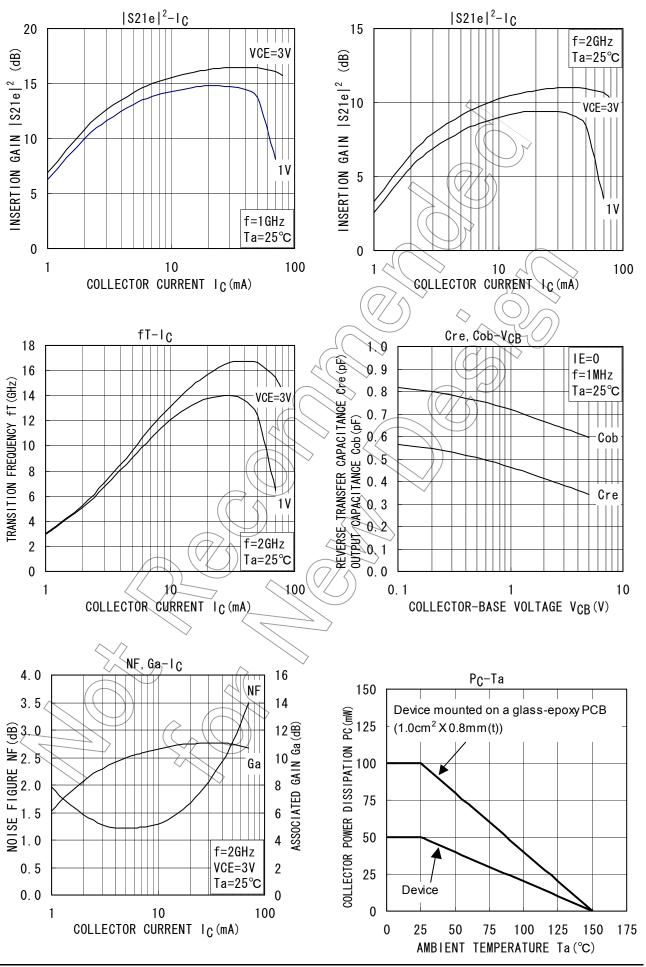
Note 1: Cre is measured using a three-terminal method with a capacitance bridge

Note 2: This product is a lead-free article.

Caution: This device is sensitive to electrostatic discharge. Be sure to provide all tools and equipment with adequate grounding.

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