## **SIEMENS**

## Product data sheet 6ES7214-1BG31-0XB0



SIMATIC S7-1200, CPU 1214C, COMPACT CPU, AC/DC/RLY, ONBOARD I/O: 14 DI 24V DC; 10 DO RELAY 2A; 2 AI 0 - 10V DC,

POWER SUPPLY: AC 85 - 264 V AC AT 47 - 63 HZ,

PROGRAM/DATA MEMORY: 75 KB

General information	
Engineering with	
Programming package	STEP 7 V11 SP2 or higher
Supply voltage	
120 V AC	Yes
230 V AC	Yes
permissible range, lower limit (AC)	85 V
permissible range, upper limit (AC)	264 V
Line frequency	
permissible frequency range, lower limit	47 Hz
permissible frequency range, upper limit	63 Hz
Input current	
Current consumption (rated value)	100 mA at 120 V AC; 50 mA at 240 V AC
Inrush current, max.	20 A ; at 264 V
Encoder supply	
24 V encoder supply	
24 V	Permissible range: 20.4V to 28.8V
Output current	

Power loss Power loss, typ. 14 W  Memory  Type of memory EEPROM  usable memory for user data 75 kbyte  Work memory  integrated 75 kbyte  expandable No  Load memory  integrated 4 Mbyte  Backup  present Yes; maintenance-free  without battery Yes  CPU processing times  for bit operations, typ. 0.085 µs; / instruction  for word operations, typ. 1.7 µs; / instruction  for floating point arithmetic, typ. 2.5 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of	Current output to backplane bus (5 V DC), max.	1600 mA; Max. 5 V DC for SM and CM
Power loss, typ.  Memory  Type of memory  usable memory for user data  75 kbyte  Work memory  integrated  expandable  No  Load memory  Integrated  A Mbyte  Backup  present  Without battery  CPU processing times  for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Number, max.  Data areas and their retentivity  retentive data area in total (incl. times, counters, flege), max.  Flag  Number, max.  Address area  I/O address area, overali  Process limage  Inputs, adjustable  Hardware configuration  Number of modules per system, max.  A signal board, 8 signal modules  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		
Type of memory  usable memory for user data  Work memory  Integrated 75 kbyte  expandable No  Load memory  Integrated 4 Mbyte  Backup  present Yes: maintenance-free  without battery Yes: maintenance-free  without battery Yes: maintenance-free  for bit operations, typ. 0.085 µs; // instruction  for word operations, typ. 1.7 µs; // instruction  for floating point arithmetic, typ. 2.5 µs; // instruction  CPU-blocks  Number of blocks (total)  BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 6535s. There is no restriction the entire working memory can be used  OB  Number, max. Limited only by RAM for code  Data areas and their retentivity  retentive data area in total (incl. times, counters, flags), max.  I to kbyte  Flag  Number, max. 8 kbyte; Size of bit memory address area  I/O address		14 W
Type of memory usable memory for user data  Work memory integrated expandable Abdyte  Backup  present Yes : maintenance-free Without battery  CPU processing times  For bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Bask (total)  DBS, FCS, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity retentive data area in total (incl. times, counters, flags), max.  10 address area  1/O address area, overall  1 tbyte  1 kbyte  1 kbyte  1 kbyte  1 kbyte  1 kbyte		
usable memory for user data  Work memory  Integrated 75 kbyte expandable No  Load memory  Integrated 4 Mbyte  Backup present Yes; maintenance-free without battery Yes  CPU processing times  for bit operations, typ. 0.085 µs; / instruction  17 µs; / instruction  17 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max. Limited only by RAM for code  Data areas and their retentivity retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max. 8 kbyte; Size of bit memory address area  I/O address area, overall 1024 bytes for inputs / 1024 bytes for outputs  Process image Inputs, adjustable 1 kbyte  Outputs, adjustable 1 kbyte  Hardware configuration  Number of modules per system, max. 3 cmm. modules, 1 signal board, 8 signal modules		EEPROM
integrated 75 kbyte expandable No  Load memory  Integrated 4 Mbyte  Backup  present Yes : maintenance-free without battery Yes  CPU processing times  for bit operations, typ. 0.085 µs : / instruction for word operations, typ. 1.7 µs : / instruction  for floating point arithmetic, typ. 2.5 µs : / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max.		75 kbyte
integrated 75 kbyte expandable No  Load memory  integrated 4 Mbyte  Backup present Yes ; maintenance-free without battery Yes  CPU processing times for bit operations, typ. 0.085 µs ; / instruction for word operations, typ. 1.7 µs ; / instruction  CPU-blocks  Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65635. There is no restriction the entire working memory can be used  OB  Number, max. Limited only by RAM for code  Data areas and their retentivity retentive data area in total (incl. times, counters, flags), max. 10 kbyte  Flag  Number, max. 8 kbyte; Size of bit memory address area  I/O address area I/O address area I/O address area I/O address area I/O address area I/O address area, overall 1024 bytes for inputs / 1024 bytes for outputs  Process Image Inputs, adjustable 1 kbyte Hardware configuration Number of modules per system, max. 3 comm. modules, 1 signal board, 8 signal modules	·	•
expandable No  Load memory  Integrated 4 Mbyte  Backup  present Yes; maintenance-free  without battery Yes  CPU processing times  for bit operations, typ. 0.085 µs; / instruction  for word operations, typ. 1.7 µs; / instruction  CPU-blocks  Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max. Limited only by RAM for code  Data areas and their retentivity  retentive data area in total (incl. times, counters, flags), max. 10 kbyte  Flag  Number, max. 8 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area  I/O address area, overall 1024 bytes for inputs / 1024 bytes for outputs  Process Image  Inputs, adjustable 1 kbyte  Hardware configuration  Number of modules per system, max. 3 comm. modules, 1 signal board, 8 signal modules		75 kbyte
Integrated 4 Mbyte  Backup present Yes; maintenance-free without battery Yes  CPU processing times for bit operations, typ. 0.085 µs; / instruction for word operations, typ. 1.7 µs; / instruction  CPU-blocks  Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max. Limited only by RAM for code  Data areas and their retentivity retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max. 8 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area  I/O address area, overall 1024 bytes for inputs / 1024 bytes for outputs  Process image Inputs, adjustable 1 kbyte  Hardware configuration  Number of modules per system, max. 3 comm. modules, 1 signal board, 8 signal modules		
Integrated  Backup  present without battery  Pes  CPU processing times  for bit operations, typ. 0.085 µs; / instruction 1.7 µs; / instruction 1.7 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max.  Data areas and their retentivity retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  3 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area, overall  Process image Inputs, adjustable 1 kbyte  Outputs, adjustable 1 kbyte  Hardware configuration  Number of modules per system, max. 3 comm. modules, 1 signal board, 8 signal modules		
Blackup present without battery Yes  CPU processing times for bit operations, typ. 0.085 µs; / instruction for word operations, typ. 1.7 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB Number, max.  Limited only by RAM for code  Data areas and their retentivity retentive data area in total (incl. times, counters, flags), max.  Flag Number, max.  8 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area, overall  Process Image Inputs, adjustable 1 kbyte Outputs, adjustable 1 kbyte Hardware configuration  Number of modules per system, max. 3 comm. modules, 1 signal board, 8 signal modules		4 Mbyte
present without battery Yes  CPU processing times  for bit operations, typ. for word operations, typ. 1.7 µs; / instruction  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  I/O address area, overall  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules	•	
without battery  CPU processing times  for bit operations, typ.  for word operations, typ.  1.7 µs; / instruction  for floating point arithmetic, typ.  2.5 µs; / instruction  CPU-blocks  Number of blocks (total)  BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area  I/O address area, overall  Process Image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		Yes ; maintenance-free
for bit operations, typ.  for word operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  2.5 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area, overall  1024 bytes for inputs / 1024 bytes for outputs  Process image  Inputs, adjustable  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules	·	
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for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area, overall  1024 bytes for inputs / 1024 bytes for outputs  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		0.085 μs ; / instruction
for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area, overall  1024 bytes for inputs / 1024 bytes for outputs  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		
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addressable blocks ranges from 1 to 65535. There is no restriction the entire working memory can be used  Number, max.  Limited only by RAM for code  Data areas and their retentivity  retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  I/O address area  I/O address area  I/O address area  I/O address area, overall  1024 bytes for inputs / 1024 bytes for outputs  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules	CPU-blocks	
Number, max.  Data areas and their retentivity  retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  Address area  I/O address area  I/O address area, overall  1024 bytes for inputs / 1024 bytes for outputs  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  2 limited only by RAM for code  10 kbyte	Number of blocks (total)	addressable blocks ranges from 1 to 65535. There is no restriction,
Pata areas and their retentivity  retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  Address area  I/O address area  I/O address area, overall  1024 bytes for inputs / 1024 bytes for outputs  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules	ОВ	
retentive data area in total (incl. times, counters, flags), max.  Flag  Number, max.  8 kbyte; Size of bit memory address area  Address area  I/O address area  I/O address area, overall  1024 bytes for inputs / 1024 bytes for outputs  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules	Number, max.	Limited only by RAM for code
Number, max.  8 kbyte; Size of bit memory address area  Address area  I/O address area  I/O address area, overall  1024 bytes for inputs / 1024 bytes for outputs  Process image  Inputs, adjustable  1 kbyte  Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules	Data areas and their retentivity	
Number, max.  Address area  I/O address area  I/O address area, overall  Process image  Inputs, adjustable  Outputs, adjustable  Outputs, adjustable  Hardware configuration  Number of modules per system, max.  8 kbyte; Size of bit memory address area  1024 bytes for inputs / 1024 bytes for outputs  1 kbyte  1 kbyte  3 comm. modules, 1 signal board, 8 signal modules	retentive data area in total (incl. times, counters, flags), max.	10 kbyte
Address area  I/O address area, overall  Process image  Inputs, adjustable  Outputs, adjustable  Hardware configuration  Number of modules per system, max.  1024 bytes for inputs / 1024 bytes for outputs  1024 bytes for inputs / 1024 bytes for outputs  1024 bytes for inputs / 1024 bytes for outputs  1024 bytes for inputs / 1024 bytes for outputs  1 kbyte  3 comm. modules, 1 signal board, 8 signal modules	Flag	
I/O address area, overall  I/O address area, overall  Process image  Inputs, adjustable  Outputs, adjustable  1 kbyte  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		8 kbyte; Size of bit memory address area
I/O address area, overall  Process image  Inputs, adjustable  Outputs, adjustable  Outputs, adjustable  Hardware configuration  Number of modules per system, max.  1024 bytes for inputs / 1024 bytes for outputs  1 kbyte  1 kbyte  3 comm. modules, 1 signal board, 8 signal modules	Address area	
I/O address area, overall  Process image  Inputs, adjustable  Outputs, adjustable  Outputs, adjustable  Hardware configuration  Number of modules per system, max.  1024 bytes for inputs / 1024 bytes for outputs  1 kbyte  1 kbyte  3 comm. modules, 1 signal board, 8 signal modules	I/O address area	
Process image Inputs, adjustable Outputs, adjustable 1 kbyte 1 kbyte Hardware configuration Number of modules per system, max. 3 comm. modules, 1 signal board, 8 signal modules		1024 bytes for inputs / 1024 bytes for outputs
Inputs, adjustable  Outputs, adjustable  1 kbyte  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		
Outputs, adjustable  1 kbyte  Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		1 kbyte
Hardware configuration  Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		
Number of modules per system, max.  3 comm. modules, 1 signal board, 8 signal modules		
		3 comm. modules, 1 signal board, 8 signal modules
		, , , , , , , , , , , , , , , , , , , ,
Clock	<u> </u>	

Hardware clock (real-time clock)	Yes
Deviation per day, max.	60 s/month at 25 °C
Backup time	480 h ; Typical
Digital inputs	
Number of digital inputs	14 ; Integrated
of which inputs usable for technological functions	6; HSC (High Speed Counting)
integrated channels (DI)	14
m/p-reading	Yes
Number of simultaneously controllable inputs	
all mounting positions	
up to 40 °C, max.	14
Input voltage	
Rated value, DC	24 V
for signal "0"	5 V DC at 1 mA
for signal "1"	15 VDC at 2.5 mA
Input current	
for signal "1", typ.	1 mA
Input delay (for rated value of input voltage)	
for standard inputs	
parameterizable	0.2 ms, 0.4 ms, 0.8 ms, 1.6 ms, 3.2 ms, 6.4 ms and 12.8 ms, selectable in groups of four
at "0" to "1", min.	0.2 ms
at "0" to "1", max.	12.8 ms
for interrupt inputs	
parameterizable	
	Yes
for counter/technological functions	Yes
•	Yes; Single phase: 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz
for counter/technological functions	Yes ; Single phase : 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80
for counter/technological functions parameterizable	Yes ; Single phase : 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80
for counter/technological functions  parameterizable  Cable length	Yes ; Single phase : 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz
for counter/technological functions parameterizable  Cable length Cable length, shielded, max.	Yes; Single phase: 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz  500 m; 50 m for technological functions
for counter/technological functions parameterizable  Cable length Cable length, shielded, max.  Cable length unshielded, max.	Yes; Single phase: 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz  500 m; 50 m for technological functions
for counter/technological functions parameterizable  Cable length Cable length, shielded, max. Cable length unshielded, max.  Digital outputs	Yes; Single phase: 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz  500 m; 50 m for technological functions  300 m; For technological functions: No
for counter/technological functions  parameterizable  Cable length  Cable length, shielded, max.  Cable length unshielded, max.  Digital outputs  Number of digital outputs	Yes; Single phase: 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz  500 m; 50 m for technological functions  300 m; For technological functions: No
for counter/technological functions  parameterizable  Cable length  Cable length, shielded, max.  Cable length unshielded, max.  Digital outputs  Number of digital outputs  integrated channels (DO)	Yes; Single phase: 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz  500 m; 50 m for technological functions  300 m; For technological functions: No  10; Relays  10
for counter/technological functions  parameterizable  Cable length  Cable length, shielded, max.  Cable length unshielded, max.  Digital outputs  Number of digital outputs  integrated channels (DO)  Short-circuit protection	Yes; Single phase: 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz  500 m; 50 m for technological functions  300 m; For technological functions: No  10; Relays  10
for counter/technological functions parameterizable  Cable length Cable length, shielded, max.  Cable length unshielded, max.  Digital outputs Number of digital outputs integrated channels (DO) Short-circuit protection Switching capacity of the outputs	Yes; Single phase: 3 at 100 kHz & 3 at 30 kHz, differential: 3 at 80 kHz & 3 at 30 kHz  500 m; 50 m for technological functions 300 m; For technological functions: No  10; Relays 10 No; to be provided externally

"0" to "1", max.	10 ms ; max.
"1" to "0", max.	10 ms; max.
Switching frequency	
of the pulse outputs, with resistive load, max.	1 Hz
Relay outputs	
Max. number of relay outputs, integrated	10
Number of relay outputs	10
Number of operating cycles, max.	mechanically 10 million, at rated load voltage 100,000
Cable length	
Cable length, shielded, max.	500 m
Cable length unshielded, max.	150 m
Analog inputs	
integrated channels (AI)	2;0 to 10 V
Number of analog inputs	2
Input ranges	
Voltage	Yes
Input ranges (rated values), voltages	
0 to +10 V	Yes
Input resistance (0 to 10 V)	≥100k ohms
Cable length	
Cable length, shielded, max.	100 m; twisted and shielded
Analog outputs	
Analog outputs  Number of analog outputs	0
	0
Number of analog outputs	0
Number of analog outputs  Analog value generation	0 10 bit
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel	
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.	10 bit
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable	10 bit Yes
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Conversion time (per channel)	10 bit Yes
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Conversion time (per channel)  Encoder	10 bit Yes
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Conversion time (per channel)  Encoder  Connectable encoders	10 bit Yes 625 μs
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Conversion time (per channel)  Encoder  Connectable encoders  2-wire sensor	10 bit Yes 625 μs
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Conversion time (per channel)  Encoder  Connectable encoders  2-wire sensor  1. Interface	10 bit Yes 625 μs  Yes
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Conversion time (per channel)  Encoder  Connectable encoders  2-wire sensor  1. Interface Interface type	10 bit Yes 625 μs  Yes  PROFINET
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Conversion time (per channel)  Encoder  Connectable encoders  2-wire sensor  1. Interface  Interface type  Physics	10 bit Yes 625 μs  Yes  PROFINET Ethernet
Number of analog outputs  Analog value generation  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Conversion time (per channel)  Encoder  Connectable encoders  2-wire sensor  1. Interface Interface type  Physics isolated	10 bit Yes 625 μs  Yes  PROFINET Ethernet Yes

Functionality	
PROFINET IO Controller	Yes
Communication functions	
S7 communication	
supported	Yes
as server	Yes
as client	Yes
Open IE communication	
TCP/IP	Yes
ISO-on-TCP (RFC1006)	Yes
UDP	Yes
Web server	
supported	Yes
User-defined websites	Yes
Test commissioning functions	
Status/control	
Status/control variable	Yes
Variables	Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters
Forcing	
Forcing	Yes
Diagnostic buffer	
present	Yes
Integrated Functions	
Number of counters	6
Counting frequency (counter) max.	100 kHz
Frequency meter	Yes
controlled positioning	Yes
PID controller	Yes
Number of alarm inputs	4
Galvanic isolation	
Galvanic isolation digital inputs	
Galvanic isolation digital inputs	500V AC for 1 minute
between the channels, in groups of	1
Galvanic isolation digital outputs	
Galvanic isolation digital outputs	Relays
between the channels	No -
between the channels, in groups of	2
Permissible potential difference	

between different circuits	500 V DC between 24 V DC and 5 V DC
EMC	
Interference immunity against discharge of static electricity	
Interference immunity against discharge of static electricity acc. to IEC 61000-4-2	Yes
Test voltage at air discharge	8 kV
Test voltage at contact discharge	6 kV
Interference immunity to cable-borne interference	
on the supply lines acc. to IEC 61000-4-4	Yes
Interference immunity on signal cables acc. to IEC 61000-4-4	Yes
Interference immunity against voltage surge	
on the supply lines acc. to IEC 61000-4-5	Yes
Interference immunity against conducted variable disturband	e induced by high-frequency fields
Interference immunity against high-frequency radiation acc. to IEC 61000-4-6	Yes
Emission of radio interference acc. to EN 55 011	
Emission of radio interference acc. to EN 55 011 (limit class A)	Yes ; Group 1
Emission of radio interference acc. to EN 55 011 (limit class B)	Yes ; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011
Degree and class of protection	
IP20	Yes
Standards, approvals, certificates	
Standards, approvals, certificates  CE mark	Yes
	Yes Yes
CE mark	
CE mark CSA approval	Yes
CE mark  CSA approval  UL approval	Yes Yes
CE mark  CSA approval  UL approval  cULus	Yes Yes Yes
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)	Yes Yes Yes Yes
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval	Yes Yes Yes Yes
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval  Marine approval	Yes Yes Yes Yes Yes Yes
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval  Marine approval  Marine approval	Yes Yes Yes Yes Yes Yes
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval  Marine approval  Marine approval  Ambient conditions	Yes Yes Yes Yes Yes Yes
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval  Marine approval  Marine approval  Ambient conditions  Operating temperature	Yes Yes Yes Yes Yes Yes Yes
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval  Marine approval  Marine approval  Ambient conditions  Operating temperature  min.	Yes Yes Yes Yes Yes Yes -20 °C
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval  Marine approval  Marine approval  Ambient conditions  Operating temperature  min.  max.	Yes Yes Yes Yes Yes Yes  Yes  -20 °C 60 °C
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval  Marine approval  Marine approval  Ambient conditions  Operating temperature  min.  max.  horizontal installation, min.	Yes Yes Yes Yes Yes Yes  Yes  -20 °C  60 °C  -20 °C
CE mark  CSA approval  UL approval  cULus  RCM (former C-TICK)  FM approval  Marine approval  Marine approval  Ambient conditions  Operating temperature  min.  max.  horizontal installation, min.  horizontal installation, max.	Yes Yes Yes Yes Yes Yes  Yes  -20 °C 60 °C -20 °C 60 °C

min.	-40 °C
max.	70 °C
Air pressure	
Operation, min.	795 hPa
Operation, max.	1080 hPa
Storage/transport, min.	660 hPa
Storage/transport, max.	1080 hPa
Relative humidity	
Operation, max.	95 % ; no condensation
Vibrations	
Vibrations	2G wall mounting, 1G DIN rail
Operation, tested according to IEC 60068-2-6	Yes
Shock test	
tested according to IEC 60068-2-27	Yes ; IEC 68, Part 2-27 half-sine: strength of the shock 15 g (peak value), duration 11 ms
Climatic and mechanical conditions for storage and transport	
Climatic conditions for storage and transport	
Free fall	
Drop height, max. (in packaging)	0.3 m ; five times, in dispatch package
Temperature	
permissible temperature range	-40 °C to +70 °C
Relative humidity	
permissible range (without condensation) at 25 °C	95 %
Mechanical and climatic conditions during operation	
Climatic conditions in operation	
Temperature	
min.	-20 °C
max.	60 °C
Air pressure acc. to IEC 60068-2-13	
permissible air pressure	1080 to 795 hPa
permissible operating height	-1000 to 2000 m
Pollutant concentrations	
SO2 at RH < 60% without condensation	S02: < 0.5 ppm; H2S: < 0.1 ppm; RH < 60% condensation-free
Configuration	
Programming	
Programming language	
LAD	Yes

SCL	Yes
Cycle time monitoring	
adjustable	Yes
Dimensions	
Width	110 mm
Height	100 mm
Depth	75 mm
Weights	
Weight, approx.	455 g
Status	Aug 6, 2014