Hex Schmitt trigger BU4584B / BU4584BFV

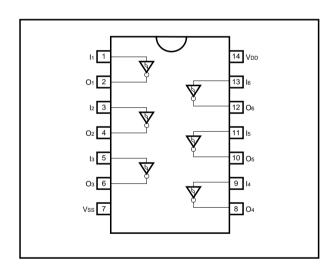
The BU4584B, BU4584BF, and BU4584BFV are inverter-type Schmitt trigger circuits, with six circuits mounted on a single chip. These are ideal when enhanced noise immunity is required, and when wave form rectification circuits with slow rise or fall input times are involved.

Features

- 1) Low power dissipation.
- 2) Wide range of operating power supply voltage.
- 3) High input impedance.

- 4) High fan-out.
- 5) Direct drive of 2 L-TTL inputs and LS-TTL input.

Block diagram



● Absolute maximum ratings (Vss = 0v, Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	V _{DD}	− 0.3 ~ + 18	V
Power dissipation	Pd	1000 (DIP), 450 (SOP), 350 (SSOP)	mW
Operating temperature	Topr	- 40 ~ + 85	°C
Storage temperature	Tstg	− 55 ~ + 150	°C
Input voltage	Vin	- 0.3 ~ V _{DD} + 0.3	V

Electrical characteristics

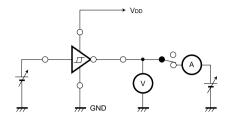
DC characteristics (unless otherwise noted, Vss = 0V, Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	V _{DD} (V)	Conditions	Measurement circuit
Input high level voltage	ViH	3.5	_	_	V	5	_	Fig.1
		7.0	_	_		10		
		11.0	_	_		15		
Input low level voltage	VIL	_	_	1.5	V	5	_	
		_	_	3.0		10		
		_	_	4.0		15		
Input high level current	Ін	_	_	0.3	μΑ	15	V _{IH} = 15V	
Input low level current	lı∟	_	_	- 0.3	μΑ	15	VIL = 0V	
		4.95	_	_		5	Io = 0mA	
Output high level voltage	Vон	9.95	_	_	V	10		
		14.95	_	_		15		
	Vol	_	_	0.05	V	5	Io = 0mA	
Output low level voltage		_	_	0.05		10		
		_	_	0.05		15		
Output high level current	Іон	- 0.44	_	_	mA	5	Vон = 4.6V	
		- 1.1	_	_		10	Vон = 9.5V	
		- 3.0	_	_		15	Vон = 13.5V	
Output low level current	loL	0.44	_	_	mA	5	Vol = 0.4V	
		1.1	_	_		10	Vol = 0.5V	
		3.0	_	_		15	Vol = 1.5V	
Static current consumption	ldd	_	_	1	μА	5	_	_
		_	_	2		10		
		_	_	4		5		
Hysteresis voltage	Vн	0.15	_	0.6	V	5	_	Fig.1
		0.25	_	1.0		10		
		0.40	_	1.5		15		

Switching characteristics (unless otherwise noted, Ta = 25°C, Vss = 0 V, CL =50pF)

Parameter	Symbol	Min.	Тур.	Max.	Unit	V _{DD} (V)	Conditions	Measurement circuit
Output rise time		_	100	_		5		
	tтьн	_	50	_	ns	10	_	Fig.2
		_	40	_		15		
Output fall time	tтн∟	_	100	_	ns	5	_	Fig.2
			50	_		10		
		_	40	_		15		
Propagation delay time, "L" to "H"	tрын	_	125	_	ns	5	_	Fig.2
		_	60	_		10		
		_	50	_		15		
Propagation delay time, "H" to "L"	tрнL	_	125	_	ns	5	_	Fig.2
		_	60	_		10		
		_	50	_		15		

Measurement circuits



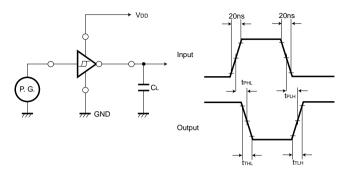


Fig.1 DC characteristics

Fig.2 Switching characteristics

•Electrical characteristic curve

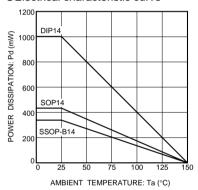
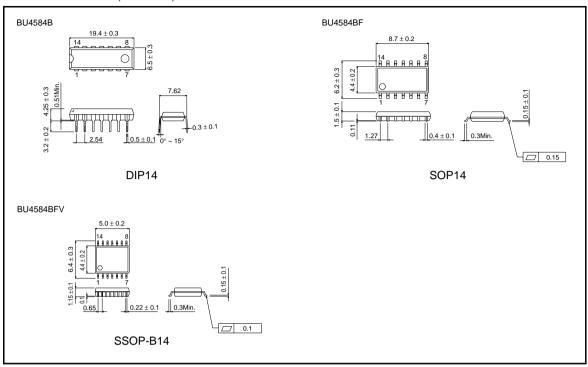


Fig.3 Power dissipation vs. Ta

External dimensions (Units: mm)



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