

Micropower µP Reset with Watchdog Timer

General Description

The AAT351x PowerManager products are members of Skyworks' Total Power Management IC (TPMIC™) product family. This family of microprocessor reset circuits provides the ultimate in versatility, allowing system designers full customization of the µP monitor and reset function without any additional components. The AAT351x family offers several combinations of threshold voltage, watchdog timeout period, reset active period, and output drive configurations, which are all factory-programmed options. All devices are available in 32 reset threshold voltages from 2.6V up to 5V, with three watchdog timeout periods from 6.3ms to 1600ms and three reset timeouts from 1ms up to 140ms. Available output configurations are active low push-pull, active low open drain, active low bi-directional, and active high push-pull.

The AAT351x family is designed to ignore fast negative transients on V_{DD} and to ensure that reset outputs remain valid down to 1V.

The AAT351x family is available in the Pb-free, space-saving 5-pin SOT23 surface mount package and is specified over the -40 to +85°C temperature range.

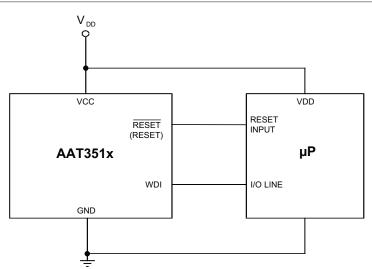
Features

- Tight Voltage Tolerance: ±1.5%
- Low Quiescent Current: 5μA
- Guaranteed Reset Valid Down to 1V
- 32 Voltage Options from 2.6V to 5.0V
- Three Reset Active Period Options:
 - 1ms, 20ms, 140ms
- Three Watchdog Timeout Period Options:
- 6.3ms, 102ms, 1600ms
- Four Output Options:
 - Open Drain
 - Inverting
 - Non-Inverting
 - Bi-Directional
- Low Temperature Coefficient: 100ppm/°C
- 5-Pin SOT23 Package

Applications

- Critical μP and μC Supply Monitoring
- Embedded Control Systems
- Industrial Controllers
- Intelligent Instruments
- Notebook Computers
- Portable Electronics
- · Power-On Reset Circuits

Typical Application



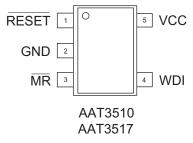
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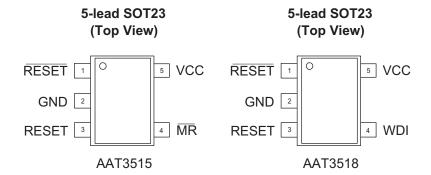
Pin Descriptions

Pin Number					
AAT3510 AAT3517	AAT3515	AAT3518	Symbol	Function	
1	1	1	RESET	AAT3510/15: RESET output goes low whenever V _{DD} falls below the reset threshold. CMOS push-pull output. AAT3517: RESET output goes low whenever V _{DD} falls below the reset threshold. Open drain output. Connect a pull-up resistor to any supply voltage up to 5.5V. AAT3518: RESET output goes low whenever VDD falls below the reset threshold. Open drain output. Connect a pull-up resistor to any supply voltage up to 5.5V.	
2	2	2	GND	Ground connection pin.	
N/A	3	3	RESET active-high output. This CMOS push-pull signal is the lo		
3	4	N/A	MR Manual reset input pin. Active low. Pull low to force a reset.		
4	N/A	4	WDI	Watchdog input pin. Triggers a reset if it remains in a steady state for the duration of the watchdog timer period.	
5	5	5	VCC	Input voltage pin.	

Pin Configuration

5-lead SOT23 (Top View)





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Absolute Maximum Ratings¹

 $T_A = 25$ °C, unless otherwise noted.

Symbol	Description	Max	Units	
V _{cc}	V _{cc} to GND	-0.3 to 6		
V _{MR} , V _{WDI}	V_{MR} , V_{WDI} \overline{MR} , WDI to GND		V	
V	RESET to GND (Push-Pull or Bidirectional Output)	$-0.3 \text{ to V}_{cc} + 0.3$	-	
V_{RESET}	RESET to GND (Open Drain Output)	-0.3 to 6		
I _{VCC} , I _{MR} , I _{WDI}	Maximum Continuous Input Current	20	mA	
I_{RESET}	RESET/RESET Output Current	20	IIIA	
dV _{cc} /dt	Rate of Rise of V _{CC}	100	V/µs	
T ₁	Operating Junction Temperature Range (-40°C to +150°C)	150	°C	

Thermal Information²

Symbol	Description	Value	Units
Θ_{JA}	Maximum Thermal Resistance	190	°C/W
P _D	Maximum Power Dissipation	526	mW

^{1.} Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied.

^{2.} Mounted on an FR4 board.

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Electrical Characteristics

 V_{CC} = 2.5V to 5.5V, T_A = -40°C to +85°C¹, unless otherwise noted. Typical values are at T_A = 25°C.

Symbol	Description	Conditions		Min	Тур	Max	Units	
V_{cc}	Operating Voltage Range	$T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$		1		5.5	V	
		AAT3510/7/8: MR & WDI	$V_{CC} = 3.6V$		5	15		
_	Cupply Current	Unconnected	$V_{CC} = 5.5V$		6	18] ,,,	
I_{CC}	Supply Current	AAT3515:	$V_{CC} = 3.6V$			15	μΑ	
		MR Unconnected	$V_{CC} = 5.5V$			18		
	Reset Threshold Voltage	$T_A = 25$ °C	T. = 25°C			V _{THNOM} +	V	
V_{TH}		.A 23 C	1 _A = 25 C		V _{TH} 1.5%			
- 111		$T_A = -40 \text{ to } +85^{\circ}\text{C}$		V _{THNOM} V _{THNOM} -		V _{THNOM} + 2.5%		
T _c	Reference Voltage Temperature Coefficient	-40°C < T _A < +85°C			±40		ppm/°C	
+	Reset Active Timeout Period	AAT351xIGV-xx-B-x-T1		20 28 40		40	ms	
t _{RP}	Reset Active Timeout Period	AAT351xIGV-xx-C-x-T1		140	200	280		
T_{RD}	V_{CC} to RESET Delay V_{CC} Falling at 1mV/ μ s			40		μs		
Push/Pul	I RESET Output (AAT3510, 3515	, 3518)						
	RESET Low Output Voltage	$V_{CC} \ge 1.0V$, $I_{SINK} = 50\mu A$						
Vol		$V_{CC} \ge 1.2V$, $I_{SINK} = 100\mu A$				0.3		
V OL		$V_{CC} \ge 2.7V$, $I_{SINK} = 1.2mA$						
		$V_{CC} \ge 4.5V$, $I_{SINK} = 3.2mA$				0.4		
V _{OH}	RESET High Output Voltage	$V_{CC} \ge 2.7V$, $I_{SOURCE} = 500\mu A$		0.8 x V _{cc}				
▼ OH	RESET Flight Output Voltage	$V_{CC} \ge 4.5V$, $I_{SOURCE} = 800\mu A$		V _{CC} - 1.5			V	
	RESET Low Output Voltage	$V_{CC} \ge 2.7V$, $I_{SINK} = 1.2mA$				0.3		
	RESET LOW Output Voltage	$V_{CC} \ge 4.5V$, $I_{SINK} = 3.2mA$				0.4		
V _{OH}		$V_{CC} \ge 1.8V$, $I_{SOURCE} = 150\mu A$		0.8 x V _{CC}			_	
	RESET High Output Voltage	$V_{CC} \ge 2.7V$, $I_{SOURCE} = 500\mu A$		-				
		$V_{CC} \ge 4.5V$, $I_{SOURCE} = 800\mu A$		V _{CC} - 1.5				

^{1.} Over-temperature limits are guaranteed by design, not production tested.

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Electrical Characteristics

 $V_{CC} = 2.5V$ to 5.5V, $T_A = -40$ °C to +85°C¹, unless otherwise noted. Typical values are at $T_A = 25$ °C.

Symbol	Description	Conditions	Min	Тур	Max	Units	
Open-Draii	n RESET Output (AAT3517, 3518)			,		
	DECET Law Output Valtage	$V_{CC} \ge 1.0V$, $I_{SINK} = 50\mu A$					
V_{OI}		$V_{CC} \ge 1.2V$, $I_{SINK} = 100\mu A$			0.3	V	
V OL	RESET Low Output Voltage	$V_{CC} \ge 2.7V$, $I_{SINK} = 1.2mA$				V	
		$V_{CC} \ge 4.5V$, $I_{SINK} = 3.2mA$			0.4		
$I_{D(OFF)}$	Reset Leakage Current				1.0	μΑ	
Watchdog	Input (AAT3510, 3517, 3518)						
+	Watchdog Timeout Period	AAT351xIGV-xx-x-B-T1	71	102	153	ms	
t_{WD}		AAT351xIGV-xx-x-C-T1 ²	1.12	1.6	2.4	S	
t _{wDI}	WDI Minimum Pulse Width	$V_{IL} = 0.3 \times V_{CC}, V_{IH} = 0.7 \times V_{CC}$		50		ns	
V _{IL}	WDI Input Thresholds		0.3 x V _{cc}				
V_{IH}	WDI Input Threshold ³				0.8 x V _{cc}	V	
т	WDI I I C II	WDI = V _{CC} , Time Average		120	160		
${ m I}_{ m WDI}$	WDI Input Current ⁴	$V_{WDI} = 0$, Time Average	-20	-15		μA	
Manual RE	SET Input (3515)						
V _{IL}	MR Input Threshold		0.3 x V _{cc}				
	MR Input Threshold				0.7 x V _{cc}	V	
	MR Input Pulse Width		1			μs	
V_{IH}	MR Glitch Rejection			100		ns	
	MR Internal Pull-Up Resistance	$T_A = 25$ °C	35	52	75	kΩ	
	MR to Reset Delay	$V_{CC} = 5V$		230		ns	

^{1.} Over-temperature limits are guaranteed by design, not production tested.

^{2.} Watchdog timeout period C is not available on AAT3518.
3. WDI is internally serviced within the watchdog period if WDI is left unconnected.

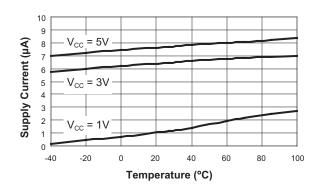
^{4.} The WDI input current is specified as the average input current when the WDI input is driven high or low. The WDI input is designed for a three-stated-output device with a 10µA maximum leakage current and capable of driving a maximum capacitive load of 200pF. The three-state device must be able to source and sink at least 200µA when

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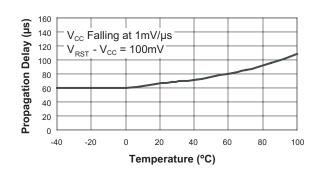
Typical Characteristics

Unless otherwise noted, $V_{IN} = 3V$, $T_A = 25$ °C.

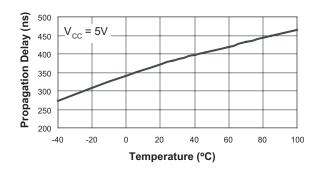
Supply Current vs. Temperature



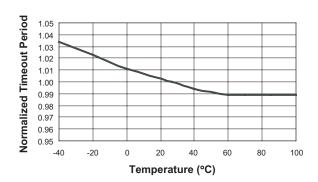
V_{cc} Falling to RESET Propagation Delay vs. Temperature



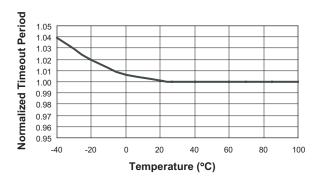
Manual Reset to RESET Propagation Delay vs. Temperature



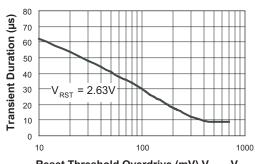
Normalized Reset Timeout Period vs. Temperature



Normalized Watchdog Timeout Period vs. Temperature



Maximum V_{cc} Transient Duration vs. Reset Threshold Overdrive



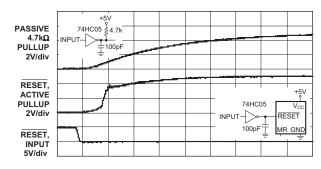
Reset Threshold Overdrive (mV) V_{RST} - V_{CC}

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Typical Characteristics

Unless otherwise noted, V_{IN} = 3V, T_A = 25°C.

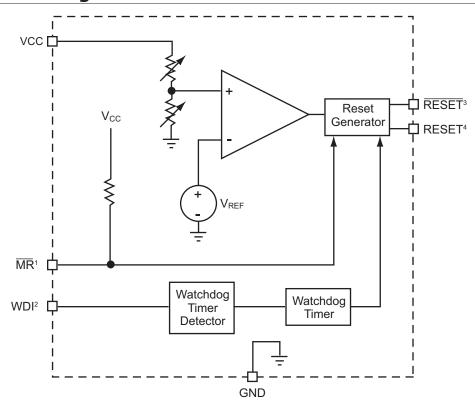
Bidirectional Pullup Characteristic



Time (200ns/div)

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Functional Block Diagram



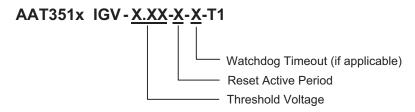
^{1.} MR pin available on AAT3510/5/7.

WDI pin avaliable on AAT3510/5/7/8.
 RESET pin available on AAT3510/5/7/8.

^{4.} RESET pin available on AAT3515/8.

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Factory-Trimmed Reset Thresholds¹ and Ordering Information



Reset Active Period

A: $T_{R(MIN)} = 1ms$ B: $T_{R(MIN)} = 20ms$ C: $T_{R(MIN)} = 140ms$

Watchdog Timeout Period

A: $T_{WD(NOM)} = 6.3ms$ B: $T_{WD(NOM)} = 102ms$ C: $T_{WD(NOM)} = 1600ms1$

VDD Threshold Voltage

2.63: $V_{TH(NOM)} = 2.63V$	$3.90: V_{TH(NOM)} = 3.9V$
$2.70: V_{TH(NOM)} = 2.7V$	$4.00: V_{TH(NOM)} = 4.0V$
$2.80: V_{TH(NOM)} = 2.8V$	4.10: $V_{TH(NOM)} = 4.1V$
$2.93: V_{TH(NOM)} = 2.93V$	4.20: $V_{TH(NOM)} = 4.2V$
$3.00: V_{TH(NOM)} = 3.0V$	4.38: $V_{TH(NOM)} = 4.3V$
$3.08: V_{TH(NOM)} = 3.08V$	4.40: $V_{TH(NOM)} = 4.38V$
3.20: $V_{TH(NOM)} = 3.2V$	4.50: $V_{TH(NOM)} = 4.5V$
3.30: $V_{TH(NOM)} = 3.3V$	4.63: $V_{TH(NOM)} = 4.63V$
3.40: $V_{TH(NOM)} = 3.4V$	$4.70: V_{TH(NOM)} = 4.7V$
3.50: $V_{TH(NOM)} = 3.5V$	4.80: $V_{TH(NOM)} = 4.8V$
3.60: $V_{TH(NOM)} = 3.6V$	4.90: $V_{TH(NOM)} = 4.9V$
3.70: $V_{TH(NOM)} = 3.7V$	5.00: $V_{TH(NOM)} = 5.0V$
3.80: $V_{TH(NOM)} = 3.8V$	

^{1.} Watchdog timeout period C is not available on AAT3518.

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Ordering Information

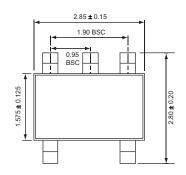
Package	Marking ¹	Part Number (Tape and Reel) ²
	HVXYY	AAT3510IGV-2.63-C-C-T1
	HUXYY	AAT3510IGV-2.93-C-C-T1
	HGXYY	AAT3510IGV-3.08-C-C-T1
	HHXYY	AAT3510IGV-4.38-C-C-T1
	HEXYY	AAT3515IGV-2.63-C-T1
	HIXYY	AAT3515IGV-2.93-B-T1
COT22 F	KLXYY	AAT3515IGV-2.93-C-T1
SOT23-5	JNXYY	AAT3515IGV-3.08-C-T1
	KMXYY	AAT3515IGV-4.38-C-T1
	MFXYY	AAT3515IGV-4.63-C-T1
	INXYY	AAT3517IGV-2.63-C-C-T1
	LZXYY	AAT3517IGV-2.8-B-C-T1
	IQXYY	AAT3517IGV-2.93-C-C-T1
	P9XYY	AAT3518IGV-3.00-B-B-T1

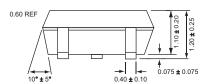


Skyworks GreenTM products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green*TM, document number SQ04-0074.

Package Information

SOT23-5







All dimensions in millimeters.

^{1.} XYY = date and assembly code.

^{2.} Sample stock is generally held on part numbers listed in BOLD.

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