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Kind regards,

Team Nexperia

# DATA SHEET

## **PDTA143T series**

PNP resistor-equipped transistors;  
R1 = 4.7 k $\Omega$ , R2 = open

Product data sheet  
Supersedes data of 2003 Sep 08

2004 Aug 04

# PNP resistor-equipped transistors; R1 = 4.7 k $\Omega$ , R2 = open

## PDTA143T series

### FEATURES

- Built-in bias resistors
- Simplified circuit design
- Reduction of component count
- Reduced pick and place costs.

### APPLICATIONS

- General purpose switching and amplification
- Inverter and interface circuits
- Circuit driver.

### QUICK REFERENCE DATA

| SYMBOL           | PARAMETER                 | TYP. | MAX. | UNIT       |
|------------------|---------------------------|------|------|------------|
| V <sub>CEO</sub> | collector-emitter voltage | –    | –50  | V          |
| I <sub>O</sub>   | output current (DC)       | –    | –100 | mA         |
| R1               | bias resistor             | 4.7  | –    | k $\Omega$ |
| R2               | open                      | –    | –    | –          |

### DESCRIPTION

PNP resistor-equipped transistor (see “Simplified outline, symbol and pinning” for package details).

### PRODUCT OVERVIEW

| TYPE NUMBER | PACKAGE       |        | MARKING CODE       | NPN COMPLEMENT |
|-------------|---------------|--------|--------------------|----------------|
|             | PHILIPS       | EIAJ   |                    |                |
| PDTA143TE   | SOT416        | SC-75  | 39                 | PDTC143TE      |
| PDTA143TEF  | SOT490        | SC-89  | 10                 | PDTC143TEF     |
| PDTA143TK   | SOT346        | SC-59  | 45                 | PDTC143TK      |
| PDTA143TM   | SOT883        | SC-101 | E6                 | PDTC143TM      |
| PDTA143TS   | SOT54 (TO-92) | SC-43  | TA143T             | PDTC143TS      |
| PDTA143TT   | SOT23         | –      | *42 <sup>(1)</sup> | PDTC143TT      |
| PDTA143TU   | SOT323        | SC-70  | *45 <sup>(1)</sup> | PDTC143TU      |

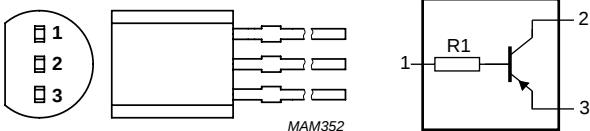
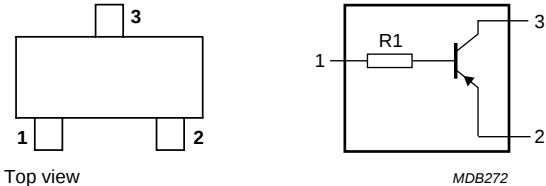
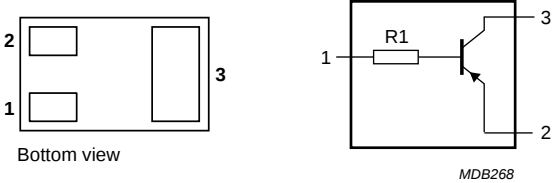
### Note

- \* = p: Made in Hong Kong.  
\* = t: Made in Malaysia.  
\* = W: Made in China.

PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series

SIMPLIFIED OUTLINE, SYMBOL AND PINNING

| TYPE NUMBER                                                    | SIMPLIFIED OUTLINE AND SYMBOL                                                        | PINNING     |                              |
|----------------------------------------------------------------|--------------------------------------------------------------------------------------|-------------|------------------------------|
|                                                                |                                                                                      | PIN         | DESCRIPTION                  |
| PDTA143TS                                                      |    | 1<br>2<br>3 | base<br>collector<br>emitter |
| PDTA143TE<br>PDTA143TEF<br>PDTA143TK<br>PDTA143TT<br>PDTA143TU |   | 1<br>2<br>3 | base<br>emitter<br>collector |
| PDTA143TM                                                      |  | 1<br>2<br>3 | base<br>emitter<br>collector |

# PNP resistor-equipped transistors; R1 = 4.7 k $\Omega$ , R2 = open

## PDTA143T series

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL           | PARAMETER                     | CONDITIONS               | MIN. | MAX. | UNIT |
|------------------|-------------------------------|--------------------------|------|------|------|
| V <sub>CBO</sub> | collector-base voltage        | open emitter             | –    | –50  | V    |
| V <sub>CEO</sub> | collector-emitter voltage     | open base                | –    | –50  | V    |
| V <sub>EBO</sub> | emitter-base voltage          | open collector           | –    | –5   | V    |
| I <sub>O</sub>   | output current (DC)           |                          | –    | –100 | mA   |
| I <sub>CM</sub>  | peak collector current        |                          | –    | –100 | mA   |
| P <sub>tot</sub> | total power dissipation       | T <sub>amb</sub> ≤ 25 °C |      |      |      |
|                  | SOT23                         | note 1                   | –    | 250  | mW   |
|                  | SOT54                         | note 1                   | –    | 500  | mW   |
|                  | SOT323                        | note 1                   | –    | 200  | mW   |
|                  | SOT346                        | note 1                   | –    | 250  | mW   |
|                  | SOT416                        | note 1                   | –    | 150  | mW   |
|                  | SOT490                        | notes 1 and 2            | –    | 250  | mW   |
|                  | SOT883                        | notes 2 and 3            | –    | 250  | mW   |
| T <sub>stg</sub> | storage temperature           |                          | –65  | +150 | °C   |
| T <sub>j</sub>   | junction temperature          |                          | –    | 150  | °C   |
| T <sub>amb</sub> | operating ambient temperature |                          | –65  | +150 | °C   |

### Notes

1. Refer to standard mounting conditions.
2. Reflow soldering is the only recommended soldering method.
3. Refer to SOT883 standard mounting conditions; FR4 with 60  $\mu$ m copper strip line.

### THERMAL CHARACTERISTICS

| SYMBOL              | PARAMETER                                   | CONDITIONS    | VALUE | UNIT |
|---------------------|---------------------------------------------|---------------|-------|------|
| R <sub>th j-a</sub> | thermal resistance from junction to ambient | in free air   |       |      |
|                     | SOT23                                       | note 1        | 500   | K/W  |
|                     | SOT54                                       | note 1        | 250   | K/W  |
|                     | SOT323                                      | note 1        | 625   | K/W  |
|                     | SOT346                                      | note 1        | 500   | K/W  |
|                     | SOT416                                      | note 1        | 833   | K/W  |
|                     | SOT490                                      | note 1        | 500   | K/W  |
|                     | SOT883                                      | notes 2 and 3 | 500   | K/W  |

### Notes

1. Refer to standard mounting conditions.
2. Reflow soldering is the only recommended soldering method.
3. Refer to SOT883 standard mounting conditions; FR4 with 60  $\mu$ m copper strip line.

PNP resistor-equipped transistors;  
R1 = 4.7 k $\Omega$ , R2 = open

PDTA143T series

## CHARACTERISTICS

T<sub>amb</sub> = 25 °C unless otherwise specified.

| SYMBOL             | PARAMETER                            | CONDITIONS                                                              | MIN. | TYP. | MAX. | UNIT       |
|--------------------|--------------------------------------|-------------------------------------------------------------------------|------|------|------|------------|
| I <sub>CBO</sub>   | collector-base cut-off current       | V <sub>CB</sub> = -50 V; I <sub>E</sub> = 0                             | –    | –    | -100 | nA         |
| I <sub>CEO</sub>   | collector-emitter cut-off current    | V <sub>CE</sub> = -30 V; I <sub>B</sub> = 0                             | –    | –    | -1   | $\mu$ A    |
|                    |                                      | V <sub>CE</sub> = -30 V; I <sub>B</sub> = 0; T <sub>j</sub> = 150 °C    | –    | –    | -50  | $\mu$ A    |
| I <sub>EBO</sub>   | emitter-base cut-off current         | V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0                              | –    | –    | -100 | nA         |
| h <sub>FE</sub>    | DC current gain                      | V <sub>CE</sub> = -5 V; I <sub>C</sub> = -1 mA                          | 200  | –    | –    |            |
| V <sub>CEsat</sub> | collector-emitter saturation voltage | I <sub>C</sub> = -5 mA; I <sub>B</sub> = -0.25 mA                       | –    | –    | -150 | mV         |
| R1                 | input resistor                       |                                                                         | 3.3  | 4.7  | 6.1  | k $\Omega$ |
| C <sub>c</sub>     | collector capacitance                | I <sub>E</sub> = I <sub>e</sub> = 0; V <sub>CB</sub> = -10 V; f = 1 MHz | –    | –    | 3    | pF         |

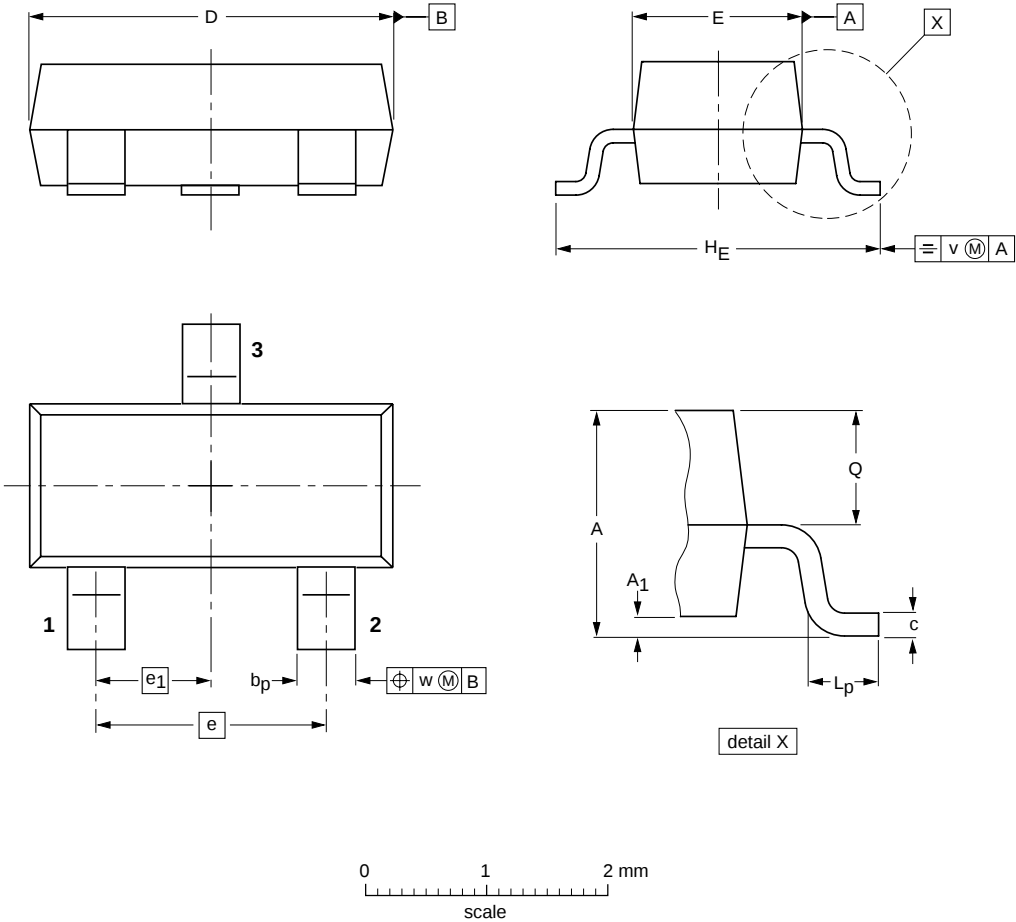
PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series

PACKAGE OUTLINES

Plastic surface-mounted package; 3 leads

SOT23



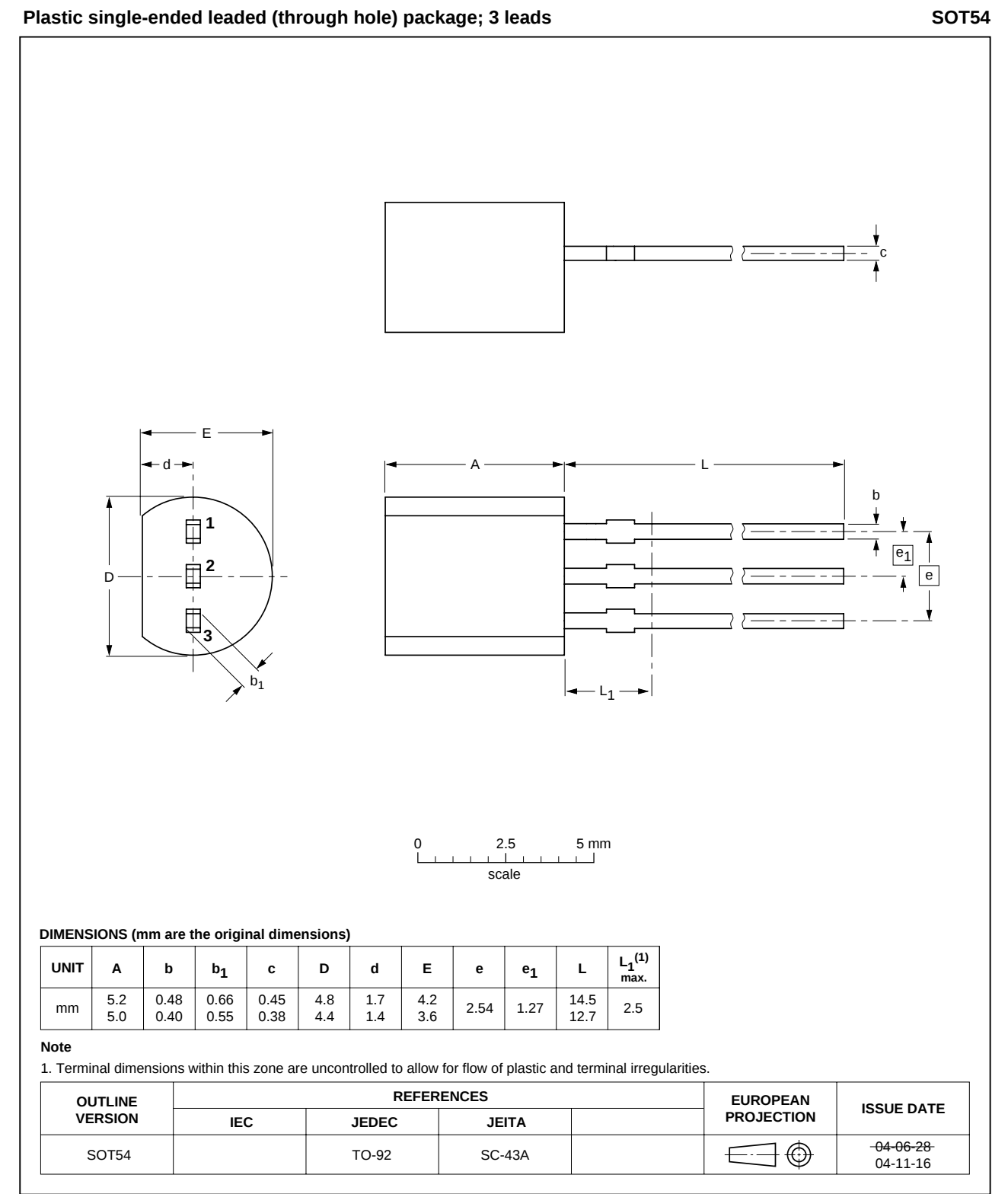
DIMENSIONS (mm are the original dimensions)

| UNIT | A          | A <sub>1</sub><br>max. | b <sub>p</sub> | c            | D          | E          | e   | e <sub>1</sub> | H <sub>E</sub> | L <sub>p</sub> | Q            | v   | w   |
|------|------------|------------------------|----------------|--------------|------------|------------|-----|----------------|----------------|----------------|--------------|-----|-----|
| mm   | 1.1<br>0.9 | 0.1                    | 0.48<br>0.38   | 0.15<br>0.09 | 3.0<br>2.8 | 1.4<br>1.2 | 1.9 | 0.95           | 2.5<br>2.1     | 0.45<br>0.15   | 0.55<br>0.45 | 0.2 | 0.1 |

| OUTLINE<br>VERSION | REFERENCES |          |       |  | EUROPEAN<br>PROJECTION | ISSUE DATE           |
|--------------------|------------|----------|-------|--|------------------------|----------------------|
|                    | IEC        | JEDEC    | JEITA |  |                        |                      |
| SOT23              |            | TO-236AB |       |  |                        | 04-11-04<br>06-03-16 |

PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series

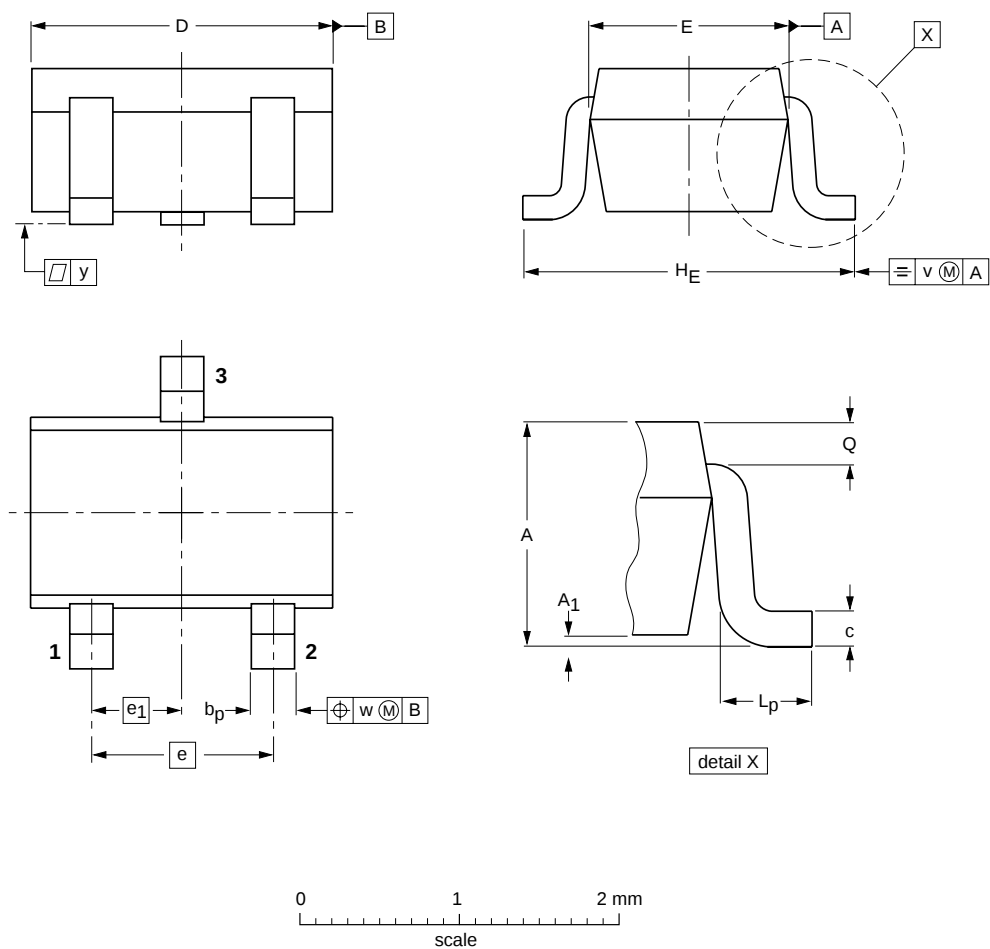


PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series

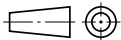
Plastic surface-mounted package; 3 leads

SOT323



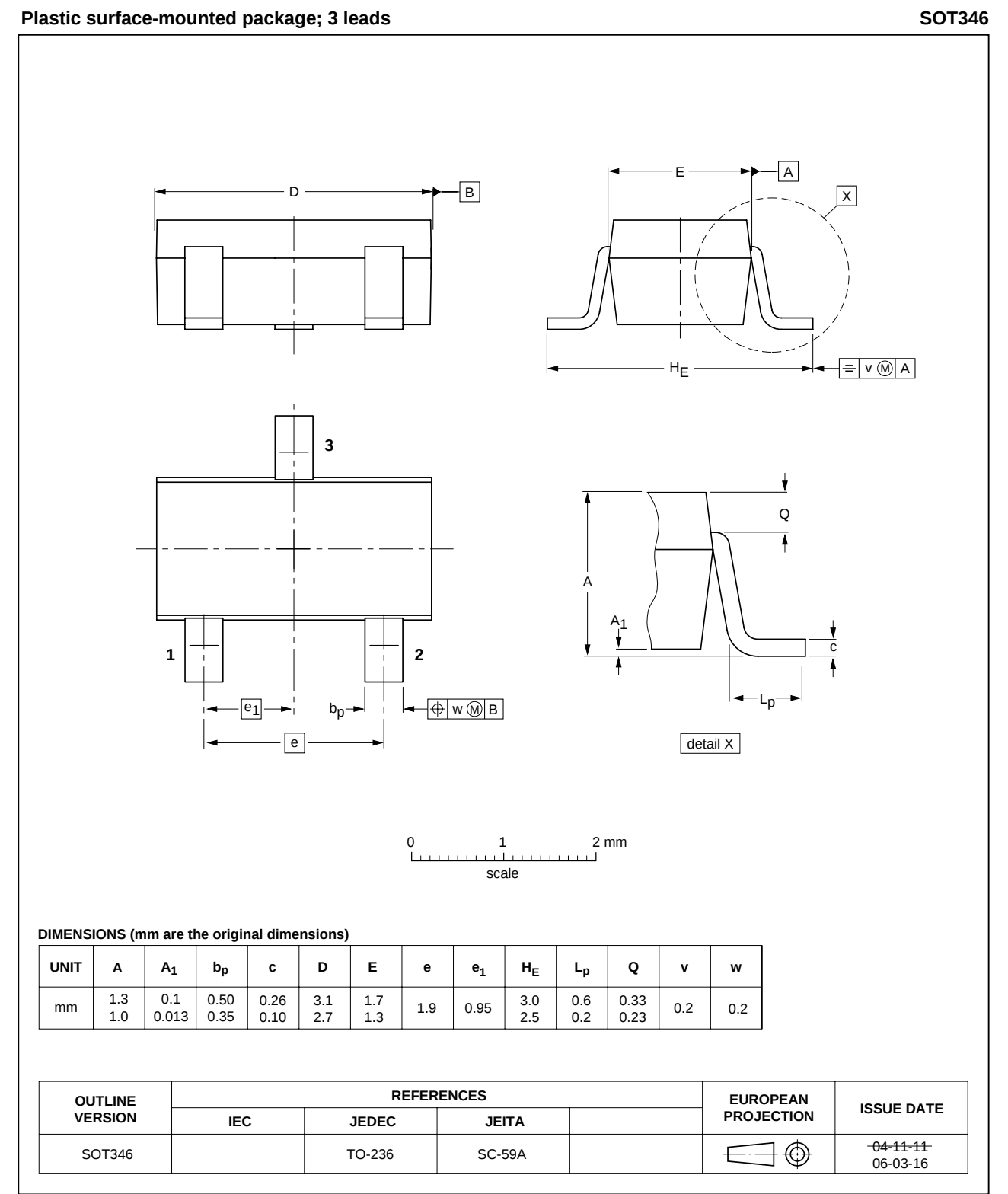
DIMENSIONS (mm are the original dimensions)

| UNIT | A          | A <sub>1</sub><br>max | b <sub>p</sub> | c            | D          | E            | e   | e <sub>1</sub> | H <sub>E</sub> | L <sub>p</sub> | Q            | v   | w   |
|------|------------|-----------------------|----------------|--------------|------------|--------------|-----|----------------|----------------|----------------|--------------|-----|-----|
| mm   | 1.1<br>0.8 | 0.1                   | 0.4<br>0.3     | 0.25<br>0.10 | 2.2<br>1.8 | 1.35<br>1.15 | 1.3 | 0.65           | 2.2<br>2.0     | 0.45<br>0.15   | 0.23<br>0.13 | 0.2 | 0.2 |

| OUTLINE<br>VERSION | REFERENCES |       |       |  | EUROPEAN<br>PROJECTION                                                                | ISSUE DATE                      |
|--------------------|------------|-------|-------|--|---------------------------------------------------------------------------------------|---------------------------------|
|                    | IEC        | JEDEC | JEITA |  |                                                                                       |                                 |
| SOT323             |            |       | SC-70 |  |  | <del>04-11-04</del><br>06-03-16 |

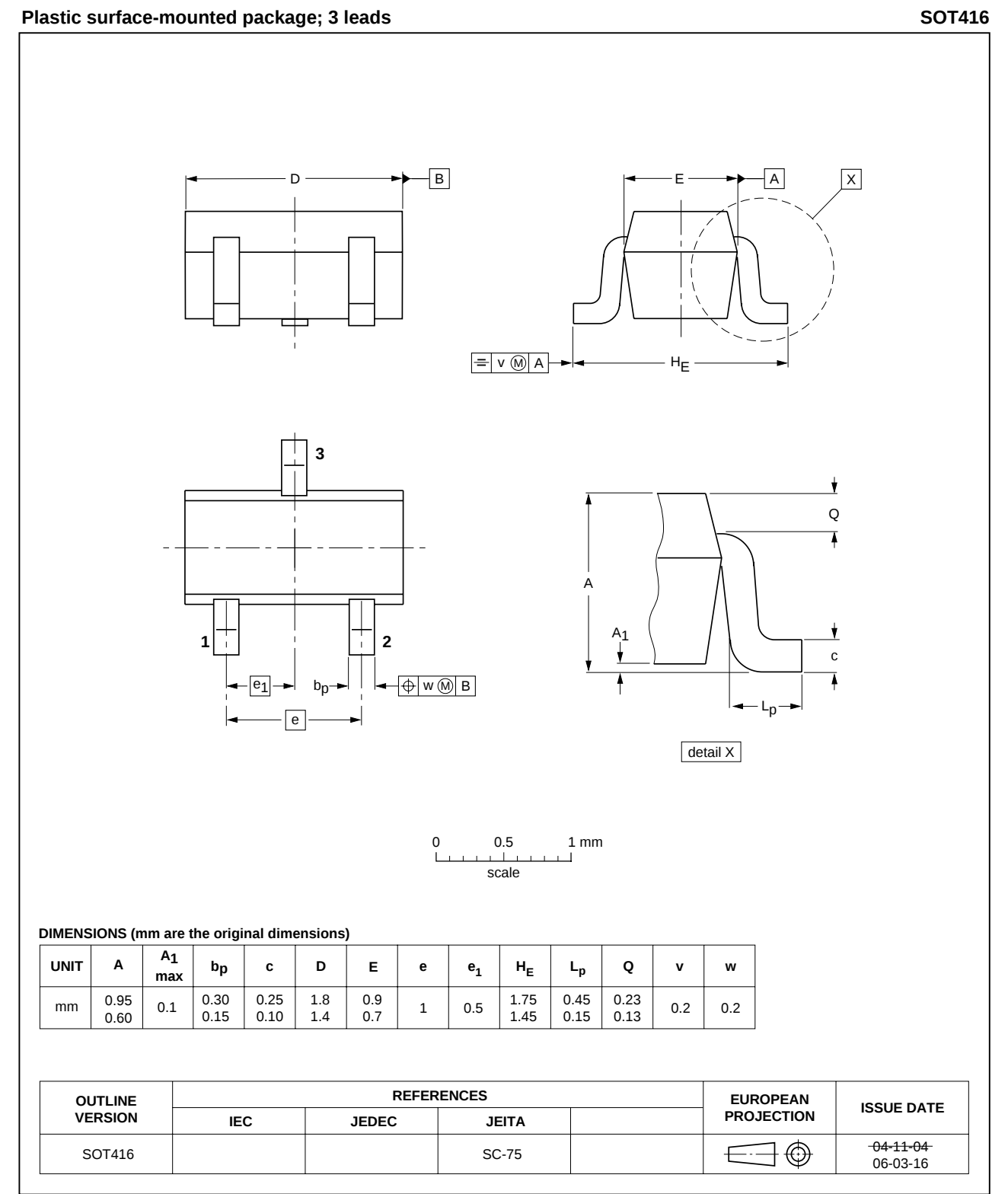
PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series



PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series

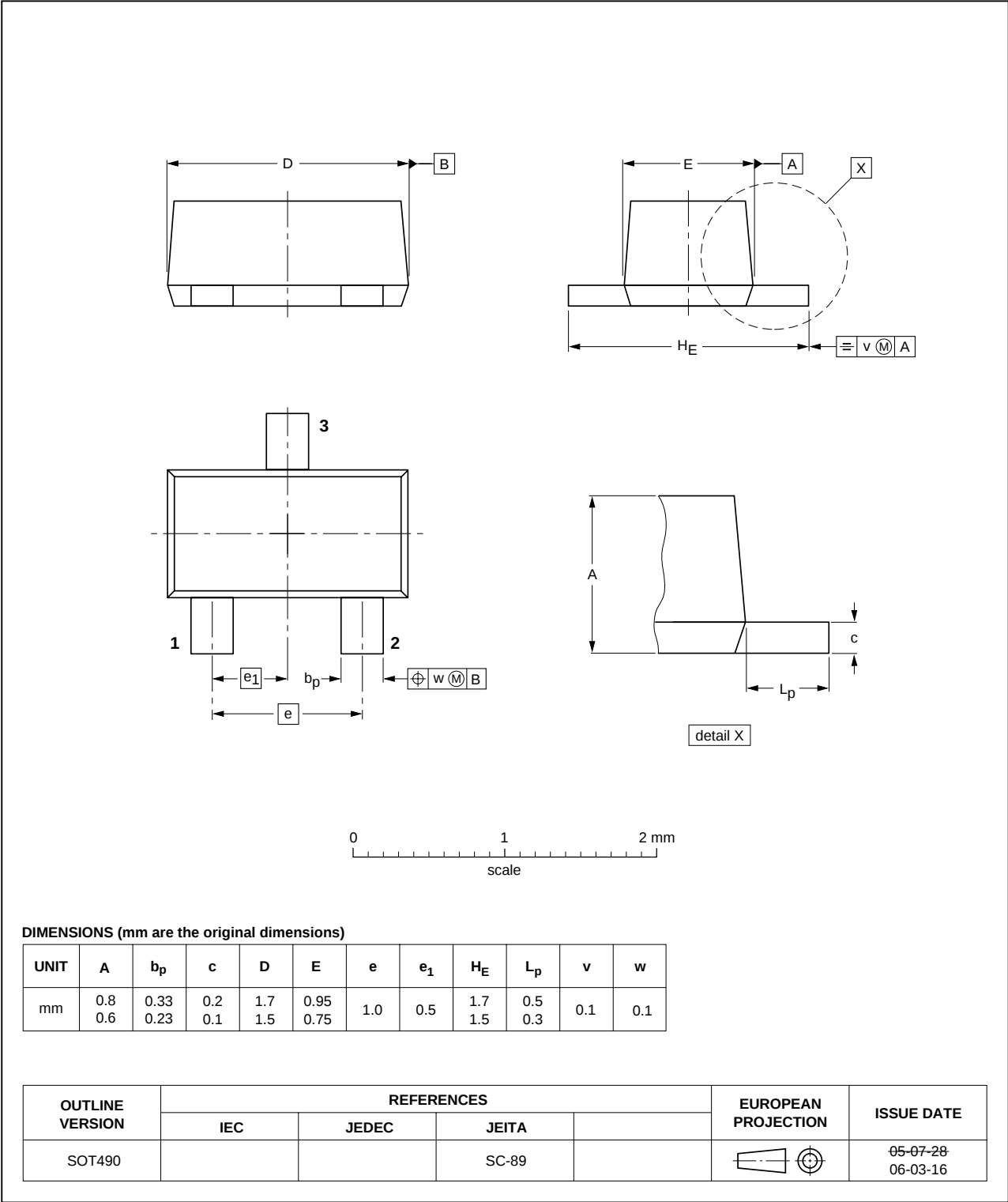


PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series

Plastic surface-mounted package; 3 leads

SOT490

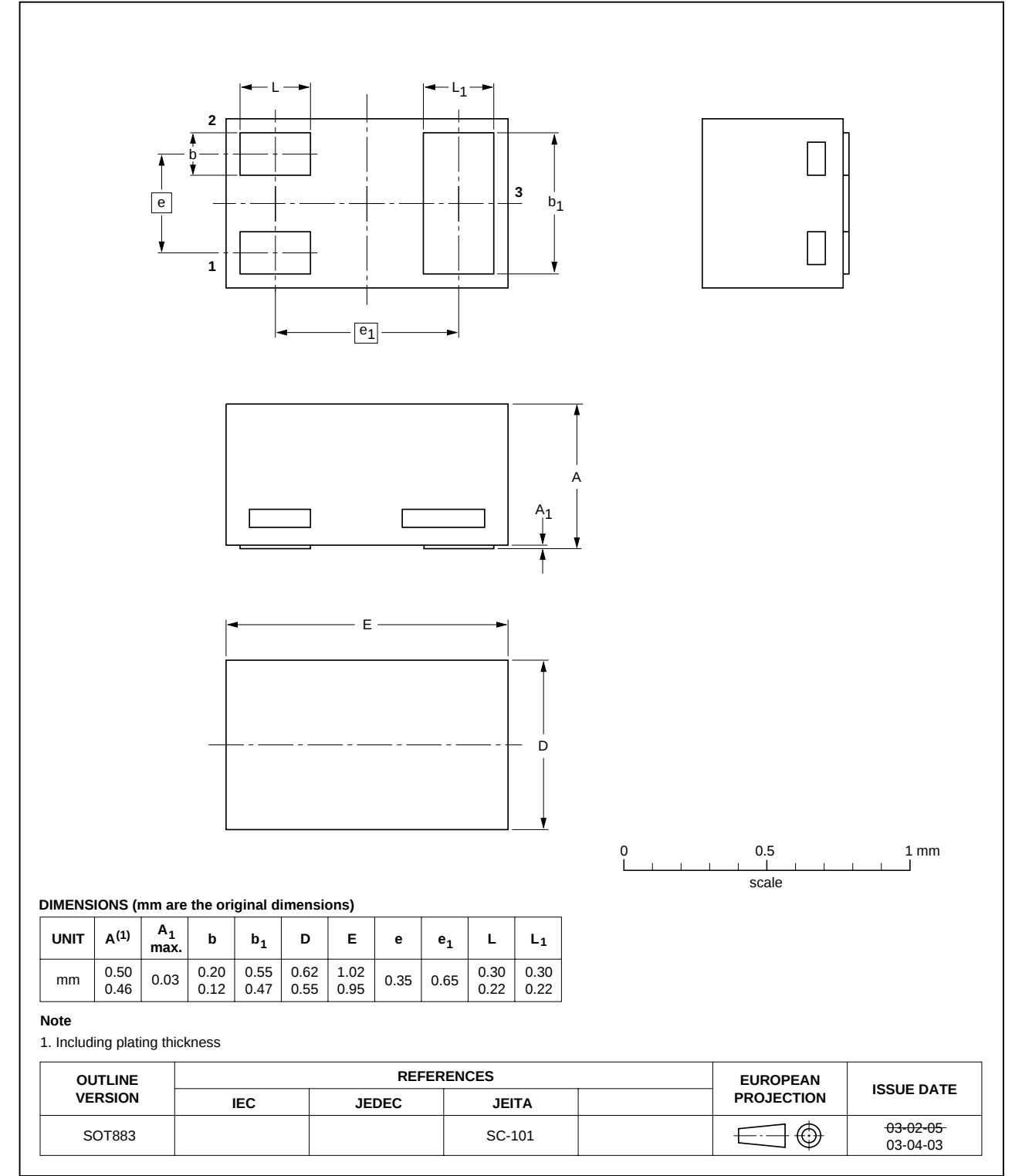


PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series

Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.5 mm

SOT883



PNP resistor-equipped transistors;  
R1 = 4.7 kΩ, R2 = open

PDTA143T series

## DATA SHEET STATUS

| DOCUMENT STATUS <sup>(1)</sup> | PRODUCT STATUS <sup>(2)</sup> | DEFINITION                                                                            |
|--------------------------------|-------------------------------|---------------------------------------------------------------------------------------|
| Objective data sheet           | Development                   | This document contains data from the objective specification for product development. |
| Preliminary data sheet         | Qualification                 | This document contains data from the preliminary specification.                       |
| Product data sheet             | Production                    | This document contains the product specification.                                     |

## Notes

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# ***NXP Semiconductors***

## **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

## **Contact information**

For additional information please visit: <http://www.nxp.com>

For sales offices addresses send e-mail to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

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