

Digital Attenuator 15.0 dB, 4-Bit, TTL Driver, DC-4.0 GHz

**MAADCC0006
V2**

Features

- Attenuation: 1 dB Steps to 15 dB
- Low DC Power Consumption
- Small Footprint, JEDEC Package
- Integral TTL Driver
- 50 Ohm Impedance
- Test Boards Available
- Tape and Reel Packaging Available
- Lead-Free CSP-1 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of AT90-0413

Description

M/A-COM's MAADCC0006 is a GaAs FET 4-Bit digital attenuator with integral driver. Step size is 1 dB providing a 15 dB attenuation range. This device is in an PQFN plastic surface mount package. The MAADCC0006 is suited for applications where accuracy, fast speed, low power consumption and low costs are required.

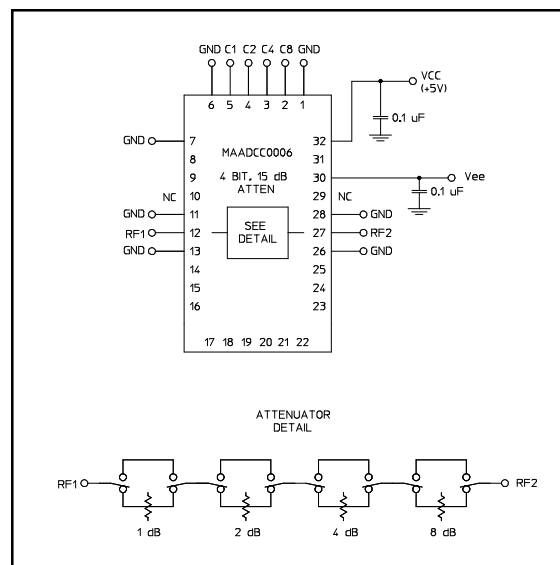
Ordering Information

Part Number	Package
MAADCC0006	Bulk Packaging
MAADCC0006TR	1000 piece reel
MAADCC0006-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

Note: Die quantity varies.

Schematic with Off-Chip Components



Pin Configuration²

Pin No.	Function	Pin No.	Function
1	GND	17	N/C
2	C8	18	N/C
3	C4	19	N/C
4	C2	20	N/C
5	C1	21	N/C
6	GND	22	N/C
7	GND	23	N/C
8	N/C	24	N/C
9	N/C	25	N/C
10	N/C ¹	26	GND
11	GND	27	RF2
12	RF1	28	GND
13	GND	29	N/C ¹
14	N/C	30	-Vee
15	N/C	31	N/C
16	N/C	32	+Vcc

1. Pins 10 & 29 must be isolated.

2. The exposed pad centered on the package bottom must be connected to RF and DC ground. (For PQFN Packages)

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

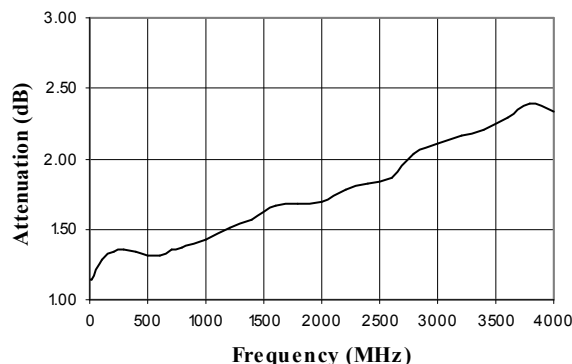
Truth Table (Digital Attenuator)

C8	C4	C2	C1	Attenuation
0	0	0	0	Loss, Reference
0	0	0	1	1.0 dB
0	0	1	0	2.0 dB
0	1	0	0	4.0 dB
1	0	0	0	8.0 dB
1	1	1	1	15.0 dB

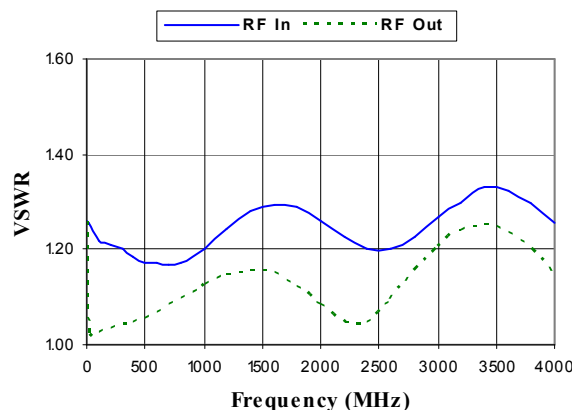
0 = TTL Low. 1 = TTL High

Typical Performance Curves

Insertion Loss



VSWR @ Insertion Loss

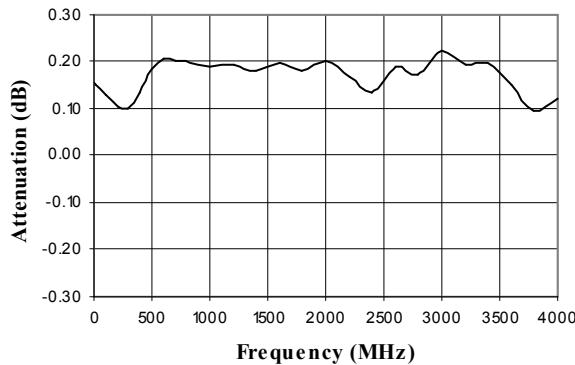


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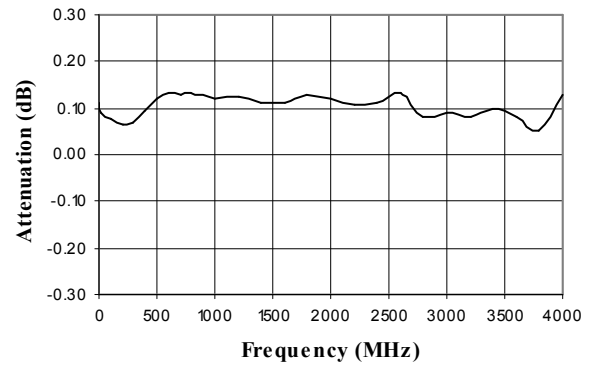
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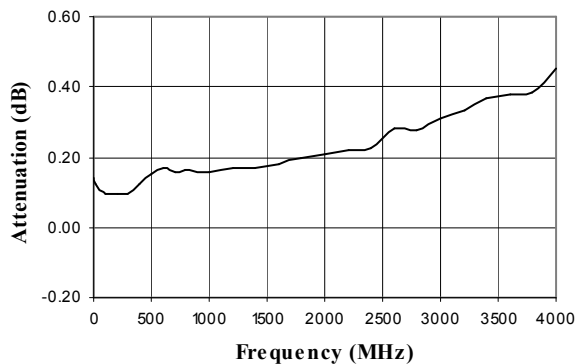
Attenuation Error, 1 dB Bit



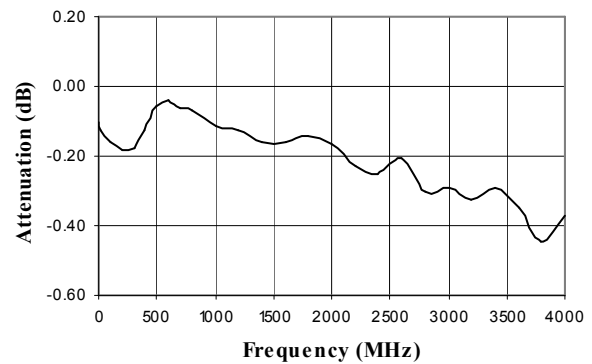
Attenuation Error, 2 dB Bit



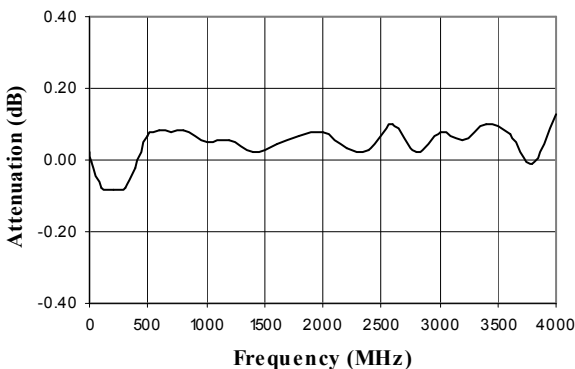
Attenuation Error, 4 dB Bit



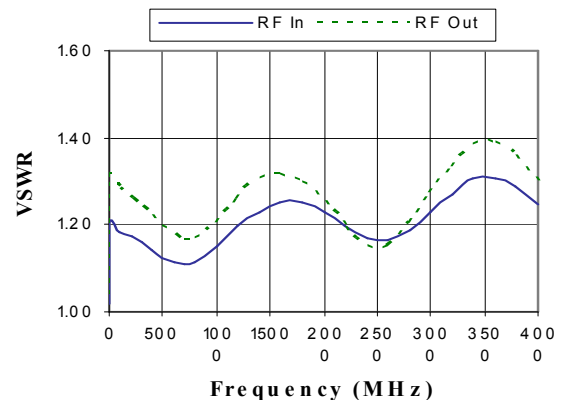
Attenuation Error, 8 dB Bit



Attenuation Error, Max. Attenuation



VSWR, 1 dB Bit

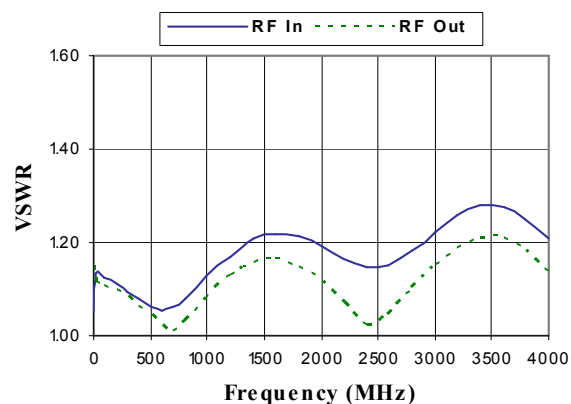


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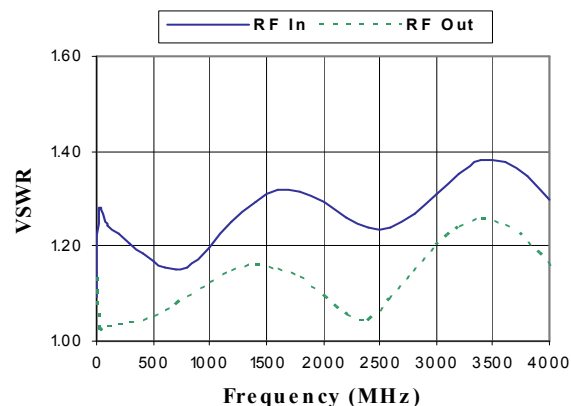
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Typical Performance Curves

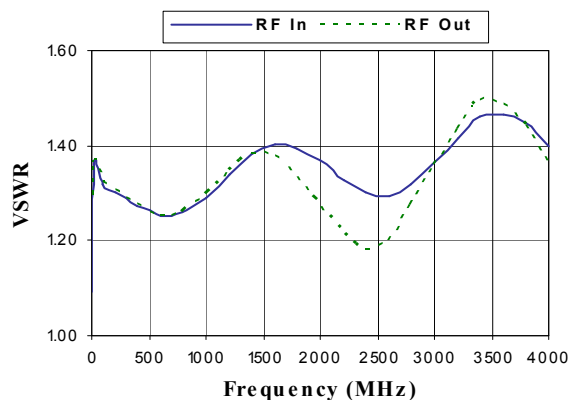
VSWR, 2 dB Bit



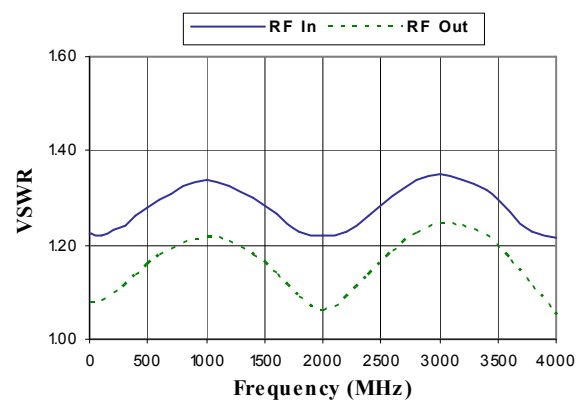
VSWR, 4 dB Bit



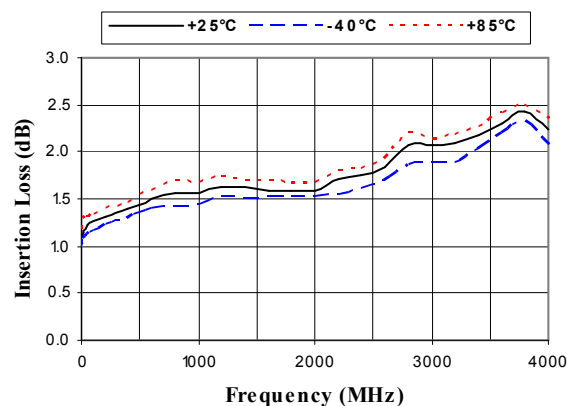
VSWR, 8 dB Bit



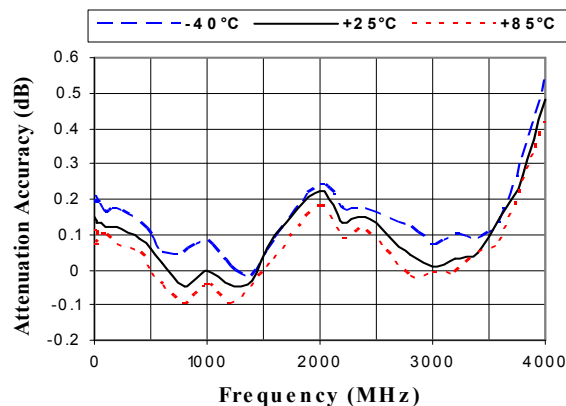
VSWR, Max. Attenuation



Insertion Loss vs. Temperature



1 dB Bit vs. Temperature

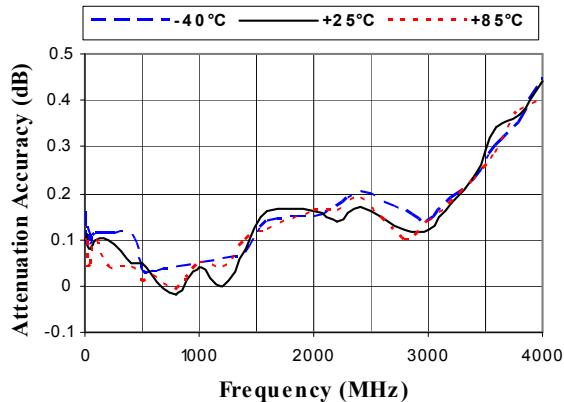


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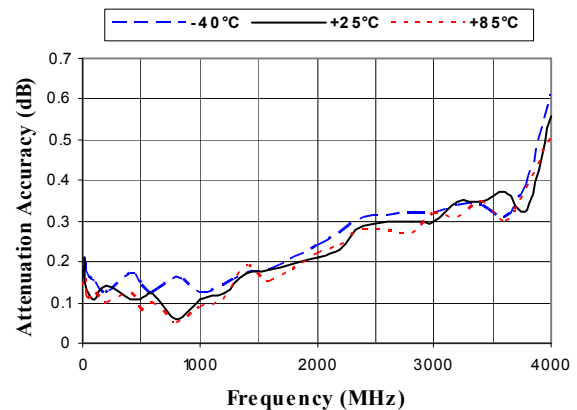
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Typical Performance Curves

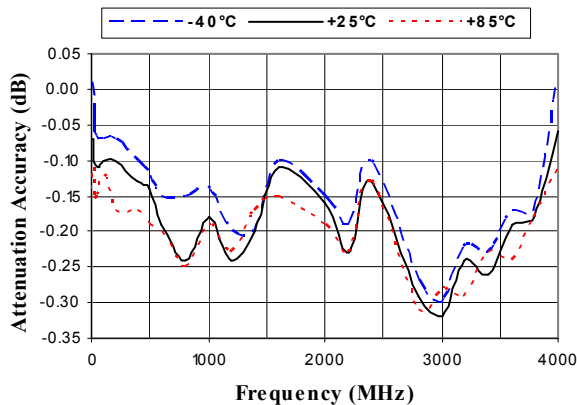
2 dB Bit vs. Temperature



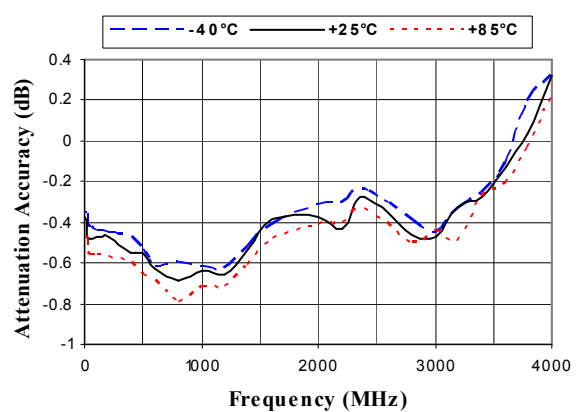
4 dB Bit vs. Temperature



8 dB Bit vs. Temperature



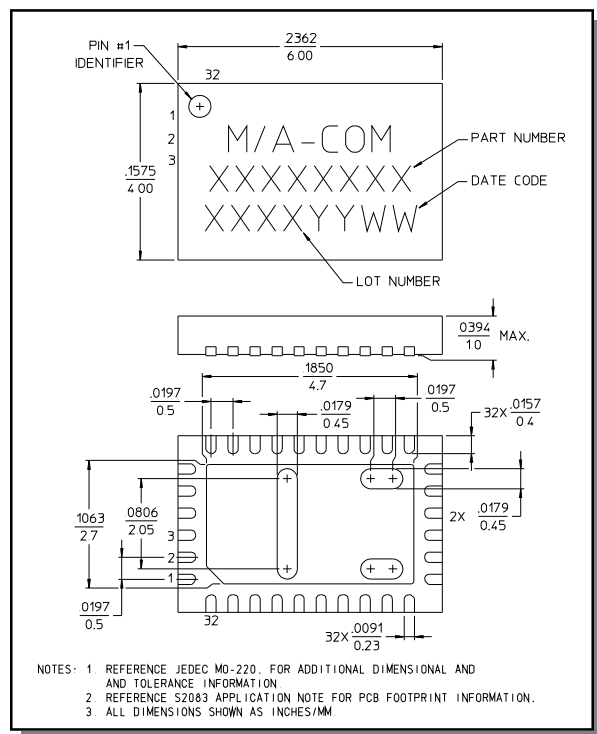
Max. Attenuation vs. Temperature



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**CSP-1, Lead-Free 4 x 6 mm, 32-lead
PQFN[†]**



[†] Reference Application Note M538 for lead-free solder reflow recommendations.