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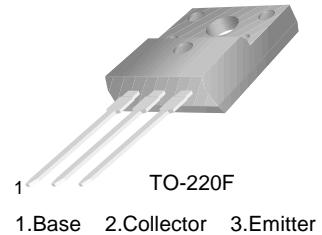


FJPF5021

FJPF5021

High Voltage and High Reliability

- High Speed Switching : $t_F = 0.1\mu\text{s}$ (Typ.)
- Wide SOA



NPN Silicon Transistor

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CBO}	Collector-Base Voltage	800	V
V_{CEO}	Collector-Emitter Voltage	500	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current (DC)	5	A
I_{CP}	Collector Current (Pulse)	10	A
I_B	Base Current	2	A
P_C	Collector Dissipation ($T_C=25^\circ\text{C}$)	40	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature	- 55 ~ 150	$^\circ\text{C}$

Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
BV_{CBO}	Collector-Base Breakdown Voltage	$I_C = 1\text{mA}$, $I_E = 0$	800			V
BV_{CEO}	Collector-Emitter Sustaining Voltage	$I_C = 5\text{mA}$, $I_B = 0$	500			V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_E = 1\text{mA}$, $I_C = 0$	7			V
$V_{CEX}(\text{sus})$	Collector-Emitter Sustaining Voltage	$I_C = 2.5\text{A}$, $I_B1 = -I_B2 = 1\text{A}$ $L = 1\text{mH}$, Clamped	500			V
I_{CBO}	Collector Cut-off Current	$V_{CB} = 500\text{V}$, $I_E = 0$			10	μA
I_{EBO}	Emitter Cut-off Current	$V_{EB} = 5\text{V}$, $I_C = 0$			10	μA
h_{FE1} h_{FE2}	DC Current Gain	$V_{CE} = 5\text{V}$, $I_C = 0.6\text{A}$ $V_{CE} = 5\text{V}$, $I_C = 3\text{A}$	15 8		50	
$V_{CE}(\text{sat})$	Collector-Emitter Saturation Voltage	$I_C = 3\text{A}$, $I_B = 0.6\text{A}$			1	V
$V_{BE}(\text{sat})$	Base-Emitter Saturation Voltage	$I_C = 3\text{A}$, $I_B = 0.6\text{A}$			1.5	V
C_{ob}	Output Capacitance	$V_{CB} = 10\text{V}$, $I_E = 0$, $f = 1\text{MHz}$		80		pF
f_T	Current Gain Bandwidth Product	$V_{CE} = 10\text{V}$, $I_C = 0.6\text{A}$		15		MHz
t_{ON}	Turn On Time	$V_{CC} = 200\text{V}$			0.5	μs
t_{STG}	Storage Time	$I_C = 5I_B1 = -2.5I_B2 = 4\text{A}$ $R_L = 50\Omega$			3	μs
t_F	Fall Time			0.1	0.3	μs

h_{FE} Classification

Classification	R	O	Y
h_{FE1}	15 ~ 30	20 ~ 40	30 ~ 50

Typical Characteristics

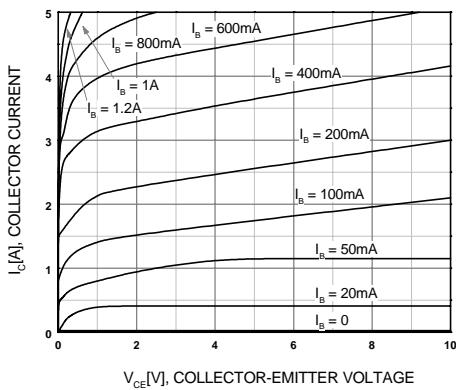


Figure 1. Static Characteristic

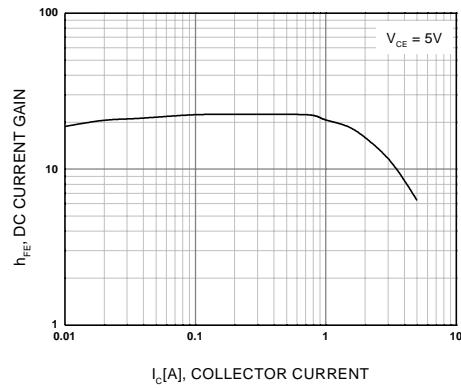


Figure 2. DC current Gain

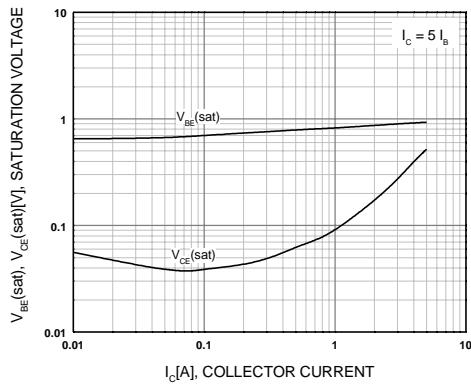


Figure 3. Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

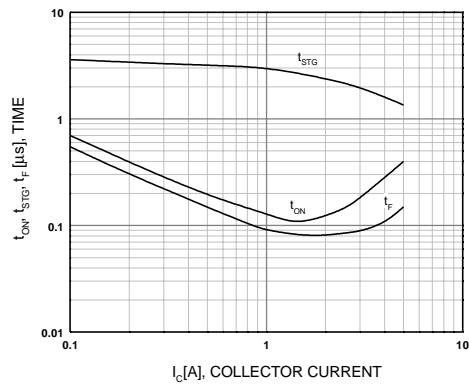


Figure 4. Switching Time

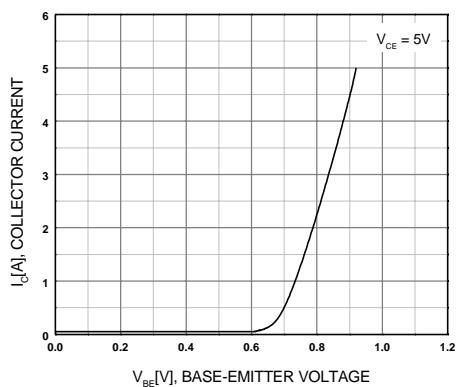


Figure 5. Base-Emitter On Voltage

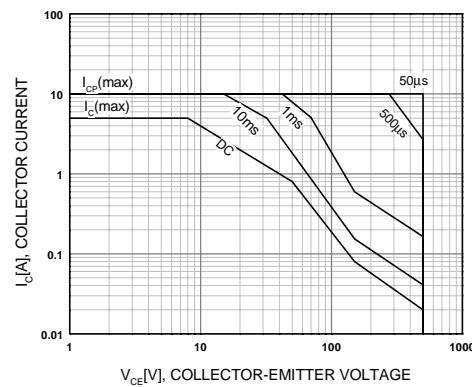


Figure 6. Forward Bias Safe Operating Area

Typical Characteristics (Continued)

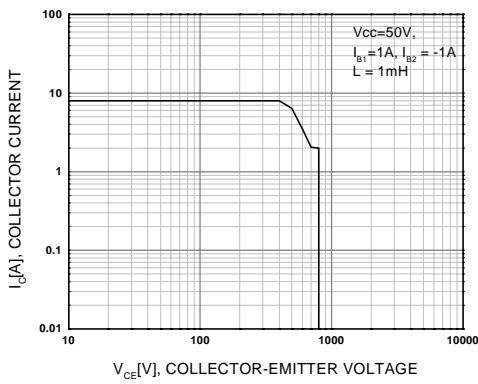


Figure 7. Reverse Bias Safe Operating Area

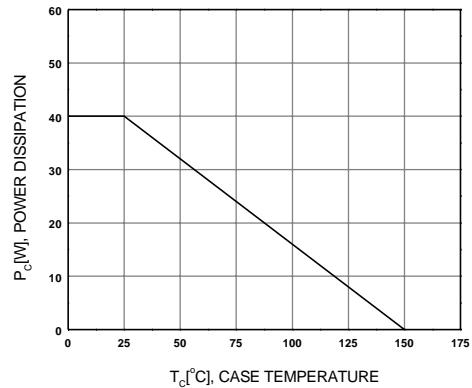
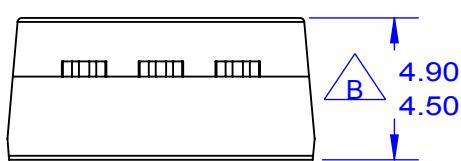
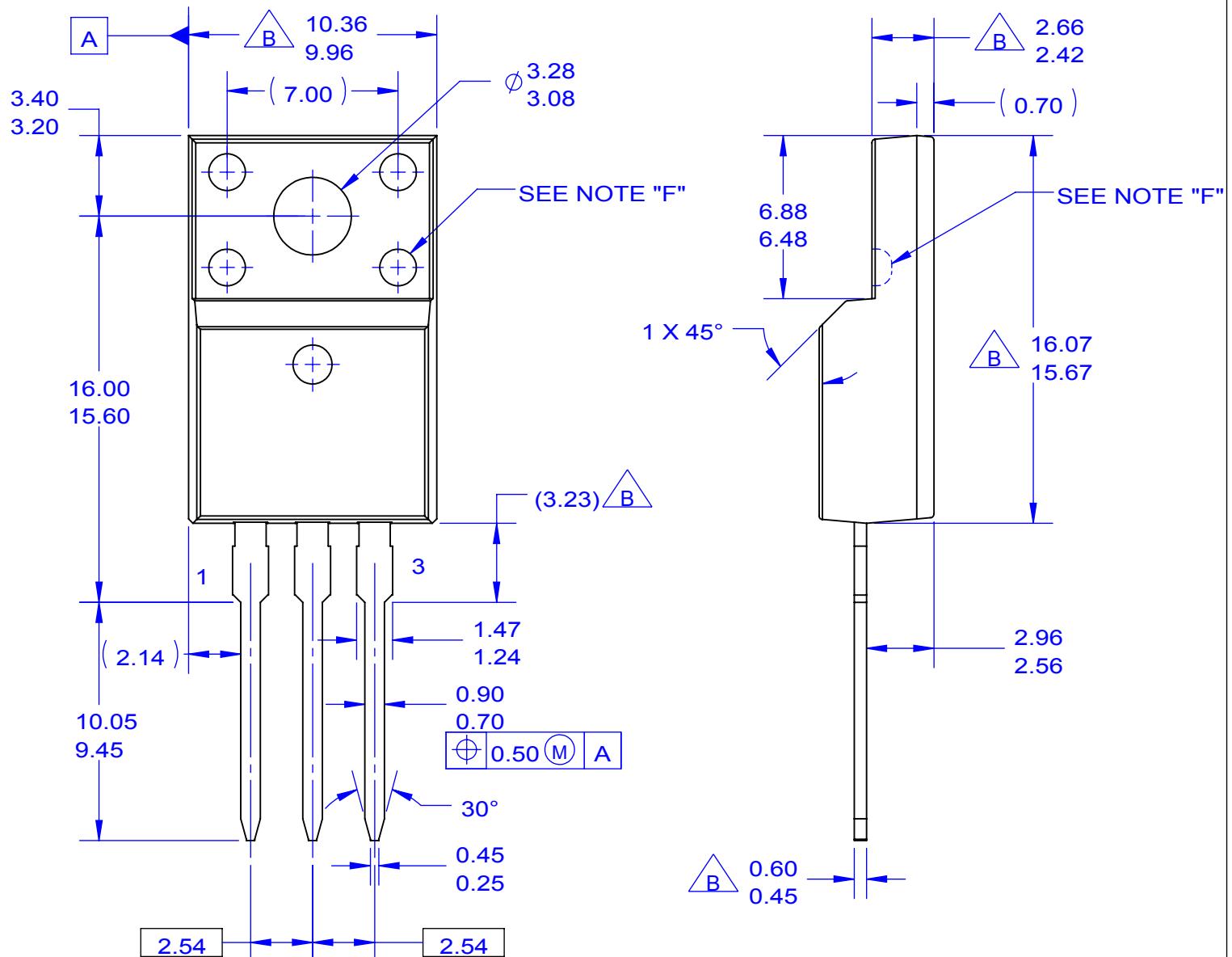


Figure 8. Power Derating



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OPTION 2 - NO SUPPORT PIN HOLE.
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