## RT1P441X SERIES

**(Transistor)** 

UNIT: mm

Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

#### **DESCRIPTION**

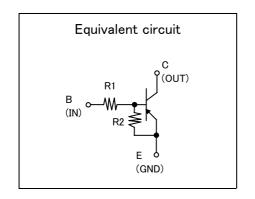
RT1P441X is a one chip transistor with built-in bias resistor,NPN type is RT1N441X.

## **FEATURE**

•Built-in bias resistor (R1=47k  $\Omega$  ,R2=47k  $\Omega$  ).

## **APPLICATION**

Inverted circuit, switching circuit, interface circuit, driver circuit.

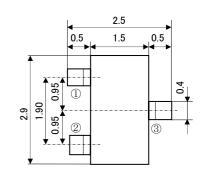


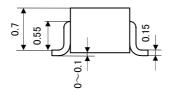
RT1P441S

## OUTLINE DRAWING

RT1P441C

RT1P441U





JEITA: — JEDEC: —

JEDEC

Terminal Connector

①:Base ②:Emitter

3: Collector

JEITA: SC-59 JEDEC: Similar to TO-236

0

Terminal Connector

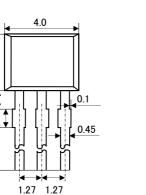
①:Base

2: Emitter

3: Collector

RT1P441M

RT1P441T

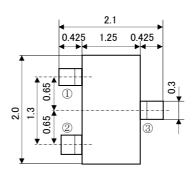


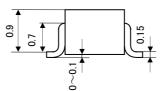


JEITA: — JEDEC: —

**Terminal Connector** 

- 1: Emitter
- 2: Collector
- 3:Base

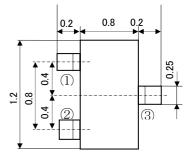


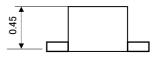


JEITA:SC-70 JEDEC:—

Terminal Connector

- (1):Base
- 2: Emitter
- 3: Collector





JEITA: — JEDEC: —

**Terminal Connector** 

- (1):Base
- 2: Emitter
- 3: Collector

# RT1P441X SERIES

**(Transistor)** 

Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

## MAXIMUM RATING (Ta=25°C)

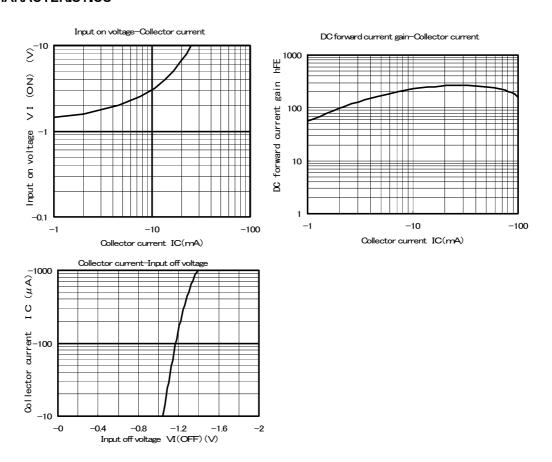
SYMBOL	PARAMETER	RATING					
		RT1P441T	RT1P441U	RT1P441M	RT1P441C	RT1P441S	UNIT
V <sub>CBO</sub>	Collector to Base voltage	-50					V
$V_{EBO}$	Emitter to Base voltage	-10					V
$V_{CEO}$	Collector to Emitter voltage	-50					V
Ic	Collector current	-100					mA
I <sub>CM</sub>	Peak Collector current	-200					mA
P <sub>c</sub>	Collector dissipation(Ta=25°C)	125(※)	125	15	50	450	mW
Tj	Junction temperature	+125		+150			°C
Tstg	Storage temperature	−55 <b>~</b> +125		−55 <b>~</b> +150			°C

 $(\center{lem:months})$  package mounted on 9mm × 19mm × 1mm glass-epoxy substrate.

## ELECTRICAL CHARACTERISTICS (Ta=25°C)

SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
		TEST CONDITION	MIN	TYP	MAX	UNIT
$V_{(BR)CEO}$	C to E break down voltage	$I_{C}=-100 \mu A, R_{BE}=\infty$	-50			٧
I <sub>CBO</sub>	Collector cut off current	$V_{CB}$ =-50V, I $_{E}$ =0			-0.1	μΑ
h <sub>FE</sub>	DC forward current gain	$V_{CE}$ =-5V, I <sub>C</sub> =-5mA	50			_
$V_{CE(sat)}$	C to E saturation voltage	$I_{C} = -10 \text{mA}, I_{B} = -0.5 \text{mA}$		-0.1	-0.3	V
$V_{I(ON)}$	Input on voltage	$V_{CE}$ =-0.2V, I $_{C}$ =-5mA		-2.3	-5.0	V
$V_{I(OFF)}$	Input off voltage	$V_{CE} = -5V$ , I <sub>C</sub> = $-100 \mu$ A	-0.8	-1.1		V
R <sub>1</sub>	Input resistance		33	47	61	kΩ
R <sub>2</sub> /R <sub>1</sub>	Resistance ratio		0.9	1.0	1.1	
f <sub>⊤</sub>	Gain band width product	$V_{CE}$ =-6V, I <sub>E</sub> =10mA		150		MHz

## TYPICAL CHARACTERISTICS





Marketing division, Marketing planning department 6-41 Tsukuba, Isahaya, Nagasaki, 854-0065 Japan

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