# TRIPLE PECL-TO-ECL TRANSLATOR

SY100EL91

#### **FEATURES**

- 620ps typical propagation delay
- Fully differential design
- Supports standard operation
- Available in 20-pin SOIC package

#### **DESCRIPTION**

The SY100EL91 is a triple PECL-to-ECL translator. It receives standard voltage PECL signals and translates them to different ECL output signals.

A VBB output is provided for interfacing with single ended PECL signals at the input. If a single ended input is to be used, the VBB output should be connected to the  $\overline{D}$  input. The active signal would then drive the D input. When used, the VBB output should be bypassed to ground via a  $0.01\mu F$  capacitor. The VBB output is designed to act as the switching reference for the EL91 under single ended input switching conditions. As a result this pin can only source/sink up to 0.5 mA of current.

To accomplish the level translation the EL91 requires three power rails. The Vcc supply should be connected to the positive supply, and the VEE pin should be connected to the negative power supply. The GND pins as expected are connected to the system ground plane. Both VEE and Vcc should be bypassed to ground via  $0.01\mu F$  capacitors.

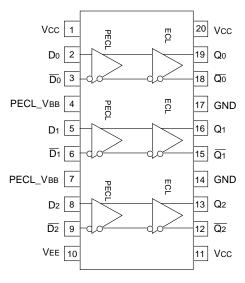
Under open input conditions, the  $\overline{D}$  input will be biased at Vcc/2 and the D input will be pulled to GND. This condition will force the Q output to a LOW, ensuring stability.

#### PIN NAMES

Pin	Function
Dn	PECL Inputs
Qn	ECL Outputs
PECL_VBB	PECL Reference Voltage Output

Micrel, Inc. SY100EL91

## PACKAGE/ORDERING INFORMATION



20-Pin Wide SOIC (Z20-1)

## Ordering Information<sup>(1)</sup>

Part Number	Package Type	Operating Range	Package Marking	Lead Finish
SY100EL91ZC	Z20-1	Commercial	SY100EL91ZC	Sn-Pb
SY100EL91ZCTR <sup>(2)</sup>	Z20-1	Commercial	SY100EL91ZC	Sn-Pb
SY100EL91ZI	Z20-1	Industrial	SY100EL91ZI	Sn-Pb
SY100EL91ZITR <sup>(2)</sup>	Z20-1	Industrial	SY100EL91ZI	Sn-Pb
SY100EL91ZG <sup>(3)</sup>	Z20-1	Industiral	SY100EL91ZG with Pb-Free bar-line indicator	NiPdAu Pb-Free
SY100EL91ZGTR <sup>(2, 3)</sup>	Z20-1	Industrial	SY100EL91ZG with Pb-Free bar-line indicator	NiPdAu Pb-Free

#### Notes:

- 1. Contact factory for die availability. Dice are guaranteed at  $T_A$  = 25°C, DC Electricals only.
- 2. Tape and Reel.
- 3. Pb-Free package is recommended for new designs.

## PECL INPUT DC ELECTRICAL CHARACTERISTICS

		TA = -40°C			7	TA = 0°C			TA = +25°C			TA = +85°C			
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit	
Vcc	Power Supply Voltage	4.75	_	5.25	4.75	_	5.25	4.75	_	5.25	4.75	_	5.25	V	
VIH	Input HIGH Voltage <sup>(1)</sup>	3.835	_	4.120	3.835	_	4.120	3.835	_	4.120	3.835	_	4.120	V	
VIL	Input LOW Voltage <sup>(1)</sup>	3.190	_	3.525	3.190	_	3.525	3.190	_	3.525	3.190	_	3.525	V	
Iн	Input HIGH Current	_	_	150	_	_	150	_	_	150	_	_	150	μΑ	
lıL	Input LOW Current Dn Dn	0.5 -600	_	_ _	0.5 -600	_	_	0.5 -600	_	_	0.5 -600	_	_ _	μА	
Vвв	Output Reference <sup>(1)</sup>	3.620	_	3.740	3.620	_	3.740	3.620	_	3.740	3.620	_	3.740	V	
Icc	Power Supply Current	_	_	11			11	_	6.0	11	_		11	mA	

#### Note:

### **ECL OUTPUT DC ELECTRICAL CHARACTERISTICS**

		TA = -40°C			TA = 0°C			TA = +25°C			TA = +85°C			
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
VEE	Power Supply Voltage	-4.2	_	-5.5	-4.2	_	-5.5	-4.2		-5.5	-4.2	_	-5.5	V
Vон	Output HIGH Voltage	-1085	_	-880	-1025	_	-880	-1025	-955	-880	-1025	_	-880	mV
VoL	Output LOW Voltage	-1830	_	-1555	-1810	_	-1620	-1810	-1705	-1620	-1810	_	-1620	mV
IEE	Power Supply Current	_	_	28	_	_	28	_	22	28	_	_	30	mA

<sup>1.</sup> These values are for Vcc = 5V. Level specifications will vary 1:1 with Vcc.

## **AC ELECTRICAL CHARACTERISTICS**

PECL: VCC = +4.75V to +5.25V, ECL: VEE = -4.2V to -5.5V

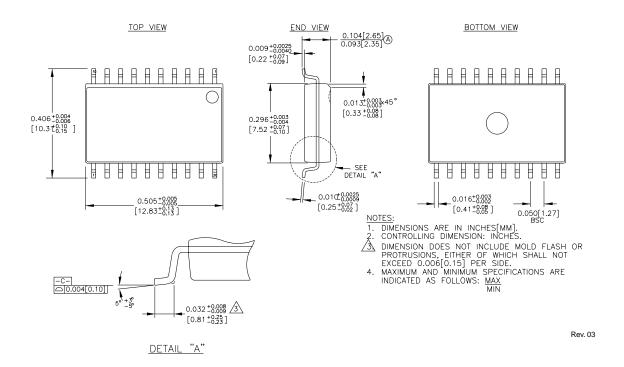
		TA = -40°C			TA = 0°C			T/	A = +25	°C	TA = +85°C			
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
tPD	Propagation Delay Diff. D to Q S.E.	520 470	620 620	720 770	520 470	620 620	720 770	520 470	620 620	720 770	550 500	650 650	750 800	ps
tskew	Within-Device Skew <sup>(1)</sup> Output-to-Output Part-to-Part (Diff.) Duty Cycle (Diff.)		_ _ _ 25	100 200 —	_ _ _	_ _ _ 25	100 200 —	_ _ _	20 — 25	100 200 —	_ _ _	_ _ _ 25	100 200 —	ps
VPP	Minimum Input Swing(2)	200	_	_	200	_	_	200	_	_	200	_	_	mV
VCMR	Common Mode Range <sup>(3)</sup> VPP < 500mV VPP > 500mV	1.3 1.5	_	Vcc-0.2 Vcc-0.2	1.2 1.4	_	Vcc-0.2 Vcc-0.2	1.2 1.4	_	Vcc-0.2 Vcc-0.2	1.2 1.4	_	Vcc-0.2 Vcc-0.2	
tr tf	Output Rise/Fall Times Q (20% to 80%)	320	400	580	320	400	580	320	400	580	320	400	580	ps

#### Notes:

- 1. Skew is measured between outputs under identical transitions.
- 2. Minimum input swing for which AC parameters are guaranteed. The device has a DC gain of ~40.
- 3. The CMR range is referenced to the most positive side of the differential input signal. Normal operation is obtained if the HIGH level falls within the specified range and the peak-to-peak voltage lies between VPP min. and 1V.

Micrel, Inc. SY100EL91

#### 20-PIN SOIC .300" WIDE (Z20-1)



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