

HD74LS14

Hex Schmitt Trigger Inverters

REJ03D0399-0300 Rev.3.00 Jul.13.2005

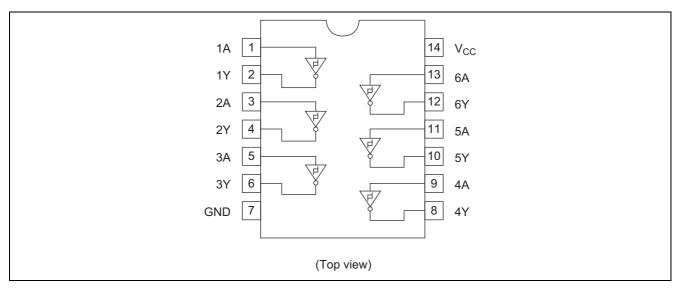
Features

• Ordering Information

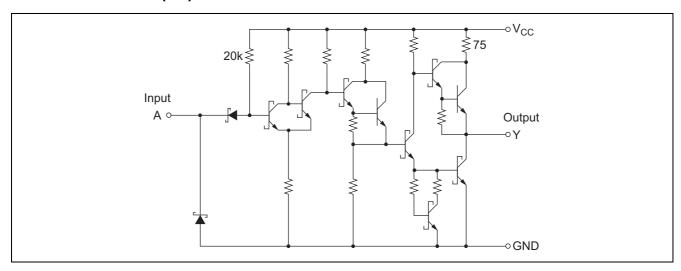
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS14P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS14FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



Circuit Schematic (1/6)



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Input voltage	V _{IN}	7	V
Supply voltage	Vcc	7	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	Vcc	4.75	5.00	5.25	V
Output current	I _{OH}	_	_	-400	μΑ
Output current	I _{OL}	_	_	8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input threshold	V _T ⁺	1.4	1.6	1.9	V	V _{CC} = 5 V
voltage	V _T ⁻	0.5	0.7	1.0	V	V _{CC} = 5 V
Hysteresis	$V_T^+ - V_T^-$	0.4	0.9	_	V	V _{CC} = 5 V
	V _{OH}	2.7	_		V	$V_{CC} = 4.75 \text{ V}, V_{I} = 0.5 \text{ V}, I_{OH} = -400 \mu\text{A}$
Output voltage	V _{OL}	_	_	0.5	V	$I_{OL} = 8 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{I} = 1.9 \text{ V}$
	VOL	_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{I} = 1.9 \text{ V}$
Input threshold	I _T +	_	-0.14	_	mA	$V_{CC} = 5 \text{ V}, \text{ V}_{I} = \text{V}_{T}^{+}$
current	I _T	_	-0.18	_	mA	$V_{CC} = 5 \text{ V}, \text{ V}_{I} = \text{V}_{T}^{-}$
	I _{IH}	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
Input current	I _{IL}	_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	I	_	_	0.1	mA	V _{CC} = 5.25 V, V _I = 7 V
Short-circuit output current	los	-20	_	-100	mA	V _{CC} = 5.25 V
Supply current	I _{CCH}	_	8.6	16	mA	V _{CC} = 5.25 V
Supply current	I _{CCL}	_	12	21	mA	V _{CC} = 5.25 V
Input clamp voltage	V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Note: $^*V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C}$

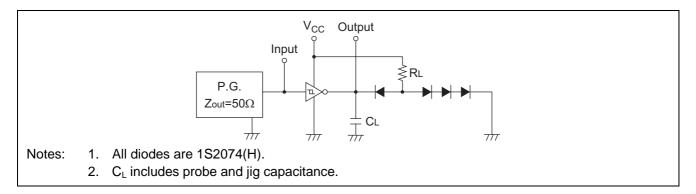
Switching Characteristics

 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$

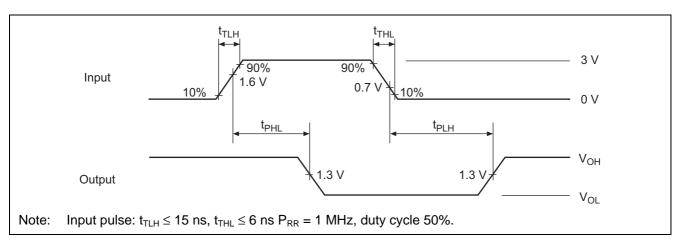
Item	Symbol	min.	typ.	max.	Unit	Condition	
Propagation delay time	t _{PLH}	_	15	22	ns	$C_L = 15 \text{ pF}, R_L = 2 \text{ k}\Omega$	
	t _{PHL}	_	15	22	ns	$C_{L} = 15 \text{pr}, \text{KL} = 2 \text{K}_{2}$	

Testing Method

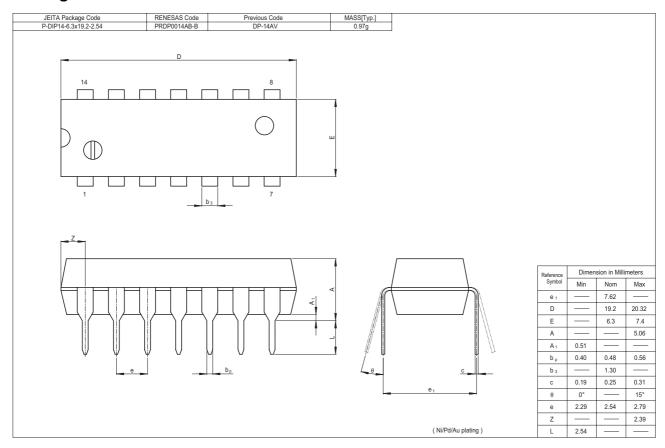
Test Circuit

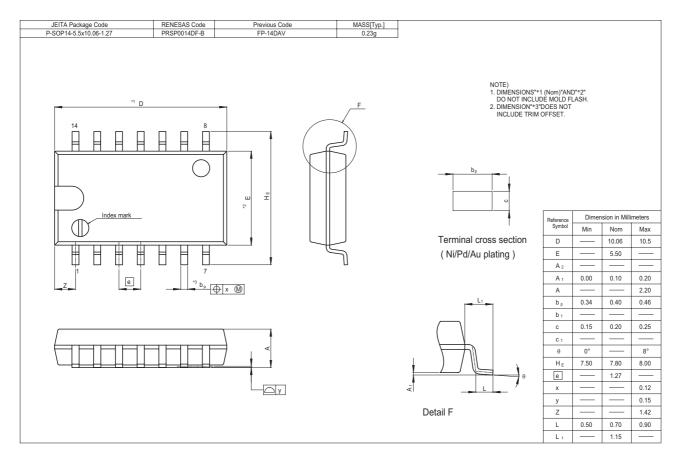


Waveform



Package Dimensions





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