MIC7111



1.8V to 11V, 15µA, 25kHz GBW, Rail-to-Rail Input and Output Operational Amplifier

General Description

The MIC7111 is a low-power operational amplifier with rail-to-rail inputs and outputs. The device operates from a 1.8V to 11V single supply or an $\pm 0.9V$ to $\pm 5.5V$ dual supply. The device consumes a low 15µA of current from a 1.8V supply and 25µA from a 10V supply. The device features a unity gain bandwidth of 25kHz and swings within 1mV of either the supply rail with a 100k Ω load. The device is capable of sinking and sourcing 25mA of current from a 1.8V supply and up to 200mA from a 10V supply. The device is available in the cost effective SOT23-5 package.

Datasheets and support documentation are available on Micrel's web site at: www.micrel.com.

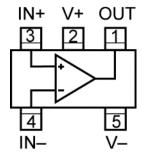
Features

- 1.8V to 11V single supply operation
- ±0.9V to ±5.5V dual supply operation
- Low 15µA supply current at 1.8V
- 25kHz gain bandwidth
- 1mV input offset voltage (typical)
- 1pA input bias current (typical)
- 0.01pA input offset current (typical)
- Input-referred noise is 110nv/√Hz at 1kHz
- Output swing to within 1mV of rails with 1.8V supply and $100k\Omega$ load
- Suitable for driving capacitive loads
- Cost effective SOT23-5 package

Applications

- · Wireless and cellular communications
- · GaAs RF bias amplifier
- Current sensing for battery chargers
- Transducer linearization and interface
- · Portable computing

Functional Configuration



SOT-23-5 (M5)

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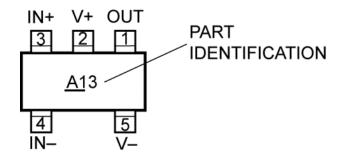
Ordering Information

Part Number	lunation Tomporature Pange	Package ⁽¹⁾
Pb-Free	Junction Temperature Range	Fackage
MIC7111YM5	−40°C to +85°C	SOT23-5

Note:

1. Other packages are available. Contact Micrel for details.

Pin Configuration



SOT23-5 (M5) (Top View)

Pin Description

Pin Number	Pin Name	Pin Function
1	OUT	Amplifier Output.
2	V+	Positive Supply
3	IN+	Non-inverting Input.
4	IN-	Inverting Input
5	V-	Negative Supply.

Absolute Maximum Ratings⁽¹⁾

Operating Ratings⁽²⁾

Supply Voltage $(V_{V+} - V_{V-})$. +1.8V to +11V
Junction Temperature (T _J)	
Maximum Junction Temperature $(T_{J(MAX)})^{(4)}$.	+85°C
Package Thermal Resistance (θ _{JA}) ⁽⁵⁾	+252°C/W
Maximum Power Dissipation	Note 4

DC Electrical Characteristics

 $V_{V+} = +1.8V; V_{V-} = 0V; V_{CM} = V_{OUT} = V_{V+}/2; R_L = 1M; T_J = +25^{\circ}C,$ bold values indicate $-40^{\circ}C \le T_J \le +85^{\circ}C,$ unless noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
V	land Offer A Valle and			0.9	7	mV
V _{OS}	Input Offset Voltage				9	IIIV
TCVos	Input Offset Voltage Temperature Drift			2.0		μV/°C
	Input Bigs Current			1	10	- A
l _B	Input Bias Current				500	pА
	Input Offact Current			0.01	0.5	- Λ
I _{OS}	Input Offset Current				75	- pA
R _{IN}	Input Resistance			>10		ΤΩ
+PSRR	Positive Power Supply Rejection Ratio	$1.8V \le V_{V+} \le 5V, V_{V-} = 0V,$ $V_{CM} = V_{OUT} = 0.9V$	60	85		dB
-PSRR	Negative Power Supply Rejection Ratio	$-1.8V \le V_V - \le -5V$, $V_V + = 0V$, $V_{CM} = V_{OUT} = -0.9V$	60	85		dB
CMRR	Common-Mode Rejection Ratio	$V_{CM} = -0.2V \text{ to } +2.0V$	50	70		dB
C _{IN}	Common-Mode Input Capacitance			3		pF
		Output HIGH, R _L = 100k,		0.14	1	
		Specified as V _{V+} –V _{OUT}			1	
				0.14	1	mV
W	Customat Malta and Customa	Output LOW, $R_L = 100k$			1	
V _{OUT}	Output Voltage Swing	Output HIGH, R _L = 2k,		6.8	23	
		Specified as V _{V+} – V _{OUT}			34	
		Output LOW B. 3k		6.8	23	
		Output LOW, $R_L = 2k$			34	

Notes:

- Absolute maximum ratings indicate limits beyond which damage to the component may occur. Electrical specifications do not apply when operating
 the device outside its recommended operating ratings.
- 2. The device is not guaranteed to function outside its operating ratings.
- 3. I/O pin voltage is any external voltage to which an input or output is referenced.
- 4. The maximum allowable power dissipation is a function of the maximum junction temperature, $T_{J(MAX)}$; the junction-to-ambient thermal resistance, θ_{JA} ; and the ambient temperature, T_A . The maximum allowable power dissipation at any ambient temperature is calculated using $P_D = (T_{J(MAX)} TA) \div \theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature.
- 5. Thermal resistance, θ_{JA} , applies to a part soldered on a printed-circuit board.
- Devices are ESD protected, however, handling precautions are recommended. All limits guaranteed by testing on statistical analysis. Human body model, 1.5kΩ in series with 100pF.

DC Electrical Characteristics (Continued)

 $V_{V+} = +1.8V$; $V_{V-} = 0V$; $V_{CM} = V_{OUT} = V_{V+}/2$; $R_L = 1M$; $T_J = +25^{\circ}C$, **bold** values indicate $-40^{\circ}C \le T_J \le +85^{\circ}C$, unless noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
I _{SC}	Output Short-Circuit Current ⁽⁷⁾	Sourcing, V _{OUT} = 0V	15	25		A
		Sinking, V _{OUT} = 1.8V	15	25		mA
^	Voltage Gain	Sourcing		400		\//ma\/
A _{VOL}		Sinking		400		V/mV
Is	Supply Current	$V_{V+} = 1.8V, V_{OUT} = V_{V+}/2$		15	35	μΑ

AC Electrical Characteristics

 $V+=+1.8V;\ V-=0V;\ V_{CM}=V_{OUT}=V_{V+}/2;\ R_L=1M;\ T_J=+25^{\circ}C,\ \text{bold}\ values\ indicate}\ -40^{\circ}C\leq T_J\leq +85^{\circ}C,\ unless\ noted.$

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
SR	Slew Rate	Voltage follower, 1V step, R _L = 100k @ 0.9V, V _{OUT} = 1V _{P-P}		0.015		V/µs
GBW	Gain Bandwidth Product	Sourcing		25		kHz

DC Electrical Characteristics (2.7V)

 $V_{V+} = +2.7V$; $V_{V-} = 0V$; $V_{CM} = V_{OUT} = V_{V+}/2$; $R_L = 1M$; $T_J = +25^{\circ}C$, **bold** values indicate $-40^{\circ}C \le T_J \le +85^{\circ}C$, unless noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
V	Input Offact Valtage			0.9	7	m)/
Vos	Input Offset Voltage				9	mV
TCVos	Input Offset Voltage Temperature Drift			2.0		μV/°C
	Input Bing Current			1	10	π Λ
I _B	Input Bias Current				500	- pA
	Input Offset Current			0.01	0.5	π Λ
I _{OS}					75	- pA
R _{IN}	Input Resistance			>10		TΩ
+PSRR	Positive Power Supply Rejection Ratio	$2.7V \le V_{V+} \le 5V, V_{V-} = 0V,$ $V_{CM} = V_{OUT} = 1.35V$	60	90		dB
-PSRR	Negative Power Supply Rejection Ratio	$-2.7V \le V_{V-} \le -5V$, $V_{V+} = 0V$, $V_{CM} = V_{OUT} = -1.35V$	60	90		dB
CMRR	Common-Mode Rejection Ratio	$V_{CM} = -0.2V \text{ to } +2.9V$	52	75		dB
C _{IN}	Common-Mode Input Capacitance			3		pF

Note:

^{7.} Short circuit may cause the device to exceed maximum allowable power dissipation (see Note 3).

DC Electrical Characteristics (2.7V) (Continued) $V_{V+} = +2.7V; \ V_{V-} = 0V; \ V_{CM} = V_{OUT} = V_{V+}/2; \ R_L = 1M; \ T_J = +25^{\circ}C, \ bold \ values \ indicate \ -40^{\circ}C \le T_J \le +85^{\circ}C, \ unless \ noted.$

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
		Output HIGH, R _L = 100k,		0.2	1	
		Specified as V _{V+} –V _{OUT}			1	
		Output LOW B. 100k		0.2	1	
W	Output Voltage Swing	Output LOW, R _L = 100k			1	m\/
V _{OUT}	Output Voltage Swing	Output HIGH, R _L = 2k,		10	33	- mV -
		Specified as V _{V+} – V _{OUT}			50	
		Output LOW, R _L = 2k		10	33	
					50	
	Output Short-Circuit Current ⁽⁷⁾	Sourcing, V _{OUT} = 0V	30	50		mA
I _{SC}	Output Short-Circuit Current	Sinking, V _{OUT} = 2.7V	30	50		IIIA
٨	Valtage Cain	Sourcing		400		\//m\/
A _{VOL}	Voltage Gain	Sinking		400		V/mV
Is	Supply Current	$V_{V+} = 2.7V, V_{OUT} = V_{V+}/2$		17	42	μΑ

AC Electrical Characteristics (2.7V)

 $V+=+2.7V;\ V-=0V;\ V_{CM}=V_{OUT}=V_{V+}/2;\ R_L=1M;\ T_J=+25^{\circ}C,\ \text{bold}\ values\ indicate}\ -40^{\circ}C \leq T_J \leq +85^{\circ}C,\ unless\ noted.$

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
SR	Slew Rate	Voltage follower, 1V step, R _L = 100k @ 1.35V , V _{OUT} = 1V _{P-P}		0.015		V/µs
GBW	Gain Bandwidth Product	Sourcing		25		kHz

DC Electrical Characteristics (5V)

 $V_{V+} = +5V; \ V_{V-} = 0V; \ V_{CM} = V_{OUT} = V_{V+}/2; \ R_L = 1M; \ T_J = +25^{\circ}C, \ \textbf{bold} \ \ \text{values indicate} \ -40^{\circ}C \leq T_J \leq +85^{\circ}C, \ unless \ noted.$

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
V	Innut Offe at Veltage			0.9	7	>/
Vos	Input Offset Voltage				9	mV
TCVos	Input Offset Voltage Temperature Drift			2.0		μV/°C
1	Input Bias Current			1	10	n ^
I _B	input bias current				500	- pA
los	Input Offset Current			0.01	0.5	pA
IOS					75	pΑ
R _{IN}	Input Resistance			>10		ΤΩ
+PSRR	Positive Power Supply Rejection Ratio	$5V \le V_{V+} \le 10V, V_{V-} = 0V,$ $V_{CM} = V_{OUT} = 2.5V$	65	95		dB
-PSRR	Negative Power Supply Rejection Ratio	$-5V \le V_V - \le -10V$, $V_V + = 0V$, $V_{CM} = V_{OUT} = -2.5V$	65	95		dB
CMRR	Common-Mode Rejection Ratio	$V_{CM} = -0.2V \text{ to } +5.2V$	57	80		dB
C _{IN}	Common-Mode Input Capacitance			3		pF
		Output HIGH, R _L = 100k,		0.3	1.5	
		Specified as V _{V+} –V _{OUT}			1.5	mV
		Output LOW, R _L = 100k		0.3	1.5	
V_{OUT}	Output Voltage Swing				1.5	
VOUI	Output Voltage Swilig	Output HIGH, R _L = 2k,		15	50	1111
		Specified as V _{V+} – V _{OUT}			75	
		Output LOW, R _L = 2k		15	50	
		Output LOVV, NL = ZK			75	
I _{SC}	Output Short-Circuit Current ⁽⁷⁾	Sourcing, V _{OUT} = 0V	80	100		mA
190	Output Onort-Oneuit Ourient	Sinking, V _{OUT} = 5V	80	100		ША
A _{VOL}	Voltage Gain	Sourcing		500		V/mV
AVUL	vollage Gain	Sinking		500		v/mv
Is	Supply Current	$V_{V+} = 5V$, $V_{OUT} = V_{V+}/2$		20	50	μΑ

AC Electrical Characteristics (5V)

 $V+=+5V;\ V-=0V;\ V_{CM}=V_{OUT}=V_{V+}/2;\ R_L=1M;\ T_J=+25^{\circ}C,\ \text{bold}\ values\ indicate}\ -40^{\circ}C\leq T_J\leq +85^{\circ}C,\ unless\ noted.$

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
SR	Slew Rate	Voltage follower, 1V step, R _L = 100k @ 1.5V, V _{OUT} = 1V _{P-P}		0.02		V/µs
GBW	Gain Bandwidth Product	Sourcing		25		kHz

DC Electrical Characteristics (10V)

 $V_{V+} = +10V; \ V_{V-} = 0V; \ V_{CM} = V_{OUT} = V_{V+}/2; \ R_L = 1M; \ T_J = +25^{\circ}C, \ \textbf{bold} \ values \ indicate} \ -40^{\circ}C \leq T_J \leq +85^{\circ}C, \ unless \ noted.$

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
V	Input Offact Valtage			0.9	7	m\/
Vos	Input Offset Voltage				9	mV
TCV_OS	Input Offset Voltage Temperature Drift			2.0		μV/°C
I-	Input Bias Current			1	10	рA
I _B	input bias current				500	pΑ
los	Input Offset Current			0.01	0.5	рA
ios	input Onset Current				75	pΑ
R _{IN}	Input Resistance			>10		ΤΩ
+PSRR	Positive Power Supply Rejection Ratio	$5V \le V_{V+} \le 10V, V_{V-} = 0V,$ $V_{CM} = V_{OUT} = 2.5V$	65	95		dB
-PSRR	Negative Power Supply Rejection Ratio	$-5V \le V_V - \le -10V$, $V_V + = 0V$, $V_{CM} = V_{OUT} = -2.5V$	65	95		dB
CMRR	Common-Mode Rejection Ratio	$V_{CM} = -0.2V \text{ to } +10.2V$	60	85		dB
C _{IN}	Common-Mode Input Capacitance			3		pF
		Output HIGH, R _L = 100k, Specified as V _{V+} –V _{OUT}		0.45	2.5	- mV
					2.5	
		Output LOW, R _L = 100k		0.45	2.5	
V_{OUT}	Output Voltage Swing				2.5	
V OU I	Output voltage Swing	Output HIGH, $R_L = 2k$,		1 0.01	80	
		Specified as V _{V+} – V _{OUT}			120	
		Output LOW, R _L = 2k		24	80	
		Cutput LOVV, IV. – ZIV			120	
I _{sc}	Output Short-Circuit Current ⁽⁷⁾	Sourcing, V _{OUT} = 0V	100	200		mA
150	Catput Chort Choult Culton	Sinking, V _{OUT} = 10V	100	200		IIIA
A _{VOL}	Voltage Gain	Sourcing		500		V/mV
, WOL	Tollago Calif	Sinking		500		V/IIIV
Is	Supply Current	$V_{V+} = 10V, V_{OUT} = V_{V+}/2$		25	65	μΑ

AC Electrical Characteristics (10V)

 $V+=+10V;\ V-=0V;\ V_{CM}=V_{OUT}=V_{V+}/2;\ R_L=1M;\ T_J=+25^{\circ}C,\ \text{bold}\ \text{values indicate}\ -40^{\circ}C\leq T_J\leq +85^{\circ}C,\ \text{unless noted}.$

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
SR	Slew Rate	Voltage follower, 1V step, $R_L = 100k @ 1.35V$ $V_{OUT} = 1V_{P-P}$		0.02		V/µs
GBW	Gain Bandwidth Product			25		kHz
фм	Phase Margin			50		0
G _M	Gain Margin			15		dB
e _N	Input-Referred Voltage Noise	f = 1kHz, V _{CM} = 1.0V		110		nV/√Hz
i _N	Input-Referred Current Noise	f = 1kHz		0.03		pA/√Hz

Application Information

Input Common Mode Voltage

The MIC7111 tolerates input overdrive by at least 300mV beyond either rail without producing phase inversion.

If the absolute maximum input voltage is exceeded, the input current should be limited to $\pm 5 \text{mA}$ maximum to prevent reducing reliability. A $10 \text{k}\Omega$ series input resistor, used as a current limiter, will protect the input structure from voltages as large as 50V above the supply or below ground. See Figure 1.

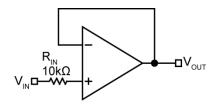


Figure 1. Input Current-Limit Protection

Output Voltage Swing

Sink and source output resistances of the MIC7111 are equal. Maximum output voltage swing is determined by the load and the approximate output resistance. The output resistance is presented in Equation 1:

$$R_{OUT} = \frac{V_{DROP}}{I_{LOAD}}$$
 Eq. 1

 V_{DROP} is the voltage dropped within the amplifier output stage. V_{DROP} and I_{LOAD} can be determined from the V_O (output swing) portion of the appropriate electrical characteristics table. I_{LOAD} is equal to the typical output high voltage minus V+/2 and divided by $R_{LOAD}.$ For example, using the DC Electrical Characteristics (5V) table, the typical output voltage drop using a $2k\Omega$ load (connected to V+/2) is 0.015V, which produces an I_{LOAD} of:

$$\frac{2.5V - 0.015V}{2k\Omega} = 1.243mA$$
 Eq. 2

Then,

$$R_{OUT} = \frac{15mV}{1.243mA} = 12.1 = 12\Omega$$
 Eq. 3

Driving Capacitive Loads

Driving a capacitive load introduces phase-lag into the output signal, and this in turn reduces op-amp system phase margin. The application that is least forgiving of reduced phase margin is a unity gain amplifier. The MIC7111 can typically drive a 500pF capacitive load connected directly to the output when configured as a unity-gain amplifier.

Using Large-Value Feedback Resistors

A large-value feedback resistor (> $500k\Omega$) can reduce the phase margin of a system. This occurs when the feedback resistor acts in conjunction with input capacitance to create phase lag in the feedback signal. Input capacitance is usually a combination of input circuit components and other parasitic capacitance, such as amplifier input capacitance and stray printed circuit board capacitance.

Figure 2 illustrates a method of compensating phase lag caused by using a large-value feedback resistor. Feedback capacitor C_{FB} introduces sufficient phase lead to overcome the phase lag caused by feedback resistor R_{FB} and input capacitance C_{IN} . The value of C_{FB} is determined by first estimating C_{IN} and then applying the following formula:

$$R_{IN} \times C_{IN} \le R_{FB} \times C_{FB}$$
 Eq. 4

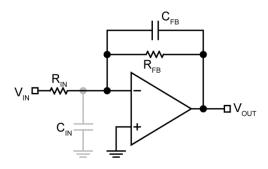


Figure 2. Cancelling Feedback Phase Lag

Since a significant percentage of C_{IN} may be caused by board layout, it is important to note that the correct value of C_{FB} may change when changing from a breadboard to the final circuit layout.

Typical Circuits

Some single-supply, rail-to-rail applications – for which the MIC7111 is well suited – are shown in the circuit diagrams of Figures 3 through 8.

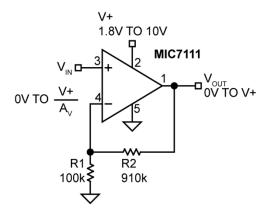


Figure 3. Noninverting Amplifier

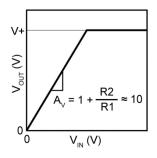


Figure 4. Noninverting Amplifier Behavior

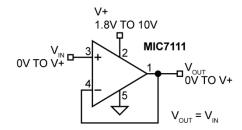


Figure 5. Voltage Follower/Buffer

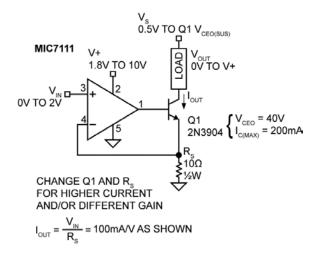


Figure 6. Voltage-Controlled Current Sink

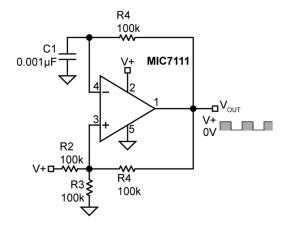


Figure 7. Square Wave Oscillator

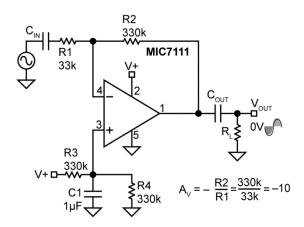
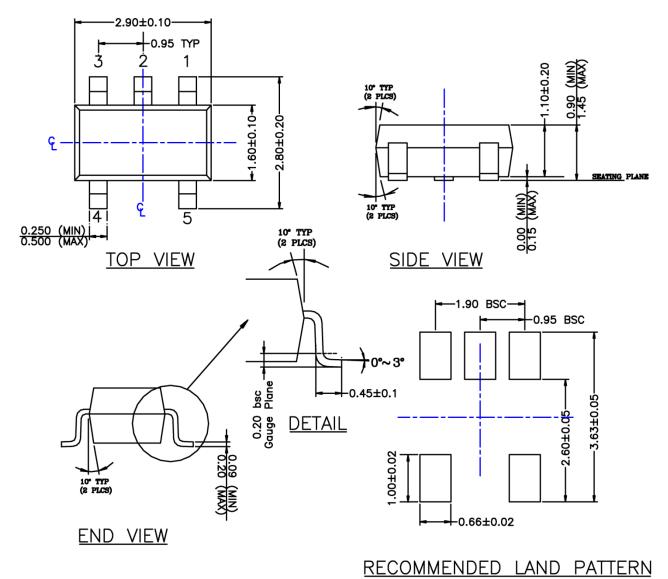


Figure 8. AC-Coupled Inverting Amplifier

Package Information⁽¹⁾ and Recommended Landing Pattern



NOTE:

- 1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & BURR.
 2. PACKAGE OUTLINE INCLUSIVE OF SOLER PLATING.
 3. DIMENSION AND TOLERANCE PER ANSI Y14.5M, 1982.
 4. FOOT LENGTH MEASUREMENT BASED ON GAUGE PLANE METHOD.
- 5. DIE FACES UP FOR MOLD, AND FACES DOWN FOR TRIM/FORM.
- 6. ALL DIMENSIONS ARE IN MILLIMETERS.

SOT23-5 (M5)

Note:

1. Package information is correct as of the publication date. For updates and most current information, go to www.micrel.com.

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