SNOS553D -MAY 1998-REVISED APRIL 2013

LM79MXX Series 3-Terminal Negative Regulators

Check for Samples: LM79M05, LM79M12, LM79M15

FEATURES

- Thermal, Short Circuit and Safe Area **Protection**
- **High Ripple Rejection**
- 0.5A Output Current
- 4% Tolerance on Preset Output Voltage

DESCRIPTION

The LM79MXX series of 3-terminal regulators is available with fixed output voltages of -5V, -12V, and -15V. These devices need only one external component—a compensation capacitor at the output. The LM79MXX series is packaged in the TO-220 power package, and is capable of supplying 0.5A of output current.

These regulators employ internal current limiting, safe area protection, and thermal shutdown for protection against virtually all overload conditions.

Low ground pin current of the LM79MXX series allows output voltage to be easily boosted above the preset value with a resistor divider. The low quiescent current of these devices with a specified maximum change with line and load ensures good regulation in the voltage boosted mode.

For output voltage other than -5V, -12V, and -15V the LM137 series provides an output voltage range from -1.2V to -57V.

Connection Diagram

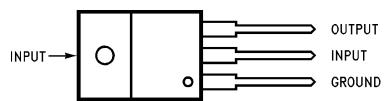


Figure 1. TO-220 Plastic Package (NDE) **Front View** See Package Number NDE0003B





These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings (1)(2)

Input Voltage	V _O = −5V	-25V
	V _O = −12V, −15V	-35V
Input/Output Differential	V _O = −5V	25V
	V _O = −12V, −15V	30V
Power Dissipation (3)	Internally Limited	
Operating Junction Temperature Range	0°C to +125°C	
Storage Temperature Range		−65°C to +150°C
Lead Temperature (Soldering, 10 sec.)	230°C	
ESD Susceptibility	TBD	

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.
- (2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
- (3) Refer to Typical Performance Characteristics and Design Considerations for details.

Electrical Characteristics LM79M05C

Conditions unless otherwise noted: $I_{OUT} = 350 mA$, $C_{IN} = 2.2 \mu F$, $C_{OUT} = 1 \mu F$, $0^{\circ}C \leq T_{J} \leq +125^{\circ}C$

Part Number				LM79M05C				
Output Voltag	ge		-5V					
Input Voltage	(Unless Otherwise Specified)			-10V				
Symbol	Parameter	Conditions	Min	Тур	Max			
Vo	Output Voltage	T _J = 25°C	-4.8	-5.0	-5.2	V		
		5mA ≤ I _{OUT} ≤ 350mA	-4.75		-5.25	V		
			(-	-25 ≤ V _{IN} ≤ -	-7)			
ΔV_{O}	Line Regulation	$T_J = 25^{\circ}C^{(1)}$		8	50	mV		
			(-	-25 ≤ V _{IN} ≤ -	-7)			
				2	30	mV		
			(-	-18 ≤ V _{IN} ≤ -	8)			
ΔV_{O}	Load Regulation	$T_J = 25^{\circ}C, (1)$		30	100	mV		
		5mA ≤ I _{OUT} ≤ 0.5A						
lQ	Quiescent Current	T _J = 25°C		1	2	mA		
ΔI_Q	Quiescent Current	With Input Voltage		0.4				
	Change		(-	-25 ≤ V _{IN} ≤ -	-8)			
		With Load,						
		5mA ≤ I _{OUT} ≤ 350mA			0.4	mA		
V _n	Output Noise Voltage	$T_A = 25^{\circ}C$,		150		μV		
		10Hz ≤ f ≤ 100Hz						
	Ripple Rejection	f = 120Hz	54	66		dB		
			(-					
Dropout Voltage		T _J = 25°C, I _{OUT} = 0.5A		1.1		V		
I _{OMAX}	Peak Output Current	T _J = 25°C		800		mA		
	Average Temperature	$I_{OUT} = 5mA$,						
	Coefficient of Output Voltage	0°C ≤ T _J ≤ 100°C		-0.4		mV/°C		

⁽¹⁾ Regulation is measured at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

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Electrical Characteristics LM79M12C, LM79M15C

Conditions unless otherwise noted: I_{OUT} = 350mA, C_{IN} = 2.2 μ F, C_{OUT} = 1 μ F, 0°C ≤ T_J ≤ +125°C

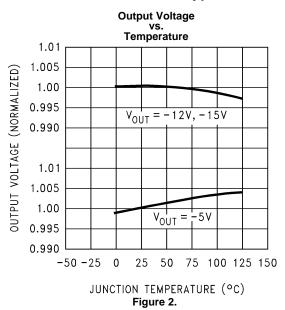
Part Nu	mber		LM79M12C LM79M15C						
Output	Voltage		-12V -19V				Units		
Input V	oltage (Unless Otherwise	e Specified)				-23V			
Symbo I	Parameter	Conditions	Conditions Min		Тур Мах		Min Typ		
Vo	Output Voltage	T _J = 25°C	-11.5	-12.0	-12.5	-14.4	-15.0	-15.6	V
		5 mA ≤ I _{OUT} ≤ 350mA	-11.4		-12.6	-14.25		-15.75	V
			(-2	7 ≤ V _{IN} ≤ -	14.5)	(-30) ≤ V _{IN} ≤ -	10.5)	
ΔV_{O}	Line Regulation	$T_J = 25^{\circ}C^{(1)}$		5	80		5	80	mV
			(-30) ≤ V _{IN} ≤ -	14.5)	(-3	0 ≤ V _{IN} ≤−1	7.5)	
				3	50		3	50	mV
			(-2	5 ≤ V _{IN} ≤ −	15)	(-2	28 ≤ V _{IN} ≤ -	-18)	
ΔV _O	Load Regulation	$T_J = 25^{\circ}C, (1)$		30	240		30	240	mV
		$5mA \le I_{OUT} \le 0.5A$							
IQ	Quiescent Current	T _J = 25°C		1.5	3		1.5	3	mA
ΔI_Q	Quiescent Current	With Input Voltage			0.4			0.4	mA
	Change		$(-30 \le V_{IN} \le -14.5)$		$(-30 \le V_{IN} \le -27)$] !		
		With Load,							
		$5mA \le I_{OUT} \le 350mA$			0.4			0.4	mA
V _n	Output Noise	$T_A = 25^{\circ}C$,		400			400		μV
	Voltage	10Hz ≤ f ≤ 100Hz							
	Ripple Rejection	f = 120Hz	54	70		54	70		dB
			(-2	$(-25 \le V_{IN} \le -15)$		$(-30 \le V_{IN} \le -17.5)$			
	Dropout Voltage	$T_J = 25$ °C, $I_{OUT} = 0.5$ A		1.1			1.1		V
I _{OMAX}	Peak Output Current	$T_J = 25^{\circ}C$		800			800		mA
	Average Temperature	$I_{OUT} = 5mA,$							
	Coefficient of Output Voltage	0°C ≤ T _J ≤ 100°C		-0.8			-1.0		mV/°C

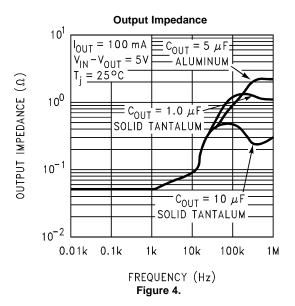
⁽¹⁾ Regulation is measured at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

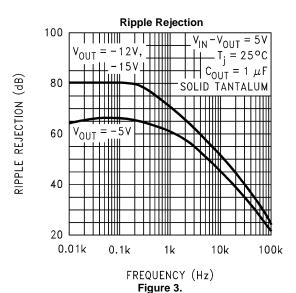
Product Folder Links: LM79M05 LM79M12 LM79M15

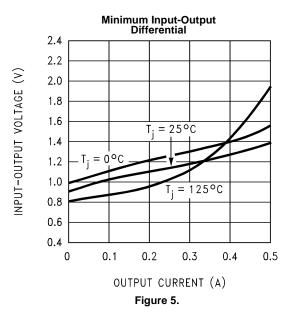


Typical Performance Characteristics



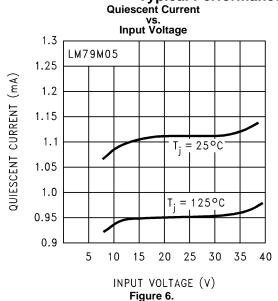


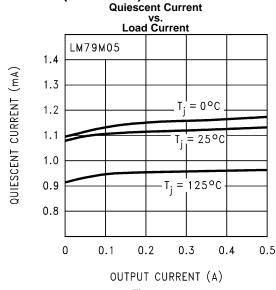




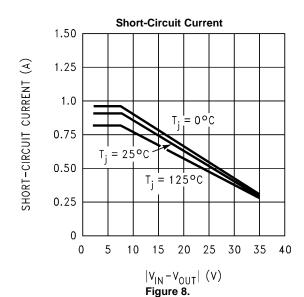


Typical Performance Characteristics (continued)











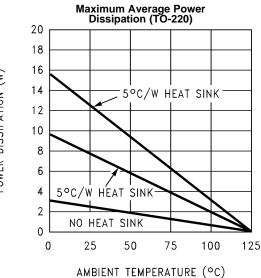


Figure 9.



Schematic Diagrams

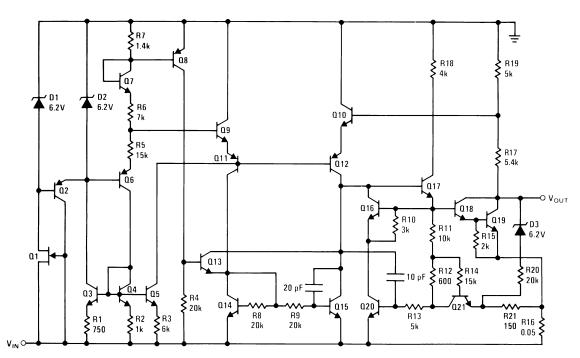


Figure 10. -5V

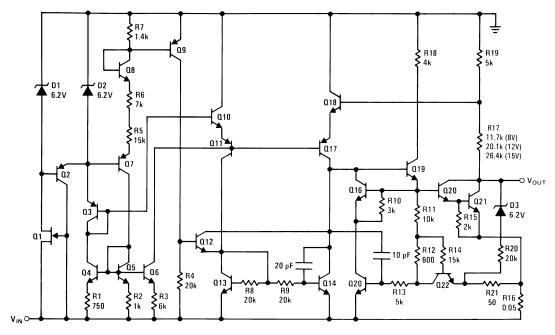


Figure 11. -12V and -15V



DESIGN CONSIDERATIONS

The LM79MXX fixed voltage regulator series have thermal-overload protection from excessive power, internal short-circuit protection which limits the circuit's maximum current, and output transistor safe-area compensation for reducing the output current as the voltage across the pass transistor is increased.

Although the internal power dissipation is limited, the junction temperature must be kept below the maximum specified temperature in order to meet data sheet specifications. To calculate the maximum junction temperature or heat sink required, the following thermal resistance values should be used:

Package	θ _{JC}	θ_{JA}
	(°C/W)	(°C/W)
TO-220	3	40

$$P_{DMAX} = \frac{T_{JMax} - T_{A}}{\theta_{JC} + \theta_{CA}} \text{ or}$$

$$= \frac{T_{JMax} - T_{A}}{\theta_{JA}} \text{ (Without a Heat Sink)}$$
(1)

 $\theta_{CA} = \theta_{CS} + \theta_{SA}$

Solving for T_J:

$$T_J$$
 = T_A + P_D (θ_{JC} + θ_{CA}) or
= T_A =+ $P_D\theta_{JA}$ (Without a Heat Sink)

Where

 T_J = Junction Temperature

T_A = Ambient Temperature

P_D = Power Dissipation

 θ_{JC} = Junction-to-Case Thermal Resistance

 θ_{CA} = Case-to-Ambient Thermal Resistance

 θ_{CS} = Case-to-Heat Sink Thermal Resistance

 θ_{SA} = Heat Sink-to-Ambient Thermal Resistance

 θ_{1A} = Junction-to-Ambient Thermal Resistance

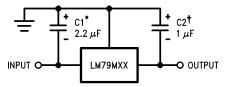
Typical Applications

Bypass capacitors are necessary for stable operation of the LM79MXX series of regulators over the input voltage and output current ranges. Output bypass capacitors will improve the transient response of the regulator.

The bypass capacitors ($2.2\mu\text{F}$ on the input, $1.0\mu\text{F}$ on the output), should be ceramic or solid tantalum which have good high frequency characteristics. If aluminum electrolytics are used, their values should be $10\mu\text{F}$ or larger. The bypass capacitors should be mounted with the shortest leads, and if possible, directly across the regulator terminals.

Product Folder Links: LM79M05 LM79M12 LM79M15



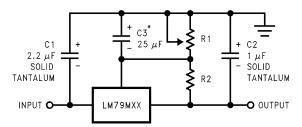


*Required if regulator is separated from filter capacitor by more than 3". For value given, capacitor must be solid tantalum. 25µF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25µF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of $100\mu F$, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

Figure 12. Fixed Regulator



*Improves transient response and ripple rejection.

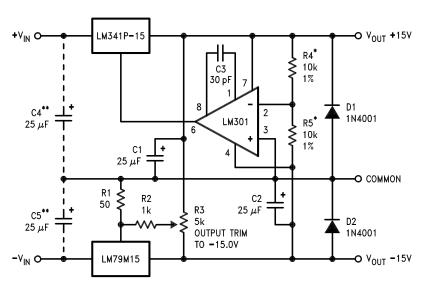
Do not increase beyond 50µF.

$$V_{OUT} = V_{SET} \left(\frac{R1 + R2}{R2} \right)$$

Select R2 as follows:

 $\begin{array}{lll} LM79M05C & 300\Omega \\ LM79M12C & 750\Omega \\ LM79M15C & 1k \end{array}$

Figure 13. Variable Output



^{*}Resistor tolerance of R4 and R5 determine matching of (+) and (-) outputs.

Figure 14. ±15V, 1 Amp Tracking Regulators

^{**}Necessary only if raw supply filter capacitors are more than 3" from regulators.

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	Performar	Performance (Typical)			
	(-15)	(+15)			
Load Regulation at 0.5A	40mV	2 mV			
Output Ripple, $C_{IN} = 3000\mu F$, $I_L = 0.5A$	100 μVrms	100 μVrms			
Temperature Stability	50mV	50mV			
Output Noise 10Hz ≤ f ≤ 10kHz	150 μVrms	150 μVrms			

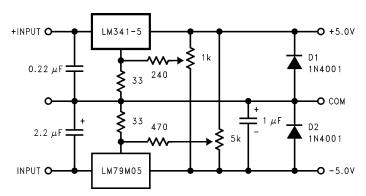


Figure 15. Dual Trimmed Supply

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REVISION HISTORY

Changes from Revision C (April 2013) to Revision D					
•	Changed layout of National Data Sheet to TI format		9		



PACKAGE OPTION ADDENDUM

25-Aug-2017

PACKAGING INFORMATION

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Orderable Device	Status	Package Type	_	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LM79M12CT	NRND	TO-220	NDE	3	45	TBD	Call TI	Call TI	0 to 125	LM79M 12CT	
LM79M12CT/NOPB	NRND	TO-220	NDE	3	45	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	0 to 125	LM79M 12CT	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

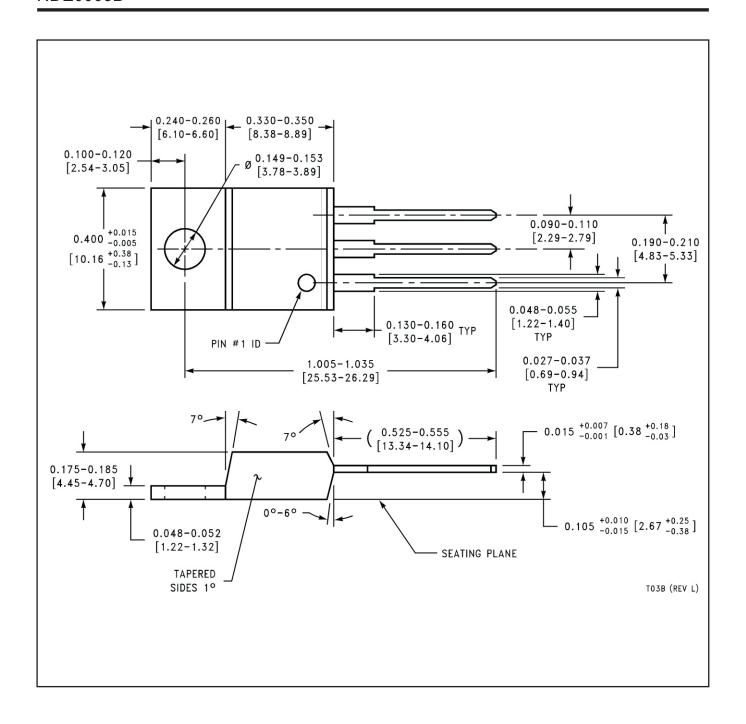
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