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ADC1213D125WO demoboard; compliant with Lattice, Altera, Xilinx FPGAboards through specific connectors

Status: **NRND**

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ADC1213D125WO
ADC1213D125WO demoboard; compliant with Lattice, Altera, Xilinx FPGA boards through specific connectors

Our ADC demonstration board is suitable for dynamic performances evaluation from low to high IF configuration. A FPGA mother board (Xilinx, Altera, and Lattice) could be connected to ease the evaluation and analysis of the ADC dynamic and enable usage of the JESD204A full features sets

Features

- SMA connector for clock and analog inputs signal
- On board Data clock generator
- On board demodulator + LO synthesizer
- Single POWER supply (on board regulators)



Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Sample & Buy
ADC1213D125WO-DB	NRND	BD0	BOARD	0	I	No	Check Availability

Documents

There are no public documents available for this product. [Click here to request documentation.](#)

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Product ID	Title
ADC1213D125HN	Dual 12-bit ADC; 125 Mbps

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