

HDSL2 TRANSFORMER

For Level One Chipset



- Matched to Level One SK70740/SK70741/SK70742 HDSL2 Chipset
- Excellent THD performance
- Maximum height .500" (12,70 mm)

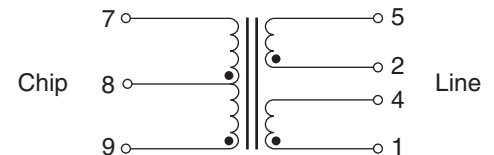
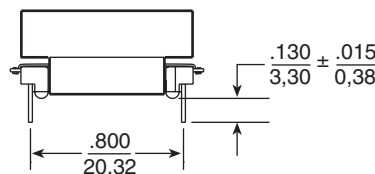
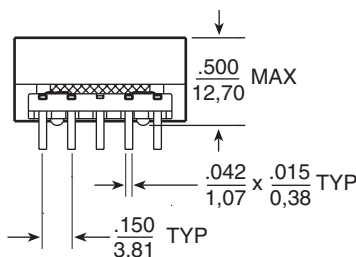
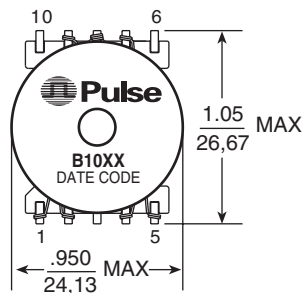
Electrical Specifications @ 25°C — Operating Temperature -40°C to +85°C

Part Number	Turns Ratio Chip-Line (±2%)	OCL @ 10 KHz, 100 mV (1-5) with (2-4) shorted	Longitudinal Balance	THD (10 KHz @ 4.95 Vrms)	DC Resistance (7-9), (4-1), (5-2)	Isolation Voltage (Vrms)
B1019	1:2	1.76 mH (±10%)	>50 dB @ 20 KHz to 600 KHz	77 dB MIN	0.60 Ω MAX	2000
B1042	1:2.3	1.76 mH (±10%)	>43 dB @ 20 KHz to 400 KHz	77 dB MIN	0.60 Ω MAX	2000

Mechanical

Schematic

B1019/B1042



Weight.....14.0 grams
Tray45/tray

Dimensions: Inches
mm
Unless otherwise specified,
all tolerances are ± .010
0,25

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