

Products > ADC1415S125F1 demo board; CMOS outputs

ADC1415S125F1 🚫 Obsolete

ADC1415S125F1 demo board; CMOS outputs

IDT's ADC1415S125F1 demo board is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS output. A data acquisition board can be used to easily analyze the ADC performance in the design and prototyping phase.

Features

- SMA connector for clock and analog inputs signal
- CMOS (buffered outputs)
 - Single power supply (on-board regulators)

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Buy Sample
ADC1415S125F1-DB	Obsolete	BD0	BOARD	0	I	No	Availability

Technical Documentation

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Title	Format	File Size	Date
User Guides & Manuals			
Demo Board Software ADC_1_r04	ZIP	2.33 MB	Jul 18, 2012

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Images

Evaluation Board - No Image Available

