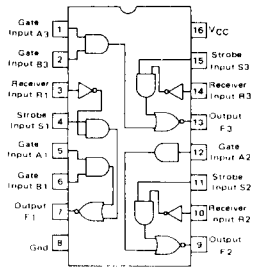


MC8T14 **MC8T24**

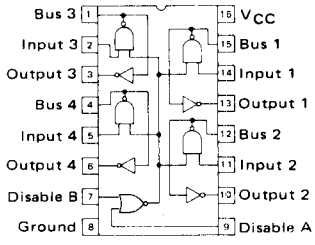
0 to 70°C
L Suffix — Case 620
P Suffix — Case 648

MC8T14 is a triple-hysteresis-equipped receiver. Specified for general TTL Systems.

MCT24 is a triple hysteresis-equipped receiver specified to meet IBM System requirements.

Device Number	$V_{H(R)}$ Volts Min.	$I_{IH(R)}$ @ $V_{IH(R)} = 3.8$ V @ $V_{IH(R)} = 3.11$ V* mA Max.	$t_{PLH(R)}$ @ $C_L = 15$ pF ns Max.
MC8T14	0.3	0.17	30
MC8T24	0.2	0.17*	30

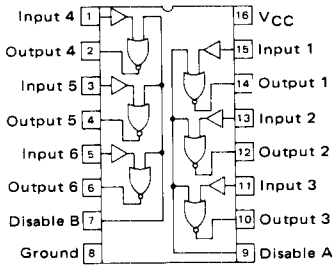
MINICOMPUTER BUS



MC3438
0 to 70°C
L Suffix — Case 620
P Suffix — Case 648

Quad Transceivers with hysteresis-equipped receivers and open-collector driver outputs which permit wire-OR connection (DM8838 equivalent).

Receiver Hysteresis Volts Min.	$V_{L(BUS)}$ @ I_{BUS} = 50 mA Volts Max.	I_{BUS} @ $V_{IH(BUS)}$ = 4.0 V μA Max.	$t_{PLH(D)}$ @ C_L = 15 pF ns Max.	$t_{PLH(R)}$ @ C_L = 15 pF ns Max.
0.25	0.7	100	25	30



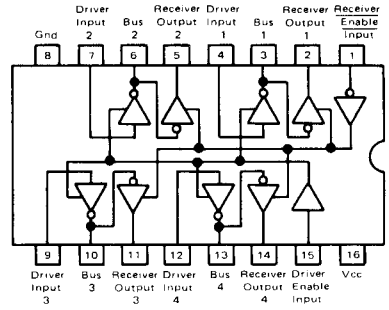
MC3437
0 to 70°C
L Suffix — Case 620
P Suffix — Case 648

Hex Receivers with Hysteresis for improved noise immunity (DM8837 equivalent)

$I_{I(R)}$ @ $V_{I(R)} = 4.0$ V μA Max.	Hysteresis Volts Min.	$t_{PLH(R)}$ @ $C_L = 15$ pF ns Max.
50	0.5	30

MICROPROCESSOR BUS

The revolutionary "Computer on a Chip" is another bus organized system. The requirements on the microprocessor bus are especially stringent. Generally, microprocessors (MPUs) are fabricated utilizing MOS technology with its attendant high circuit density characteristics. However, MOS structures become unduly large when it is necessary to conduct large amounts of current. Therefore it is necessary that each of the elements attached to the MPU systems can tolerate a total loading equal to only about one conventional TTL load.

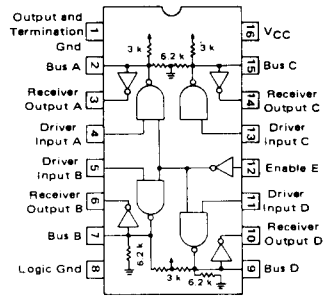


MC8T26/MC6880
0 to +75°C
L Suffix — Case 620
P Suffix — Case 648
Quad three-state bus transceiver

I_{IH} and I_{IL} Input Current (Either Logic State) μA	I_{OHL} Output Disabled Leakage Current — High Logic State Max. μA	t_{PLH}, t_{PHL} Propag. Delay Time — High to Low or Low to High ns Max.
200	100	17

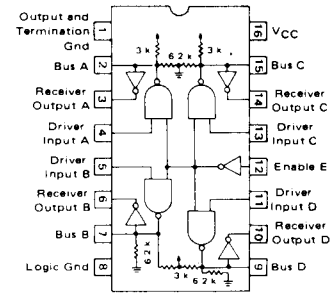
INSTRUMENTATION BUS

Steps toward standardization of the instrumentation interface bus have been made by the International Electrotechnical Commission (IEC) and the IEEE with Standard 488-1975. Acceptance of these standards will permit interconnection of many types of measurement apparatus, manufactured by numerous firms, into complex systems simply by plugging in connecting cables.



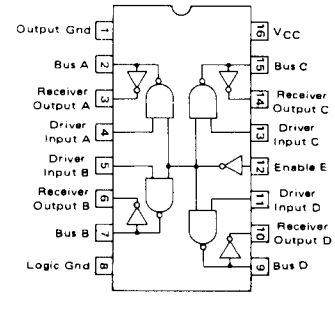
MC3440
0 to 70°C
P Suffix — Case 648

Quad Transceivers with 3 Drivers Sharing a Common Enable Input
R1 = 3.0 k (to V_{CC})
R2 = 6.2 k (to Gnd)



MC3441
0 to 70°C
P Suffix —Case 648

Quad Transceivers with all four drivers controlled by a Common-Enable Input
R1 = 3.0 k (to V_{CC})
R2 = 6.2 k (to Gnd)



MC3443
0 to 70°C
P Suffix — Case 648

Quad Transceivers without termination resistors. Functional equivalent to 75138