

EC2545TS-9.8304M
[Click part number to visit Part Number Details page](#)
REGULATORY COMPLIANCE (Data Sheet downloaded on Nov 10, 2018)
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**ITEM DESCRIPTION**

Quartz Crystal Clock Oscillators XO (SPXO) HCMOS/TTL (CMOS) 5.0Vdc 4 Pad 5.0mm x 7.0mm Ceramic Surface Mount (SMD) 9.8304MHz ± 50 ppm -10°C to +70°C

ELECTRICAL SPECIFICATIONS

Nominal Frequency	9.8304MHz
Frequency Tolerance/Stability	± 50 ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration)
Aging at 25°C	± 5 ppm/year Maximum
Operating Temperature Range	-10°C to +70°C
Supply Voltage	5.0Vdc $\pm 10\%$
Input Current	10mA Maximum (No Load)
Output Voltage Logic High (Voh)	2.4Vdc Minimum with (LS)TTL Load, Vdd-0.5Vdc Minimum with CMOS Load
Input Current Logic High (Ioh)	-4mA
Output Voltage Logic Low (Vol)	0.4Vdc Maximum with (LS)TTL Load, 0.5Vdc Maximum with CMOS Load
Input Current Logic Low (Iol)	4mA
Rise/Fall Time	10nSec Maximum (Measured at 0.4Vdc to 2.4Vdc with (LS)TTL Load; Measured at 10% to 90% of waveform with CMOS Load)
Duty Cycle	50 ± 10 (%) (Measured at 50% of waveform with CMOS Load or at 1.4Vdc with (LS)TTL Load)
Load Drive Capability	Low Drive (10LSTTL Load or 30pF CMOS Load Maximum)
Output Logic Type	CMOS
Pin 1 Connection	Tri-State (High Impedance)
Tri-State Input Voltage (Vih and Vil)	+2.0Vdc Minimum to enable output, +0.8Vdc Maximum to disable output (High Impedance), No Connect to enable output.
RMS Phase Jitter	1pSec Maximum (12kHz to 20MHz offset frequency)
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Flammability	UL94-V0
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A

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Technical drawings of the Marking Orientation (MO) component showing top, side, and front views with dimensions.

Top View:

- Overall width: 7.00 ± 0.15
- Overall height: 5.00 ± 0.15
- Text: MARKING ORIENTATION
- Feature 1: A small circular hole in the bottom-left corner.

Side View:

- Overall height: $1.6 + 0.2 / - 0.5$

Front View:

- Overall width: 5.08 ± 0.15
- Overall height: 2.60 ± 0.15
- Feature 1: A small rectangular notch in the bottom-right corner.
- Feature 2: A small rectangular notch in the bottom-left corner.
- Feature 3: A small rectangular notch in the top-left corner.
- Feature 4: A small rectangular notch in the top-right corner.
- Dimension 1: 1.4 ± 0.1 (width of the bottom-left notch)
- Dimension 2: 3.68 ± 0.15 (width of the bottom-right notch)
- Dimension 3: 1.2 ± 0.2 (height of the bottom-right notch)

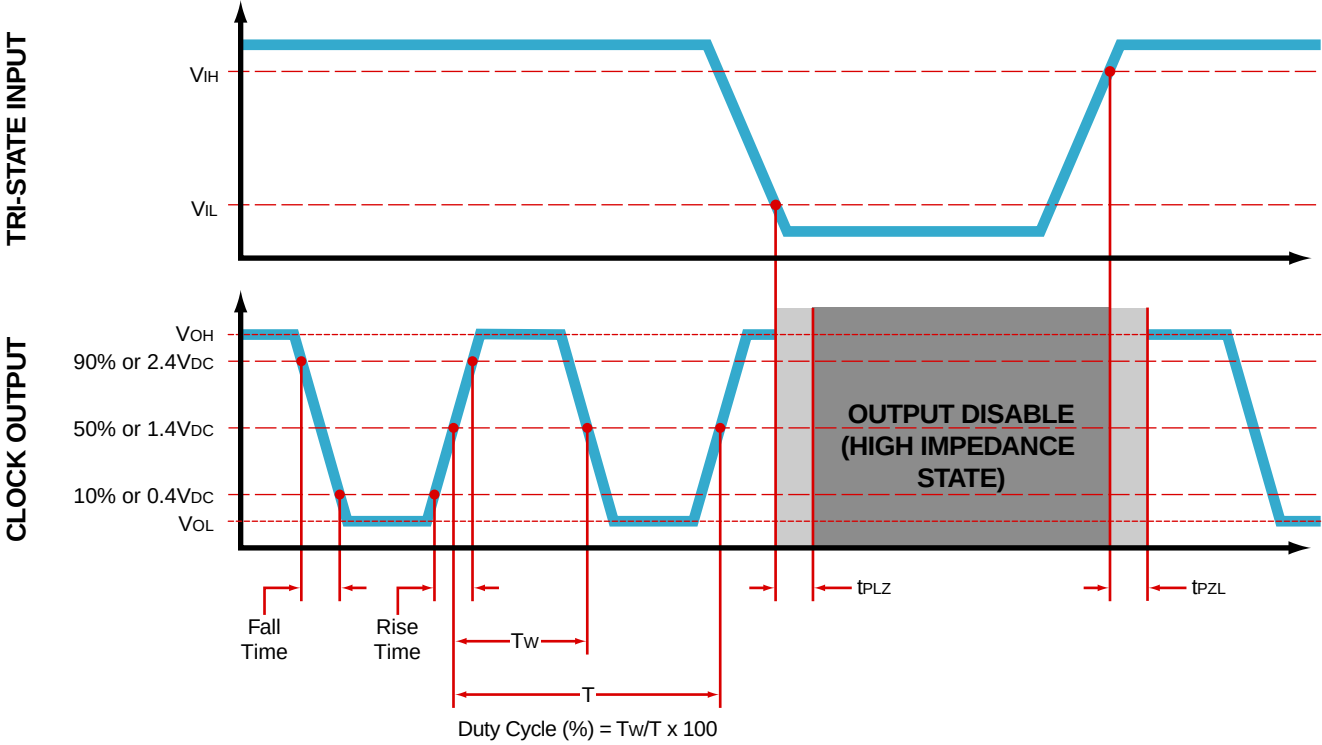
LINE	MARKING
1	ECLIPTEK
2	9.8304M
3	XXXXXX XXXXXX=Ecliptek Manufacturing Identifier

1-800-ECLIPTEK or 714.433.1200

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OUTPUT WAVEFORM & TIMING DIAGRAM

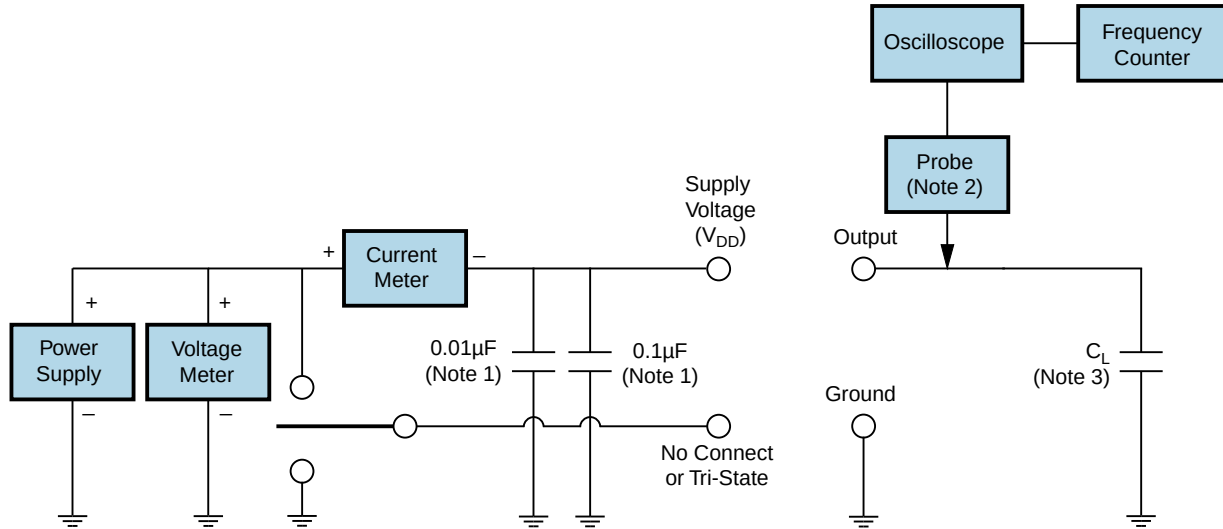


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Test Circuit for CMOS Output



Note 1: An external $0.1\mu\text{F}$ low frequency tantalum bypass capacitor in parallel with a $0.01\mu\text{F}$ high frequency ceramic bypass capacitor close to the package ground and V_{DD} pin is required.

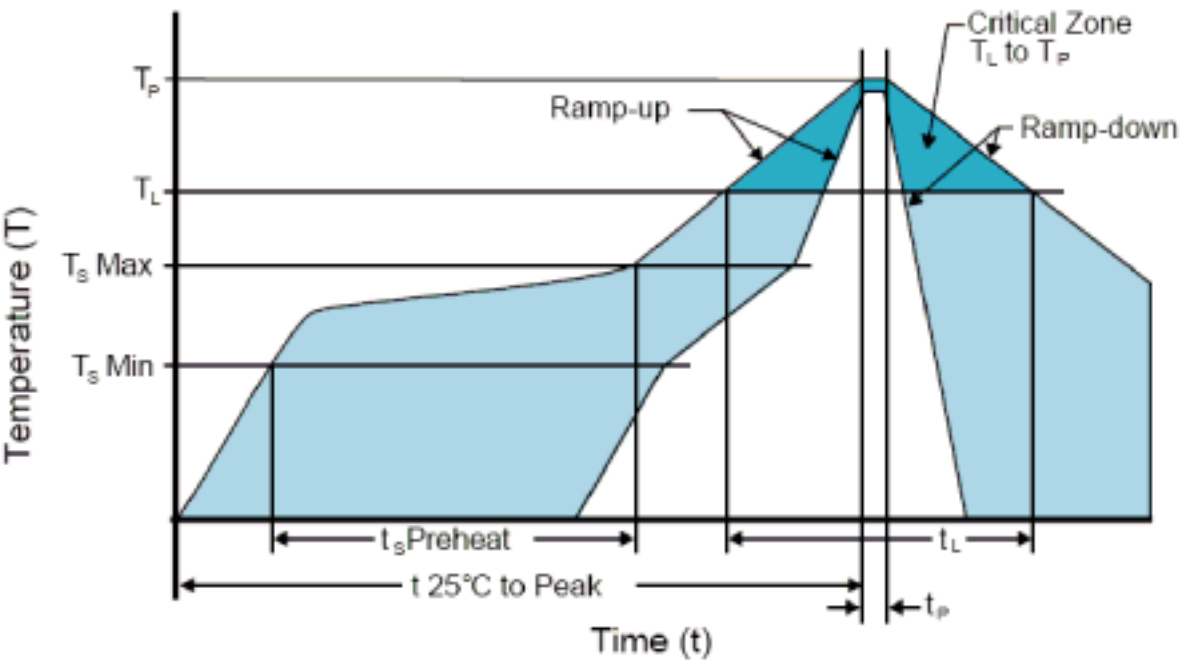
Note 2: A low capacitance ($<12\text{pF}$), 10X attenuation factor, high impedance ($>10\text{Mohms}$), and high bandwidth ($>300\text{MHz}$) passive probe is recommended.

Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance.

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Recommended Solder Reflow Methods



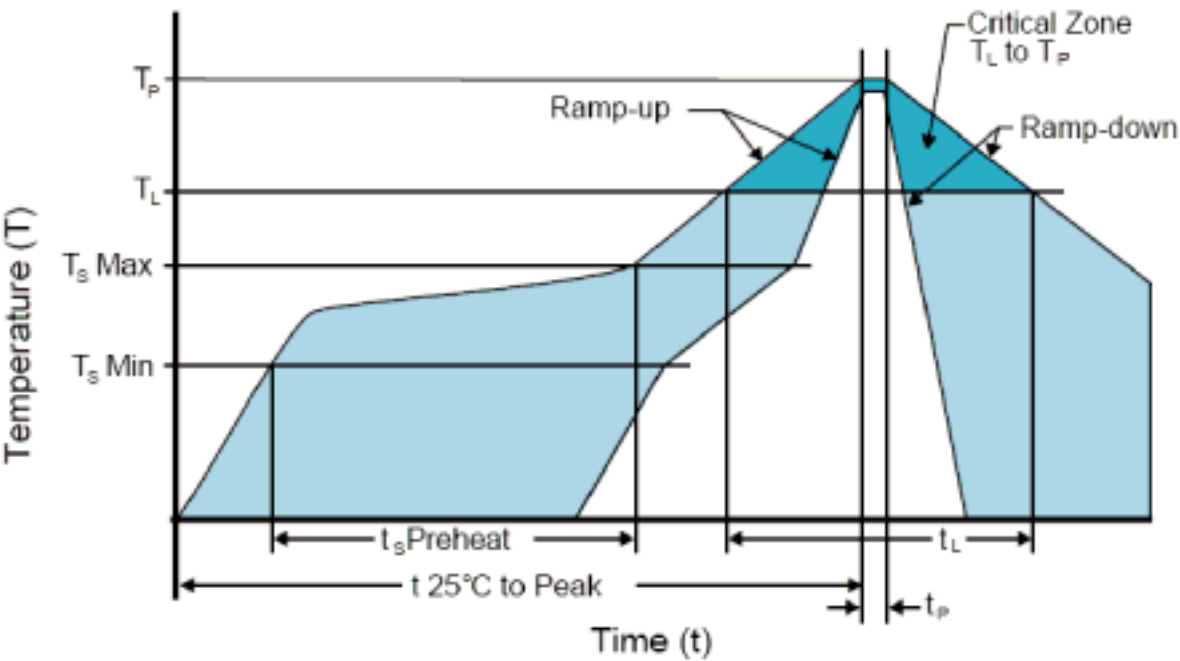
High Temperature Infrared/Convection

TS MAX to TL (Ramp-up Rate)	3°C/Second Maximum
Preheat	
- Temperature Minimum (TS MIN)	150°C
- Temperature Typical (TS TYP)	175°C
- Temperature Maximum (TS MAX)	200°C
- Time (ts MIN)	60 - 180 Seconds
Ramp-up Rate (TL to TP)	3°C/Second Maximum
Time Maintained Above:	
- Temperature (TL)	217°C
- Time (tL)	60 - 150 Seconds
Peak Temperature (TP)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (TP Target)	250°C +0/-5°C
Time within 5°C of actual peak (tp)	20 - 40 Seconds
Ramp-down Rate	6°C/Second Maximum
Time 25°C to Peak Temperature (t)	8 Minutes Maximum
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

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Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

TS MAX to TL (Ramp-up Rate)	5°C/Second Maximum
Preheat	
- Temperature Minimum (TS MIN)	N/A
- Temperature Typical (TS TYP)	150°C
- Temperature Maximum (TS MAX)	N/A
- Time (ts MIN)	60 - 120 Seconds
Ramp-up Rate (TL to TP)	5°C/Second Maximum
Time Maintained Above:	
- Temperature (TL)	150°C
- Time (tL)	200 Seconds Maximum
Peak Temperature (TP)	240°C Maximum
Target Peak Temperature (TP Target)	240°C Maximum 2 Times / 230°C Maximum 1 Time
Time within 5°C of actual peak (tP)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
Ramp-down Rate	5°C/Second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)