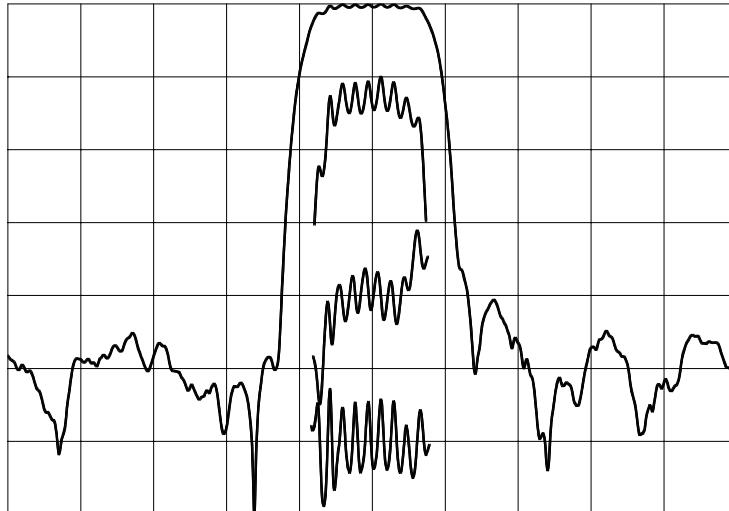


Micro Networks, 324 Clark Street, Worcester, MA 01606, USA tel: 508-852-5400, fax: 508-852-8456, [www.micronetworks.com](http://www.micronetworks.com)

**TYPICAL PERFORMANCE**
**PRELIMINARY**


Horizontal: 3 MHz/div

Vertical (from top):

Magnitude	10.1 dB/div
Phase Deviation	5 deg/div
Group Delay Variation	100 ns/div

**SPECIFICATION**

Parameter	Min	Typ	Max	Units
Center Frequency (Fc) <sup>1</sup>	69.85	70	70.15	MHz
Insertion Loss		7.1	8	dB
1 dB Bandwidth	3.4	4.1		MHz
3 dB Bandwidth	4	4.9		MHz
35 dB Bandwidth		7.25	8	MHz
Passband Ripple		0.6	1	dB
Phase Deviation from Linear <sup>2</sup>		4	7	deg
Group Delay Variation <sup>2</sup>		100	150	ns
Absolute Delay		0.95		μs
Substrate		LiNbO <sub>3</sub>		-
Temperature Coefficient of Frequency (Tc) <sup>3</sup>		-90		ppm/°C
Ambient Temperature		25		°C
System Source and Load Impedance		50		Ω

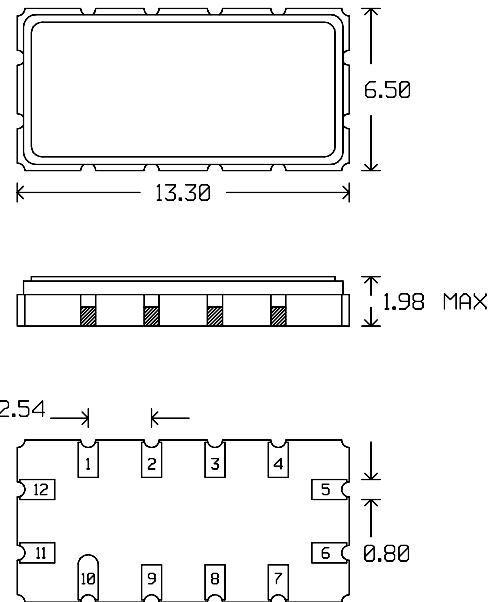
Notes:

1. Average of lower & upper 3 dB frequencies.
2. Evaluated over 70% of the 3 dB bandwidth.
3. Typical change of filter frequency response with temperature is  $\Delta f/f_{ref} = (T - T_{ref}) * T_c$  ppm.

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## PACKAGE OUTLINE

**PRELIMINARY**

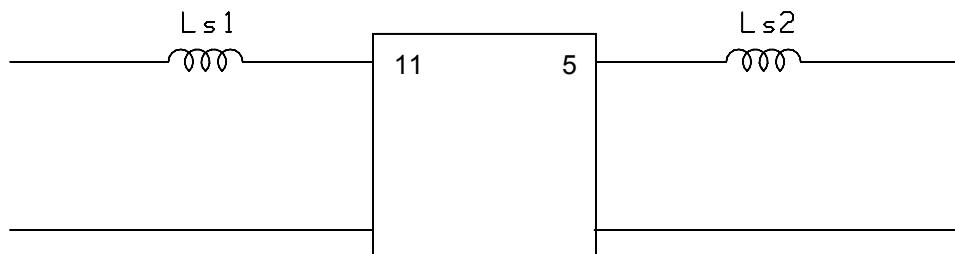


Units: mm

### Pin Configuration:

Input: 11  
Output: 5  
Ground: 1,2,3,4,6,7,8,9,10,11,12

## MATCHING CIRCUIT



Component values in  $50 \Omega$ :  $L_{s1} = 150 \text{ nH}$   $L_{s2} = 120 \text{ nH}$   
(Minimum Q = 45)

### Notes

- Optimum component values may change depending on board layout. The values shown here are intended as a guide only.