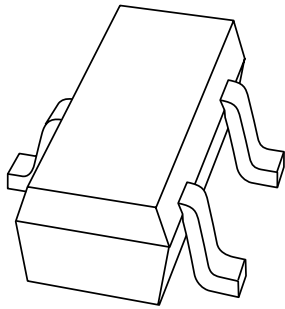


DATA SHEET



BFR92AT

NPN 5 GHz wideband transistor

Product specification
Supersedes data of 1999 Nov 02

2000 Mar 28

NPN 5 GHz wideband transistor

BFR92AT

FEATURES

- High power gain
- Gold metallization ensures excellent reliability
- SOT416 (SC-75) package.

APPLICATIONS

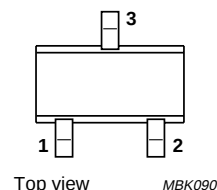
RF amplifiers, mixers and oscillators with signal frequencies up to 1 GHz.

DESCRIPTION

Silicon NPN transistor encapsulated in a plastic SOT416 (SC-75) package. The BFR92AT uses the same crystal as the SOT23 version: BFR92A.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



Marking code: P2.

Fig.1 SOT416.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	–	20	V
V_{CEO}	collector-emitter voltage	open base	–	–	15	V
I_C	collector current (DC)		–	–	25	mA
P_{tot}	total power dissipation	up to $T_s = 75\text{ °C}$; note 1	–	–	150	mW
h_{FE}	current gain	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$	40	90	–	
C_{re}	feedback capacitance	$I_C = 0$; $V_{CE} = 10\text{ V}$; $f = 1\text{ MHz}$; $T_{amb} = 25\text{ °C}$	–	0.35	–	pF
f_T	transition frequency	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 500\text{ MHz}$	3.5	5	–	GHz
G_{UM}	maximum unilateral power gain	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 1\text{ GHz}$; $T_{amb} = 25\text{ °C}$	–	14	–	dB
		$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 2\text{ GHz}$; $T_{amb} = 25\text{ °C}$	–	8	–	dB
F	noise figure	$I_C = 5\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 1\text{ GHz}$; $\Gamma_s = \Gamma_{opt}$	–	2	–	dB
T_j	junction temperature		–	–	150	°C

Note

1. T_s is the temperature at the soldering point of the collector pin.

NPN 5 GHz wideband transistor

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LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 60134).

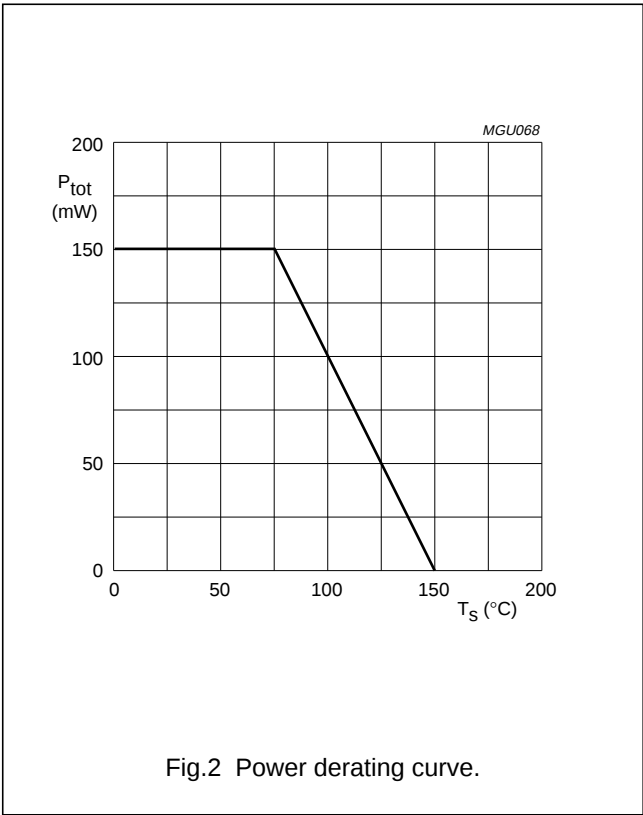
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	–	20	V
V _{CEO}	collector-emitter voltage	open base	–	15	V
V _{EBO}	emitter-base voltage	open collector	–	2	V
I _C	collector current (DC)		–	25	mA
P _{tot}	total power dissipation	up to T _s = 75 °C; see Fig.2; note 1	–	150	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C

Note

1. T_s is the temperature at the soldering point of the collector pin.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point	500	K/W



NPN 5 GHz wideband transistor

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CHARACTERISTICS

$T_j = 25\text{ °C}$; unless otherwise specified.

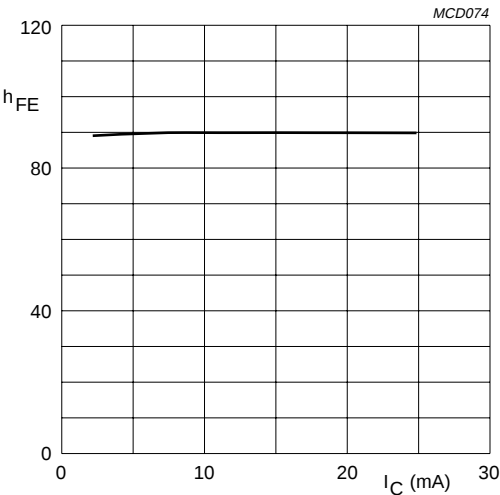
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CBO}	collector leakage current	$I_E = 0$; $V_{CB} = 10\text{ V}$	–	–	50	nA
h_{FE}	DC current gain	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$	40	90	–	
C_c	collector capacitance	$I_E = I_C = 0$; $V_{CB} = 10\text{ V}$; $f = 1\text{ MHz}$	–	0.6	–	pF
C_e	emitter capacitance	$I_C = I_E = 0$; $V_{EB} = 0.5\text{ V}$; $f = 1\text{ MHz}$	–	0.9	–	pF
C_{re}	feedback capacitance	$I_C = 0$; $V_{CE} = 10\text{ V}$; $f = 1\text{ MHz}$	–	0.35	–	pF
f_T	transition frequency	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 500\text{ MHz}$	3.5	5	–	GHz
G_{UM}	maximum unilateral power gain; note 1	$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 1\text{ GHz}$; $T_{amb} = 25\text{ °C}$	–	14	–	dB
		$I_C = 15\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 2\text{ GHz}$; $T_{amb} = 25\text{ °C}$	–	8	–	dB
F	noise figure	$I_C = 5\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 1\text{ GHz}$; $\Gamma_s = \Gamma_{opt}$	–	2	–	dB
		$I_C = 5\text{ mA}$; $V_{CE} = 10\text{ V}$; $f = 2\text{ GHz}$; $\Gamma_s = \Gamma_{opt}$	–	3	–	dB

Note

1. G_{UM} is the maximum unilateral power gain, assuming s_{12} is zero and $G_{UM} = 10 \log \frac{|s_{21}|^2}{(1 - |s_{11}|^2)(1 - |s_{22}|^2)}$ dB

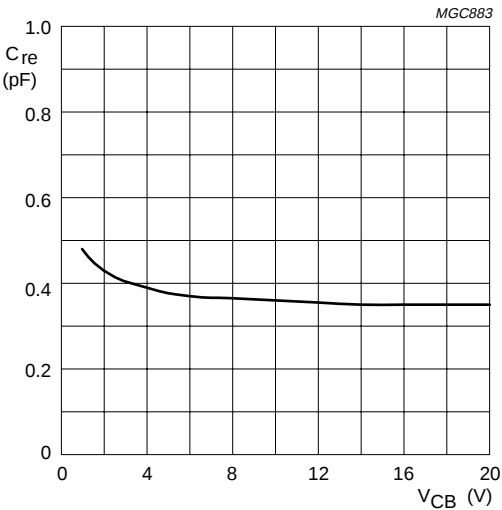
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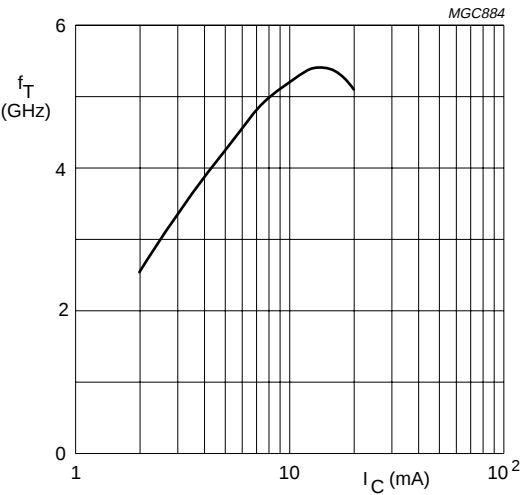
$V_{CE} = 10\text{ V}$.

Fig.3 DC current gain as a function of collector current; typical values.



$I_C = 0$; $f = 1\text{ MHz}$.

Fig.4 Feedback capacitance as a function of collector-base voltage; typical values.

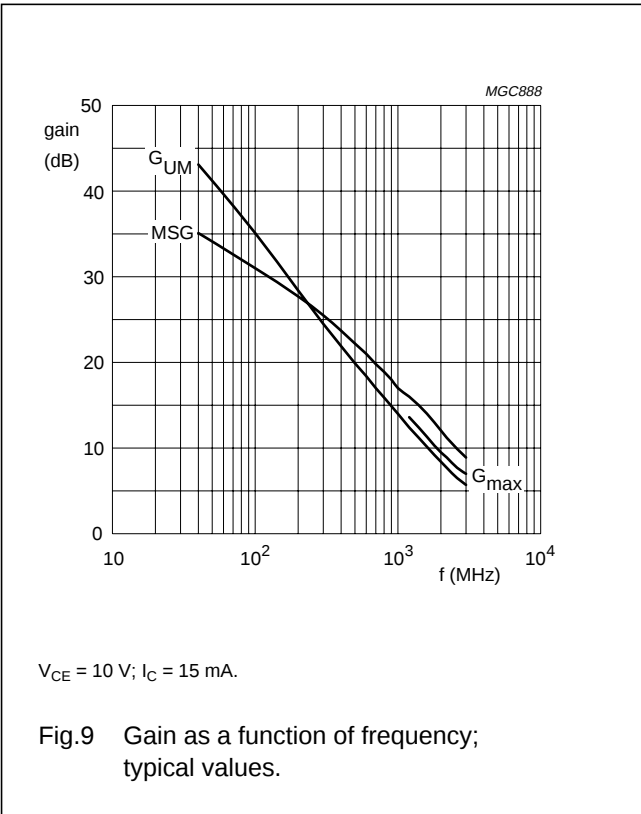
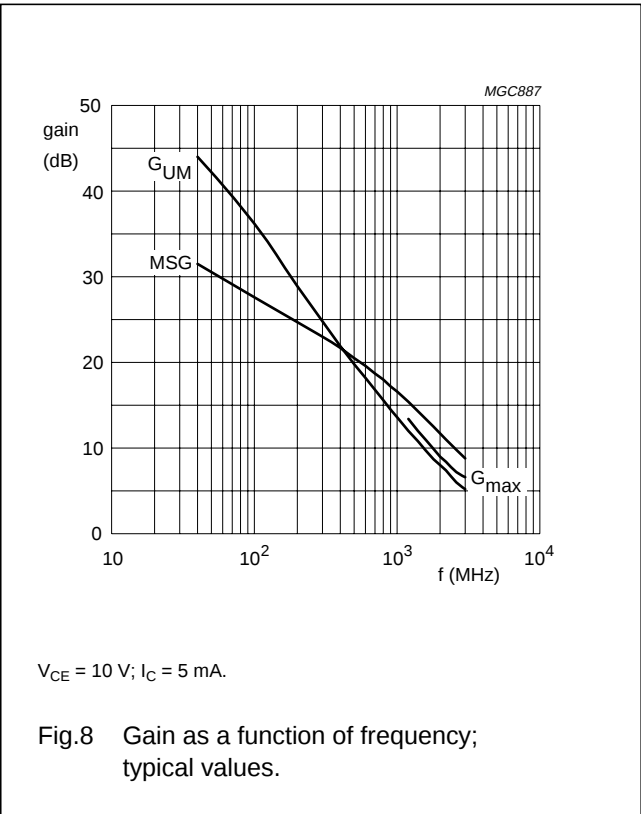
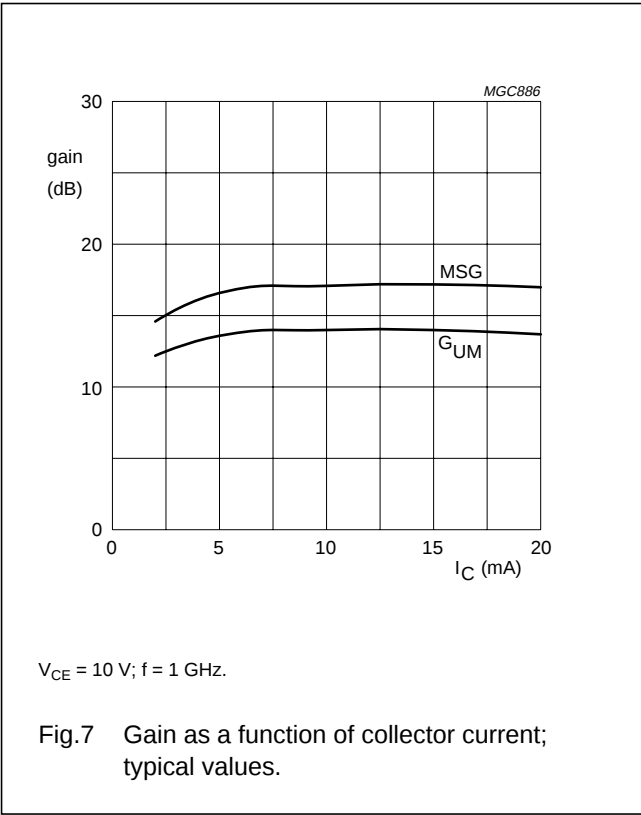
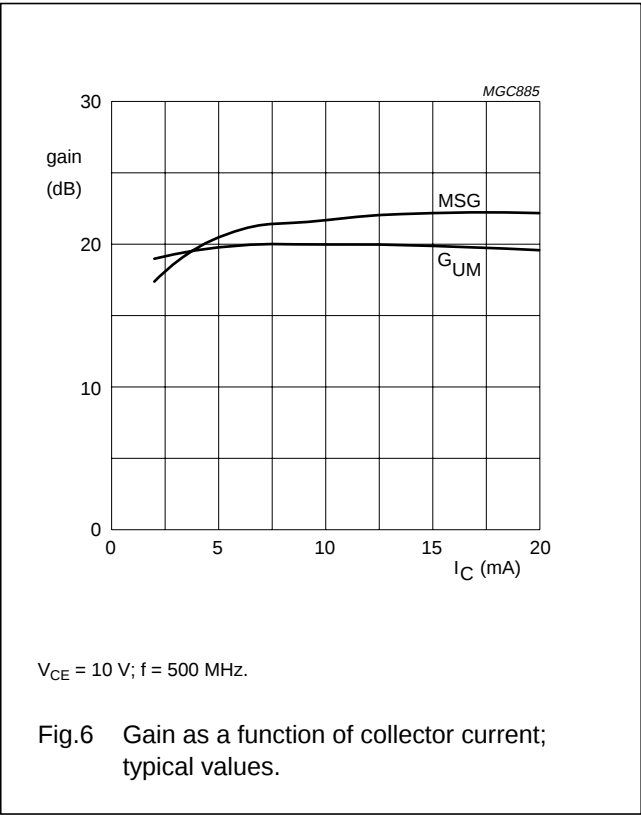


$V_{CE} = 5\text{ V}$; $f = 500\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$.

Fig.5 Transition frequency as a function of collector current; typical values.

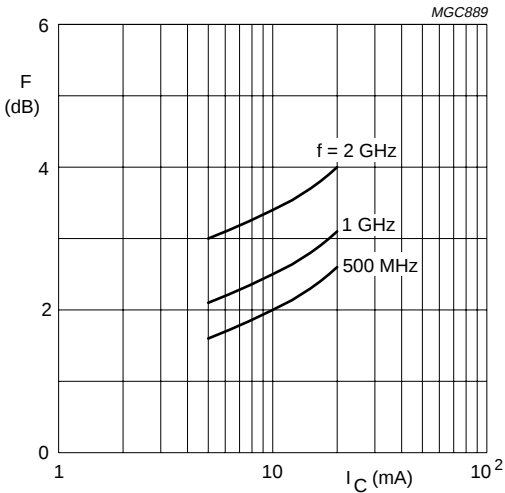
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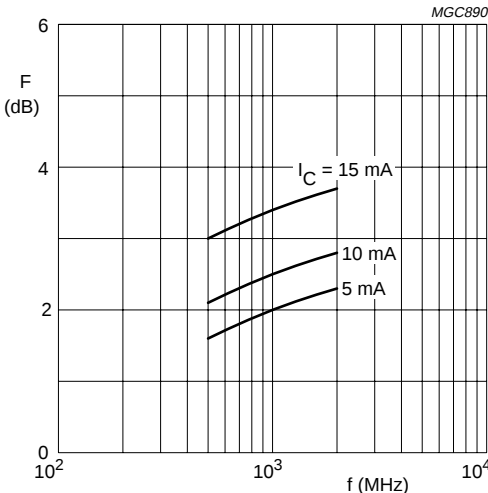
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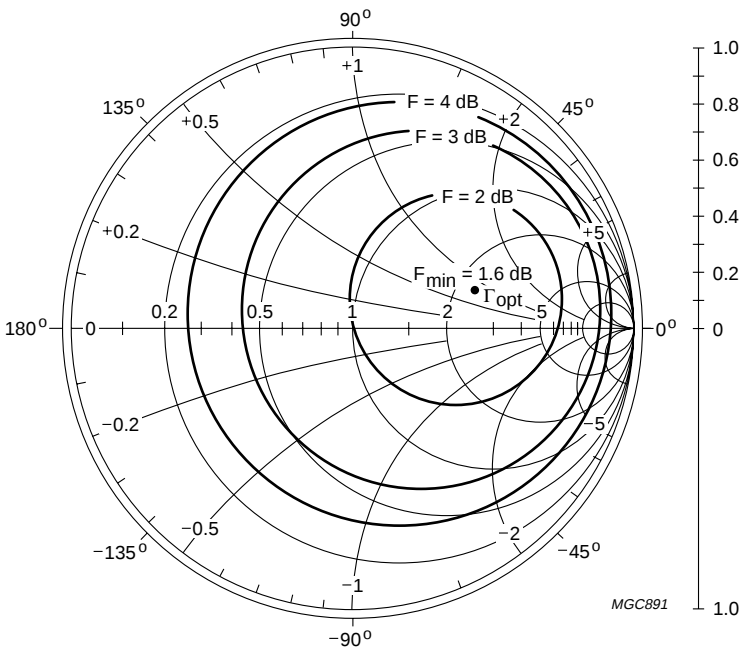
$V_{CE} = 10$ V.

Fig.10 Minimum noise figure as a function of collector current; typical values.



$V_{CE} = 10$ V.

Fig.11 Minimum noise figure as a function of frequency; typical values.



$f = 500$ MHz; $V_{CE} = 10$ V; $I_C = 5$ mA; $Z_0 = 50 \Omega$.

Fig.12 Common emitter noise figure circles; typical values.

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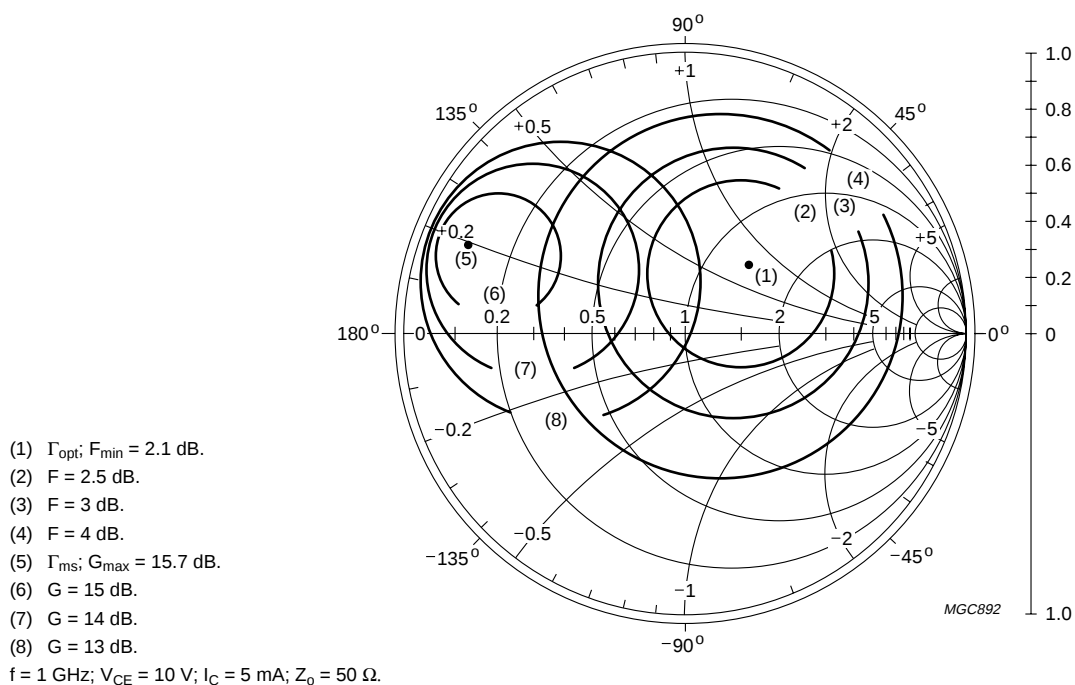


Fig.13 Common emitter noise figure circles; typical values.

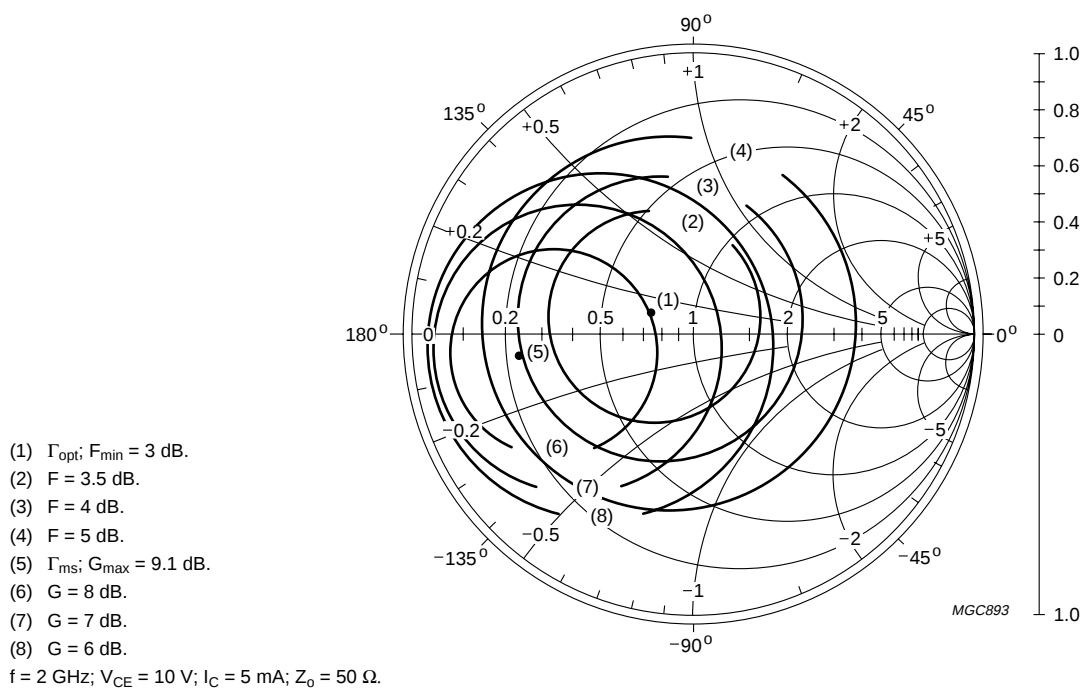
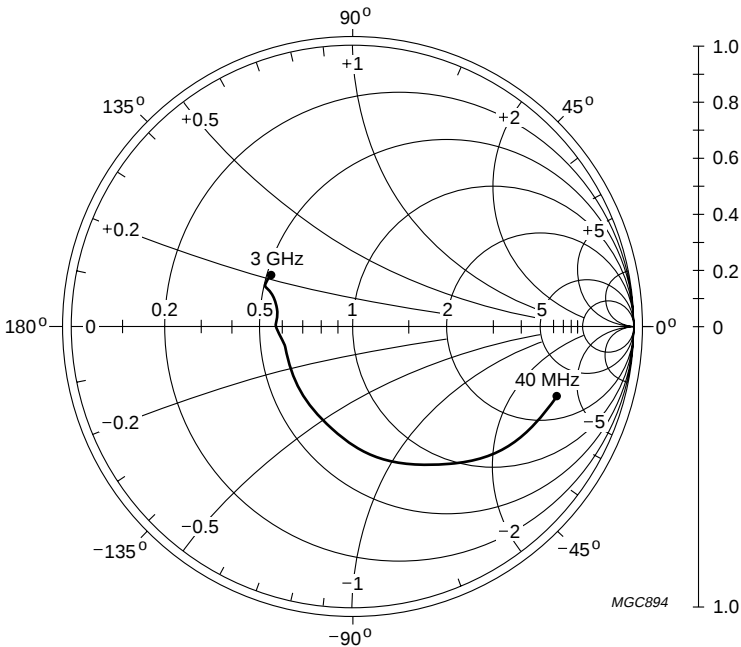


Fig.14 Common emitter noise figure circles; typical values.

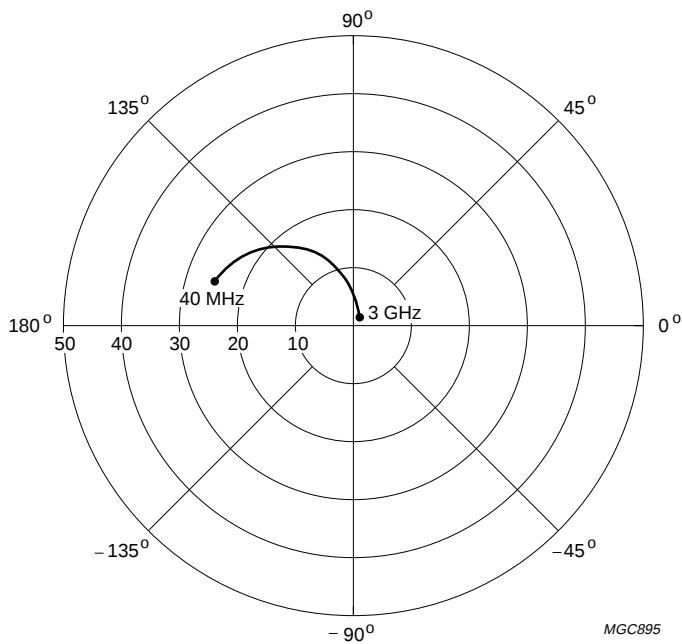
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$V_{CE} = 10\text{ V}$; $I_C = 15\text{ mA}$; $Z_0 = 50\ \Omega$.

Fig.15 Common emitter input reflection coefficient (s_{11}); typical values.

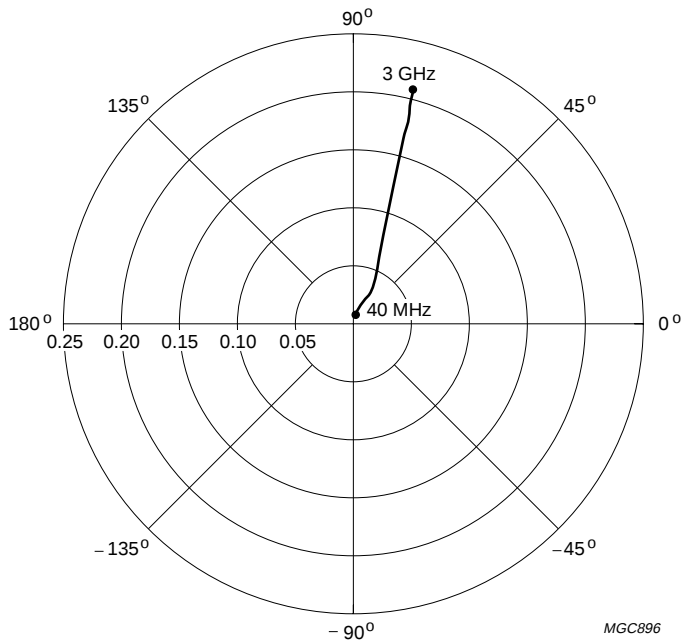


$V_{CE} = 10\text{ V}$; $I_C = 15\text{ mA}$.

Fig.16 Common emitter forward transmission coefficient (s_{21}); typical values.

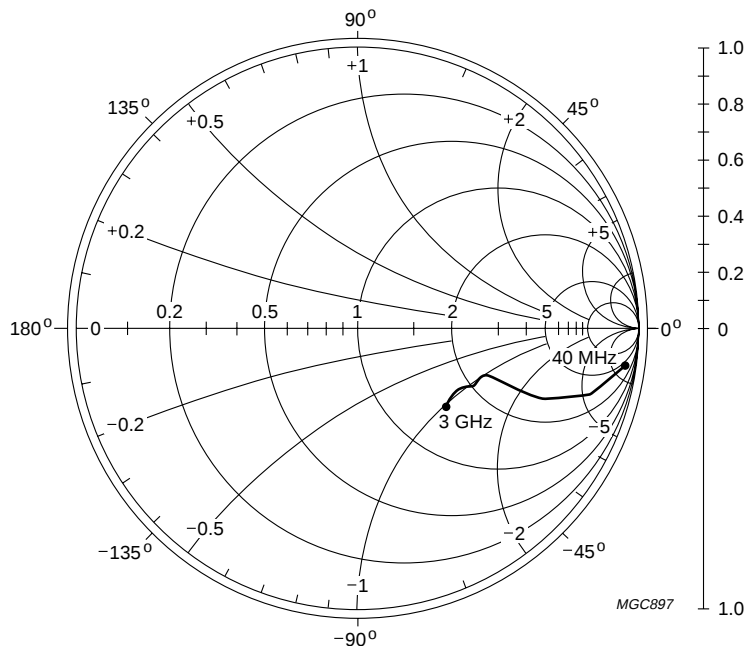
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$V_{CE} = 10\text{ V}; I_C = 15\text{ mA}$.

Fig.17 Common emitter reverse transmission coefficient (s_{12}); typical values.



$V_{CE} = 10\text{ V}; I_C = 15\text{ mA}; Z_0 = 50\ \Omega$.

Fig.18 Common emitter output reflection coefficient (s_{22}); typical values.

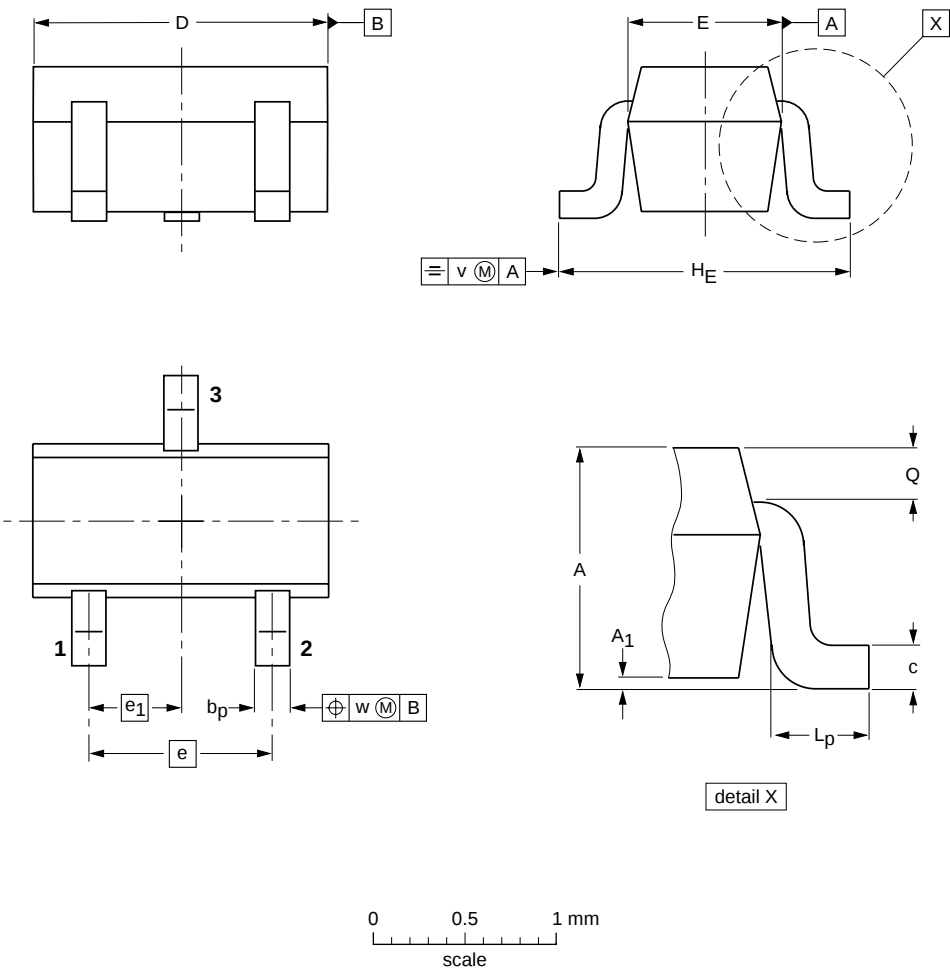
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PACKAGE OUTLINE

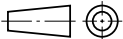
Plastic surface mounted package; 3 leads

SOT416



DIMENSIONS (mm are the original dimensions)

UNIT	A	A1 max	bp	c	D	E	e	e1	HE	Lp	Q	v	w
mm	0.95 0.60	0.1	0.30 0.15	0.25 0.10	1.8 1.4	0.9 0.7	1	0.5	1.75 1.45	0.45 0.15	0.23 0.13	0.2	0.2

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT416			SC-75			97-02-28

NPN 5 GHz wideband transistor

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DATA SHEET STATUS

DATA SHEET STATUS	PRODUCT STATUS	DEFINITIONS ⁽¹⁾
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

Note

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NOTES

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Philips Semiconductors – a worldwide company

Argentina: see South America

Australia: 3 Figtree Drive, HOMEBUSH, NSW 2140,
Tel. +61 2 9704 8141, Fax. +61 2 9704 8139

Austria: Computerstr. 6, A-1101 WIEN, P.O. Box 213,
Tel. +43 1 60 101 1248, Fax. +43 1 60 101 1210

Belarus: Hotel Minsk Business Center, Bld. 3, r. 1211, Volodarski Str. 6,
220050 MINSK, Tel. +375 172 20 0733, Fax. +375 172 20 0773

Belgium: see The Netherlands

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Bulgaria: Philips Bulgaria Ltd., Energoproject, 15th floor,
51 James Bourchier Blvd., 1407 SOFIA,
Tel. +359 2 68 9211, Fax. +359 2 68 9102

Canada: PHILIPS SEMICONDUCTORS/COMPONENTS,
Tel. +1 800 234 7381, Fax. +1 800 943 0087

China/Hong Kong: 501 Hong Kong Industrial Technology Centre,
72 Tat Chee Avenue, Kowloon Tong, HONG KONG,
Tel. +852 2319 7888, Fax. +852 2319 7700

Colombia: see South America

Czech Republic: see Austria

Denmark: Sydhavnsgade 23, 1780 COPENHAGEN V,
Tel. +45 33 29 3333, Fax. +45 33 29 3905

Finland: Sinikalliontie 3, FIN-02630 ESPOO,
Tel. +358 9 615 800, Fax. +358 9 6158 0920

France: 51 Rue Carnot, BP317, 92156 SURESNES Cedex,
Tel. +33 1 4099 6161, Fax. +33 1 4099 6427

Germany: Hammerbrookstraße 69, D-20097 HAMBURG,
Tel. +49 40 2353 60, Fax. +49 40 2353 6300

Hungary: see Austria

India: Philips INDIA Ltd, Band Box Building, 2nd floor,
254-D, Dr. Annie Besant Road, Worli, MUMBAI 400 025,
Tel. +91 22 493 8541, Fax. +91 22 493 0966

Indonesia: PT Philips Development Corporation, Semiconductors Division,
Gedung Philips, Jl. Buncit Raya Kav.99-100, JAKARTA 12510,
Tel. +62 21 794 0040 ext. 2501, Fax. +62 21 794 0080

Ireland: Newstead, Clonskeagh, DUBLIN 14,
Tel. +353 1 7640 000, Fax. +353 1 7640 200

Israel: RAPAC Electronics, 7 Kehilat Saloniki St, PO Box 18053,
TEL AVIV 61180, Tel. +972 3 645 0444, Fax. +972 3 649 1007

Italy: PHILIPS SEMICONDUCTORS, Via Casati, 23 - 20052 MONZA (MI),
Tel. +39 039 203 6838, Fax +39 039 203 6800

Japan: Philips Bldg 13-37, Kohnan 2-chome, Minato-ku,
TOKYO 108-8507, Tel. +81 3 3740 5130, Fax. +81 3 3740 5057

Korea: Philips House, 260-199 Itaewon-dong, Yongsan-ku, SEOUL,
Tel. +82 2 709 1412, Fax. +82 2 709 1415

Malaysia: No. 76 Jalan Universiti, 46200 PETALING JAYA, SELANGOR,
Tel. +60 3 750 5214, Fax. +60 3 757 4880

Mexico: 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905,
Tel. +9-5 800 234 7381, Fax +9-5 800 943 0087

Middle East: see Italy

Netherlands: Postbus 90050, 5600 PB EINDHOVEN, Bldg. VB,
Tel. +31 40 27 82785, Fax. +31 40 27 88399

New Zealand: 2 Wagener Place, C.P.O. Box 1041, AUCKLAND,
Tel. +64 9 849 4160, Fax. +64 9 849 7811

Norway: Box 1, Manglerud 0612, OSLO,
Tel. +47 22 74 8000, Fax. +47 22 74 8341

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Philippines: Philips Semiconductors Philippines Inc.,
106 Valero St. Salcedo Village, P.O. Box 2108 MCC, MAKATI,
Metro MANILA, Tel. +63 2 816 6380, Fax. +63 2 817 3474

Poland: Al.Jerozolimskie 195 B, 02-222 WARSAW,
Tel. +48 22 5710 000, Fax. +48 22 5710 001

Portugal: see Spain

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Russia: Philips Russia, Ul. Usatcheva 35A, 119048 MOSCOW,
Tel. +7 095 755 6918, Fax. +7 095 755 6919

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2092 JOHANNESBURG, P.O. Box 58088 Newville 2114,
Tel. +27 11 471 5401, Fax. +27 11 471 5398

South America: Al. Vicente Pinzon, 173, 6th floor,
04547-130 SÃO PAULO, SP, Brazil,
Tel. +55 11 821 2333, Fax. +55 11 821 2382

Spain: Balmes 22, 08007 BARCELONA,
Tel. +34 93 301 6312, Fax. +34 93 301 4107

Sweden: Kottbygatan 7, Akalla, S-16485 STOCKHOLM,
Tel. +46 8 5985 2000, Fax. +46 8 5985 2745

Switzerland: Allmendstrasse 140, CH-8027 ZÜRICH,
Tel. +41 1 488 2741 Fax. +41 1 488 3263

Taiwan: Philips Semiconductors, 6F, No. 96, Chien Kuo N. Rd., Sec. 1,
TAIPEI, Taiwan Tel. +886 2 2134 2886, Fax. +886 2 2134 2874

Thailand: PHILIPS ELECTRONICS (THAILAND) Ltd.,
209/2 Sanpavuth-Bangna Road Prakanong, BANGKOK 10260,
Tel. +66 2 745 4090, Fax. +66 2 398 0793

Turkey: Yukari Dudullu, Org. San. Blg., 2.Cad. Nr. 28 81260 Umraniye,
ISTANBUL, Tel. +90 216 522 1500, Fax. +90 216 522 1813

Ukraine: PHILIPS UKRAINE, 4 Patrice Lumumba str., Building B, Floor 7,
252042 KIEV, Tel. +380 44 264 2776, Fax. +380 44 268 0461

United Kingdom: Philips Semiconductors Ltd., 276 Bath Road, Hayes,
MIDDLESEX UB3 5BX, Tel. +44 208 730 5000, Fax. +44 208 754 8421

United States: 811 East Arques Avenue, SUNNYVALE, CA 94088-3409,
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