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MICROCOMPUTER

MN101C

MN101CF74G

Onboard Serial

Programming Manual

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Section 1 Connecting the PX-FW2

1.1 Connecting the PX-FW2

The MN101CF74G includes an on-chip 128 KB flash EEPROM that can be erased and written electrically. The Panasonic PX-FW2 is used to program this on-chip flash EEPROM with the MN101CF74G in the onboard state. This chapter describes the procedure for connection the MN101CF74G to the PX-FW2.

1.1.1 PX-FW2 Connecting Signals

To connect the MN101CF74G to the PX-FW2, a total of 6 lines must be connected: three communication signal lines, reset, VDD, and VSS. The MN101CF74G is connected to the PX-FW2 using a 10-conductor flat cable. The PX-FW2 can be connected to the target board easily if a connector for a 10-conductor flat cable is mounted on the target board. If a connector cannot be mounted on the target board, the 10-conductor flat cable can be directly soldered to the board.

Note: The length of the cable must not exceed 30 cm. The serial communication system may not work correctly with cables longer than 30 cm.

Table 1.1.1 MN101CF74G and PX-FW2 Pin Correspondence

MN101CF74G signals (pin number)	PX-FW2 connector signals (pin number)	I/O	Notes
NRST (23)	NRST (1)	MN101CF74G <-- PX-FW2	RESET
PC1 (10)	TDO (3)	MN101CF74G <--> PX-FW2	DATA
PC2 (11)	TCLK (9)	MN101CF74G <-- PX-FW2	CLOCK
DMOD (24)	DMOD (8)	MN101CF74G <-- PX-FW2	WRITER MODE
VDD	VDD (4)	MN101CF74G --> PX-FW2	POWER SUPPLY
VSS	GND (2,10)	-	GND

Note: If a flash programmer other than the Panasonic PX-FW2 is used, the connection pins may differ from those shown in tables 1.1.1.

1.1.2 The example of a connection circuit with PX-FW2

If at all possible, all other circuits should be disconnected when the MN101CF74G is connected to the PX-FW2 with this connection circuit. If disconnecting other circuits is difficult, design the connection circuit so that communication is performed reliably by selecting optimal component values based on the instructions in this manual.

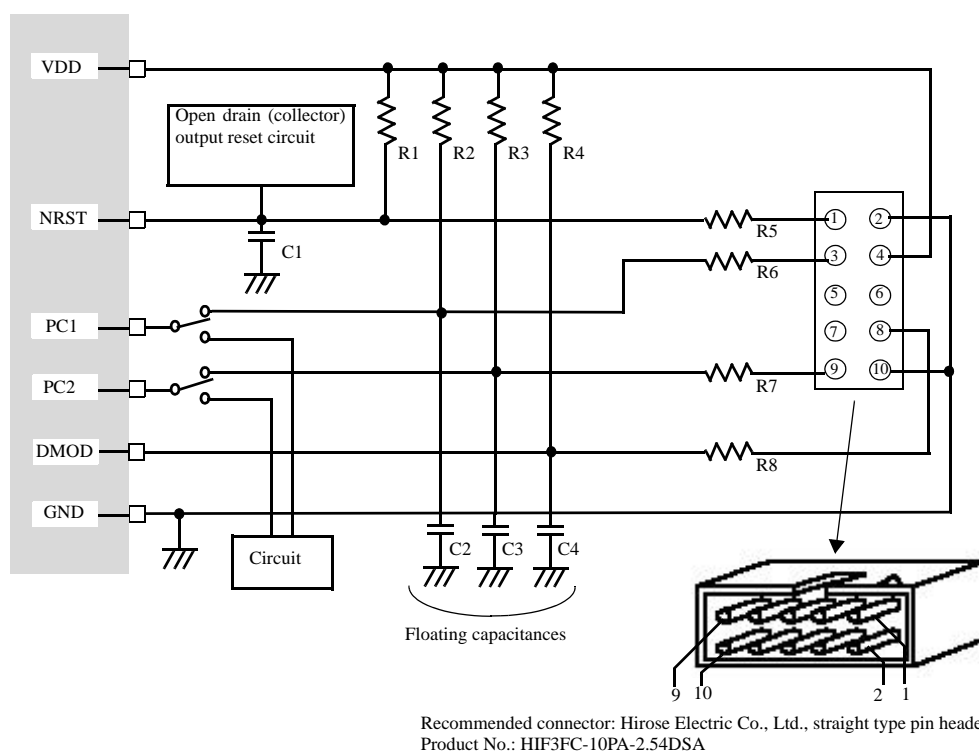


Fig 1.1.1 Sample Connection Circuit Using a 10-Conductor Flat Cable

Note: Be careful not to mistake the pin numbers on the 10-conductor flat cable connector. The pin numbers on this connector are set up so that, when the connector is viewed from the connection target side with the cutout pointing, the upper right pin is pin 1, and the lower right pin is pin 2. Similarly, the upper left pin is pin 9 and the lower left pin is pin 10.

The communication data pin and the communication clock pin must be pulled up. There are cases other than those where a PX-FW2 is used where these pins must be pulled up as well. See the IC documentation for details.

Use an open collector (or similar) reset circuit, and design the circuit so that signals do not collide.

1.2 Component Value Calculations

This section presents the calculations for each of the components used for connection with the PX-FW2.

1.2.1 Component Values

The table shows the values of the components used in figure 1.1.1.

- The value of the resistor R1 must be greater than R_{upRst} .
- The values of the resistors R2, R3, and R4 must be greater than R_{upMin} and greater than 1 k ohm.
- The value of the resistor R5 must be less than 1/10 that of R1 and less than R_{sMax} .
- The value of the resistor R6 must be less than 1/10 that of R2 and less than R_{sMax} .
- The value of the resistor R7 must be less than 1/10 that of R3 and less than R_{sMax} .
- The value of the resistor R8 must be less than 1/10 that of R4 and less than R_{sMax} .
- C1 must be less than C_{rst} and less than 100 uF.
- C2, and C3 must be under 50 pF.
- Except for the Vdd and VSS lines, the line length of the signal from the connector to the microcontroller must be less than 30 cm.

[Refer to 1.1.2 The example of a connection circuit with PX-FW2]

Note: This document is written assuming that the clock and data outputs used from the PX-FW2 are the push-pull outputs.
The required component values will differ from those shown here if the PX-FW2 open-drain outputs are used.

1.2.2 Reset Signal Capacitor (C1) Maximum Value Calculation

Writing to MN101CF74G, it uses the oscillator stabilization time after the microcontroller resets. The rise time of the reset must be less than 1/3 of the oscillator stabilization time (T_{wait}). The maximum value, C_{rst} , of the reset signal capacitor is determined from the equation (1).

$$C_{rst} = \frac{\text{The oscillator stabilization time}}{3 * \text{Pull-up resistor R1}} \quad \dots \text{Equation (1)}$$

1.2.3 Pull-up Resistor (R1) Minimum Value Calculation

The maximum output current from the PX-FW2 is 12 mA. Since this value is the maximum load current available for outputting a low level, R_{upRst} can be determined from equation (2).

$$R_{upRst} = \frac{\text{Operating supply voltage (VDD)}}{12\text{mA}} - R_{sRst} \quad \dots \text{Equation (2)}$$

1.2.4 Relationship Between R_{upRst} and R_{sRst}

If you want to insert a resistor with a large value in series with the reset pin, the pull-up resistor (R_{upRst}) and the series resistor (R_{sRst}) must meet the condition in equation (3) so that the signal level falls all the way to the low level.

$$R_{upRst} * \frac{1}{10} \geq R_{sRst} \quad \dots \text{Equation (3)}$$

1.2.5 Pull-up Resistor (R2, R3 and R4) Minimum Value Calculations

Find the maximum output current, I_{OL} , for the pins used for communication from the microcomputer IC documentation. Since that value is the maximum load current available for outputting a low level, R_{upMin} can be determined from equation (4).

$$R_{upMin} = \frac{\text{Operating supply voltage (VDD)}}{\text{Pin maximum output current (I}_{OL})} \quad \dots \text{Equation (4)}$$

1.2.6 Communication Pin Series Resistor (R6, R7 and R8) Maximum Value Calculations

If series resistors are inserted in the communication pin lines, the signal transmission speed will be slowed due to the influence of the load capacitors (C2, C3, and C4). To assure reliable communication, the time for the signal voltage to change by 63% of the supply voltage (i.e. the time constant) must be held to under 1/8 of the communication period.

If we assume that the load capacitance is 50 pF and the communication frequency is 1 MHz, then the maximum resistor value (RsMax) allowable for reliable communication will be, from equation (5), 2.5 k ohm.

$$R_{sMax} = \frac{1}{8 * \text{Communication frequency (f)} * \text{Load capacitance (C)}} \quad \dots \text{Equation (5)}$$

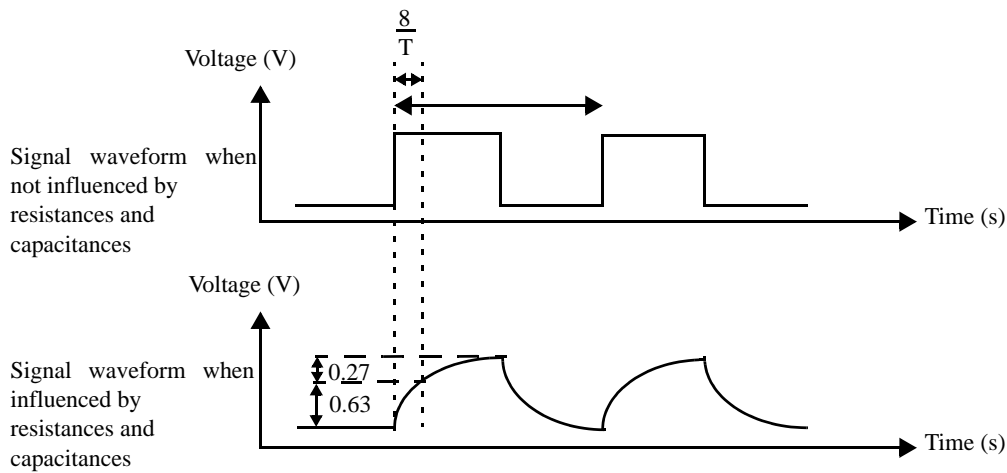


Fig 1.2.1 Relationship Between Communication Frequency, R, and C

1.2.7 Relationship Between R_{upMin} and R_{sMax}

It will be possible to insert resistors with large values in series in the communication lines if the communication speed is lowered. However, even in that case, the pull-up resistor (R_{upMin}) and the series resistor (R_{sMax}) must meet the condition in equation (6) so that the signals fall to the low level.

$$R_{upMin} * \frac{1}{10} \geq R_{sMax} \quad \dots \text{Equation (6)}$$

1.3 Flash Memory Programming Procedure

This section describes the procedures for onboard serial programming using the PX-FW2.

1.3.1 Overview of the Flash Memory Programming Procedure

This program runs on the microcontroller and is used to control programming the MN101CF74G on-chip flash EEPROM using the Panasonic PX-FW2.
Using PX-FW2 to operate on-board serial writing of MN101CF74G, micron program that is called loader program is not needed.

Note: Using on-board serial writer other than PX-FW2, loader program may be needed.

1.3.2 Key Code

The MN101CF74G area that allows key code settings is the area from 0x4001 to 0x4FFF.
An arbitrary area of 7 to 255 bytes within this area can be set as the key code area.
See the PX-FW2 manual for details on the security functions and key codes.

1.3.3 Flash Programming Control Program

The programming control program is loaded in RAM in the target microcontroller and implements the flash memory write control algorithm. This program includes functions for erasing flash memory, for writing the user program to flash memory, and for reading out data from flash memory. This program is loaded into RAM from the programmer using serial communication between the PX-FW2 and the loader program.

Note that the file MN101CF74G.exe for MN101CF74G is included in the additional product information pack. Executing this file registers both MN101CF74G product information and the programming control program with Flash Commander.

1.4 Loader Area Programming Procedure

This section describes the procedures of onboard serial programming for the loader area using the PX-FW2.

1.4.1 Overview of the Flash Memory Programming Procedure

This program runs on the microcontroller and is used to control programming the MN101CF74G on-chip flash EEPROM using the Panasonic PX-FW2.

Using PX-FW2 to operate on-board serial writing of MN101CF74G, micron program that is called loader program is not needed.

Note: Using on-board serial writer other than PX-FW2, loader program may be needed.

1.4.2 Key Code

The MN101CF74G area that allows key code settings is the area from 0x4001 to 0x47FF. And arbitrary area of 7 to 255 bytes within this area can be set as the key code area. See the PX-FW2 manual for details on the security functions and key codes.

1.4.3 Flash Programming Control Program

The programming control program is loaded in RAM in the target microcontroller and implements the flash memory write control algorithm. This program includes functions for erasing flash memory, for writing the user program to flash memory, and for reading out data from flash memory. This program is loaded into RAM from the programmer using serial communication between the PX-FW2 and the loader program.

Note that the file MN101CF74G.exe for MN101CF74G(Loader) is included in the additional product information pack. Executing this file registers both MN101CF74G(Loader) product information and the programming control program with Flash Commander.

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