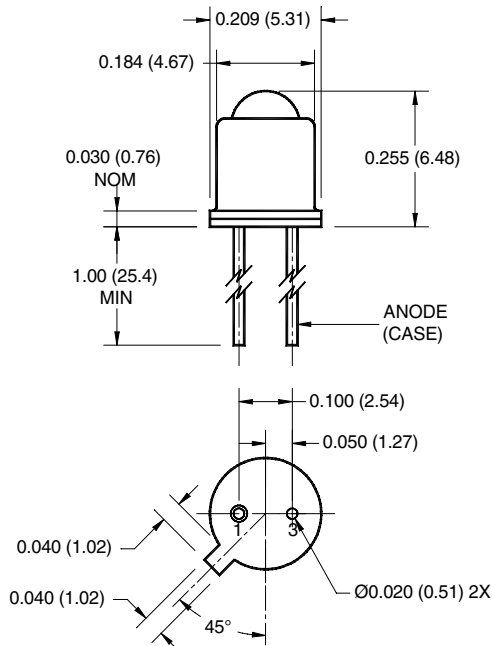


PACKAGE DIMENSIONS

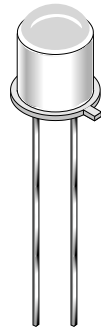


NOTES:

1. Dimensions for all drawings are in inches (mm).
2. Tolerance of $\pm .010$ (.25) on all non-nominal dimensions unless otherwise specified.

FEATURES

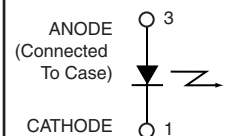
- Good optical to mechanical alignment
- Mechanically and wavelength matched to the TO-18 series phototransistor
- Hermetically sealed package
- High irradiance level
- (*) Indicates JEDEC registered values



DESCRIPTION

- The 1N6264 is a 940 nm LED in a narrow angle, TO-46 package.

SCHEMATIC



1. Derate power dissipation linearly 1.70 mW/°C above 25°C ambient.
2. Derate power dissipation linearly 13.0 mW/°C above 25°C case.
3. RMA flux is recommended.
4. Methanol or isopropyl alcohols are recommended as cleaning agents.
5. Soldering iron tip 1/16" (1.6mm) minimum from housing.
6. As long as leads are not under any stress or spring tension
7. Total power output, P_O , is the total power radiated by the device into a solid angle of 2π steradians.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
* Operating Temperature	T_{OPR}	-65 to +125	°C
* Storage Temperature	T_{STG}	-65 to +150	°C
* Soldering Temperature (Iron)(3,4,5 and 6)	T_{SOL-I}	240 for 5 sec	°C
* Soldering Temperature (Flow)(3,4 and 6)	T_{SOL-F}	260 for 10 sec	°C
* Continuous Forward Current	I_F	100	mA
* Forward Current (pw, 1µs; 200Hz)	I_F	10	A
* Reverse Voltage	V_R	3	V
* Power Dissipation ($T_A = 25^\circ\text{C}$)(1)	P_D	170	mW
Power Dissipation ($T_C = 25^\circ\text{C}$)(2)	P_D	1.3	W

ELECTRICAL / OPTICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$) (All measurements made under pulse conditions)

PARAMETER	TEST CONDITIONS	SYMBOL	MIN	TYP	MAX	UNITS
* Peak Emission Wavelength	$I_F = 100$ mA	λ_P	935	—	955	nm
Emission Angle at 1/2 Power	$I_F = 100$ mA	θ	—	± 8	—	Deg.
* Forward Voltage	$I_F = 100$ mA	V_{F1}	—	—	1.7	V
* Reverse Leakage Current	$V_R = 3$ V	I_R	—	—	10	µA
* Total Power	$I_F = 100$ mA	P_O	6	—	—	mW
Rise Time 0-90% of output		t_r	—	1.0	—	µs
Fall Time 100-10% of output		t_f	—	1.0	—	µs

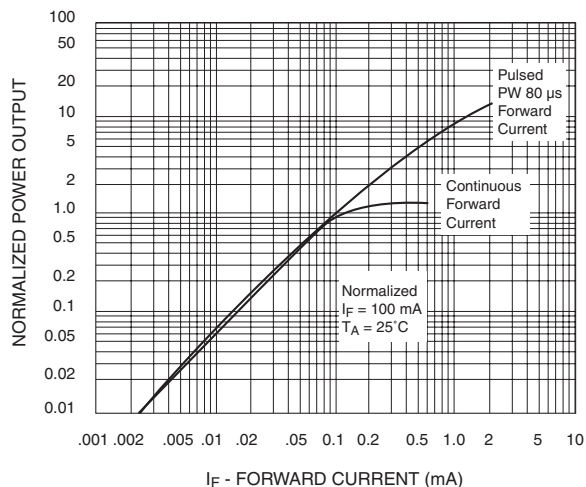


Fig. 1 Power Output vs. Input Current

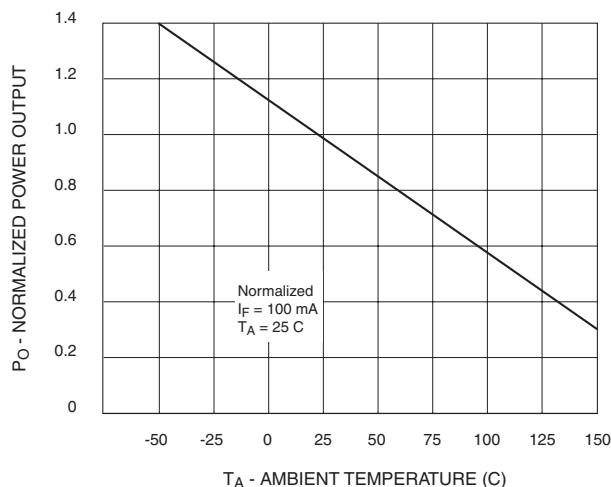


Fig. 2 Power Output vs. Temperature

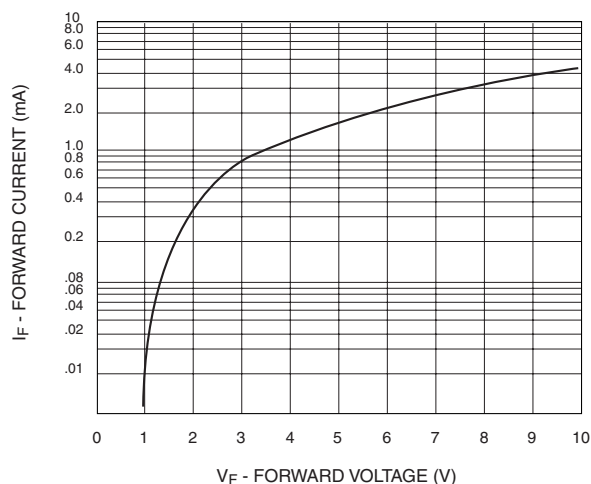


Fig. 3 Forward Voltage vs. Forward Current

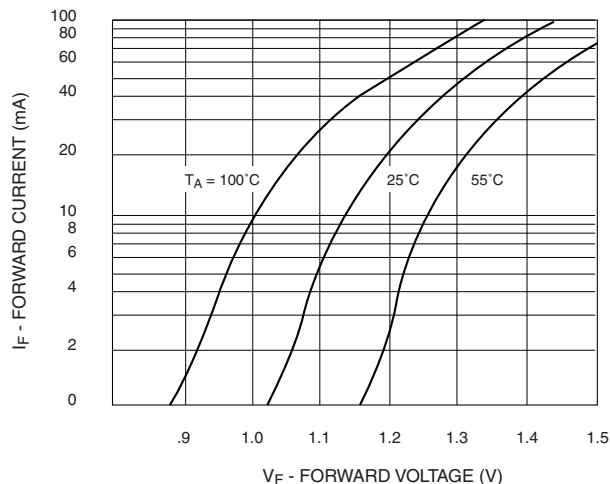


Fig. 4 Forward Voltage vs. Forward Current

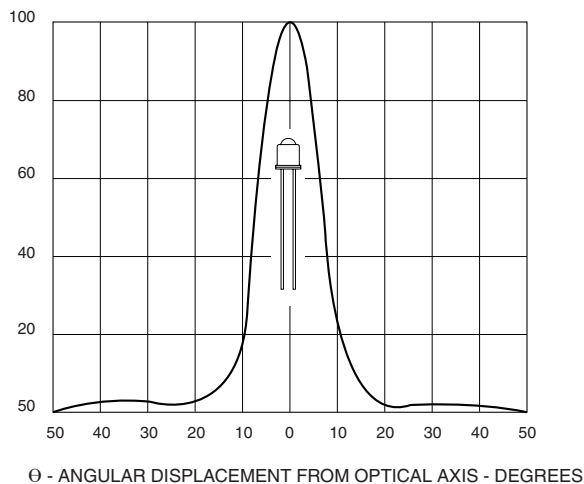


Fig. 5 Typical Radiation Pattern

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.