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Status: **NRND**

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ADC1213D125W1
ADC1213D125W1 demo board

IDT's ADC demo board is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204A feature set.

Features

- Single POWER supply (on board regulators)
- On Board Virtex5 FPGA for data acquisition
- SMA connector for clock and analog inputs signal
- On board Data clock generator for ADC and FPGA



Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Sample & Buy
ADC1213D125W1-DB	NRND	BD0	BOARD	0	I	No	Check Availability

Documents

There are no public documents available for this product. [Click here to request documentation.](#)

Related Products

Product ID	Title
ADC1213D125HN	Dual 12-bit ADC; 125 Mbps

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