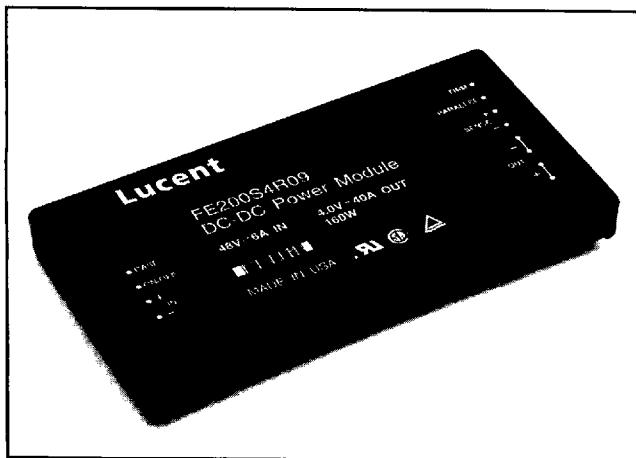




FE200S4R09 Power Module: dc-dc Converter; 38 Vdc to 60 Vdc Input, 4 Vdc Output; 160 W



The FE200S4R09 Power Module uses advanced, surface-mount technology and delivers high-quality, compact, dc-dc conversion at an economical price.

Options

- Power good signal

Features

- Size: 121.9 mm x 63.5 mm x 12.7 mm (4.80 in. x 2.50 in. x 0.50 in.)
- High efficiency: 79% typical
- Parallel operation with load sharing
- Complete input and output filtering
- Within FCC requirements for Telecom
- Constant frequency
- Case ground pin
- Input-to-output isolation
- Remote sense
- Remote on/off
- Output voltage set-point adjustment (trim)
- Short-circuit protection
- Output overvoltage clamp
- Thermal protection
- *UL** Recognized, *CSA*† Certified, *TÜV*‡ Licensed

* *UL* is a registered trademark of Underwriters Laboratories, Inc.
† *CSA* is a registered trademark of Canadian Standards Association.
‡ *TÜV* is a registered trademark of Technischer Überwachungs-Verein.

Applications

- Redundant and distributed power architectures
- Telecommunications

Description

The FE200S4R09 Power Module is a dc-dc converter that operates over an input voltage range of 38 Vdc to 60 Vdc and provides a precisely regulated dc output. The output is fully isolated from the input, allowing versatile polarity configurations and grounding connections. The module has a maximum power rating of 160 W at a typical full-load efficiency of 79%.

Built-in filtering, for both the input and output of each device, eliminates the need for external filters. Two or more modules may be paralleled with forced load sharing for redundant or enhanced power applications. The package, which mounts on a printed-circuit board, accommodates a heat sink for high-temperature applications.

■ 0050026 0033925 96T ■

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Input Voltage (continuous)	V_i	—	60	Vdc
I/O Isolation Voltage	—	—	500	V
Operating Case Temperature (See Thermal Considerations section and Figure 15.)	T_c	0	80	°C
Storage Temperature	T_{stg}	-55	125	°C

Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

Table 1. Input Specifications

Parameter	Symbol	Min	Typ	Max	Unit
Operating Input Voltage	V_i	38	48	60	Vdc
Maximum Input Current ($V_i = 0$ V to 60 V)	I_i, max	—	—	8	A
Inrush Transient	i^2t	—	—	1.0	A ² s
Input Reflected-ripple Current, Peak-to-peak (5 Hz to 20 MHz, 12 μ H source impedance) (See Figure 6.)	—	—	35	—	mAp-p
Input Ripple Rejection (120 Hz)	—	—	60	—	dB

Fusing Considerations

CAUTION: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of a sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a normal-blow, dc fuse with a maximum rating of 20 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data for further information.

Electrical Specifications (continued)

Table 2. Output Specifications

Parameter	Symbol	Min	Typ	Max	Unit
Output Voltage (Over all operating input voltage, resistive load, and temperature conditions until end of life; see Figure 7 and Feature Descriptions.)	Vo	3.80	—	4.20	Vdc
Output Voltage Set Point (Vi = 48 V; Io = Io, max; Tc = 25 °C): Unit Operating in Parallel or PARALLEL Pin Shorted to SENSE(–) (See Figure 7 and Feature Descriptions.) PARALLEL Pin Open	Vo, set	3.96	—	4.04	Vdc
Output Regulation: Line (Vi = 38 V to 60 V) Load (Io = Io, min to Io, max) Temperature (Tc = 0 °C to 80 °C)	Vo, set	3.96	—	4.08	Vdc
Output Ripple and Noise Voltage (See Figure 4 and Figure 8.): RMS Peak-to-peak (5 Hz to 20 MHz)	—	—	0.05 0.1 0.2	0.2 0.4 1.0	% % %
Output Current (At Io < Io, min, the modules may exceed output ripple specifications.)	Io	1	—	40	A
Output Current-limit Inception (Vo = 90% of Vo, set; see Figure 2 and Feature Descriptions.)	—	103	—	130	% Io, max
Output Short-circuit Current (Vo = 250 mV; see Figure 2.)	—	—	115	150	% Io, max
External Load Capacitance (electrolytic, total for one unit or multiple paralleled units)	—	0	—	5000	μF
Efficiency (Vi = 48 V; Io = Io, max; Tc = 25 °C; see Figure 3 and Figure 7.)	η	77	79	—	%
Dynamic Response (ΔIo/Δt = 1 A/10 μs, Vi = 48 V, Tc = 25 °C): Load Change from Io = 50% to 75% of Io, max: Peak Deviation Settling Time (Vo < 10% of peak deviation) Load Change from Io = 50% to 25% of Io, max: Peak Deviation Settling Time (Vo < 10% of peak deviation)	—	—	75 200	—	mV μs mV μs

Electrical Specifications (continued)

Table 3. Isolation Specifications

Parameter	Min	Typ	Max	Unit
Isolation Capacitance	—	1700	—	pF
Isolation Resistance	10	—	—	MΩ

General Specifications

Parameter	Min	Typ	Max	Unit
Calculated MTBF (Io = 80% of Io, max; Tc = 40 °C)	2,000,000	—	—	hours
Weight	—	—	200 (7)	g (oz.)

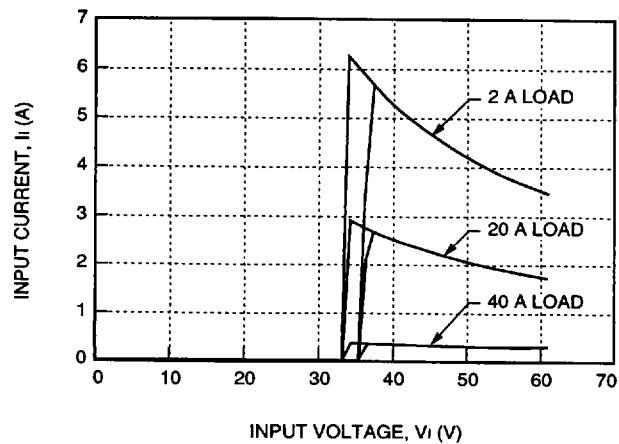
Feature Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for further information.

Parameter	Symbol	Min	Typ	Max	Unit
Remote On/Off Signal Interface (Vi = 0 V to 60 V; open collector or equivalent compatible; signal referenced to Vi(–) terminal; see Figure 9 and Feature Descriptions.):					
Logic Low—Module On					
Logic High—Module Off					
Logic Low:					
At Io/Off = 1.0 mA	Von/off	0	—	1.2	V
At Von/off = 0.0 V	Ion/off	—	—	1.0	mA
Logic High:					
At Io/Off = 0.0 μA	Von/off	—	—	18	V
Leakage Current	Ion/off	—	—	50	μA
Turn-on Time (Io = 80% of Io, max; Vo within ±1% of steady state)	—	—	—	—	ms
Output Voltage Adjustment (See Feature Descriptions.):					
Output Voltage Remote-sense Range	—	—	—	0.5	V
Output Voltage Set-point Adjustment Range (trim)	—	90	—	110	% Vo, nom
Parallel Operation Load Sharing (See Feature Descriptions.)	—	—	—	20	% Io, max
Output Overvoltage Clamp	Vo, clamp	—	125	—	% Vo, nom
Power Good Signal (optional) (See Feature Descriptions.):					
Low Impedance—Module Operating	Rpwrgood	—	—	100	Ω
High Impedance—Module Off	Ipwrgood	—	—	1	mA
	Rpwrgood	1	—	—	MΩ
	Ipwrgood	—	—	40	V
Overtemperature Shutdown	Tc	—	95	—	°C

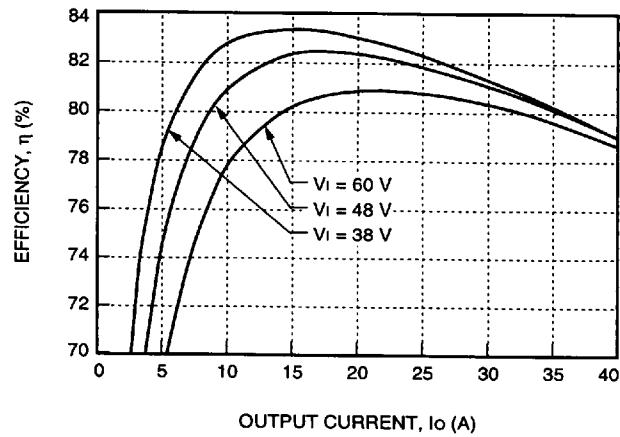
Characteristic Curves

The following figures provide typical characteristics for the FE200S4R09 Power Module.



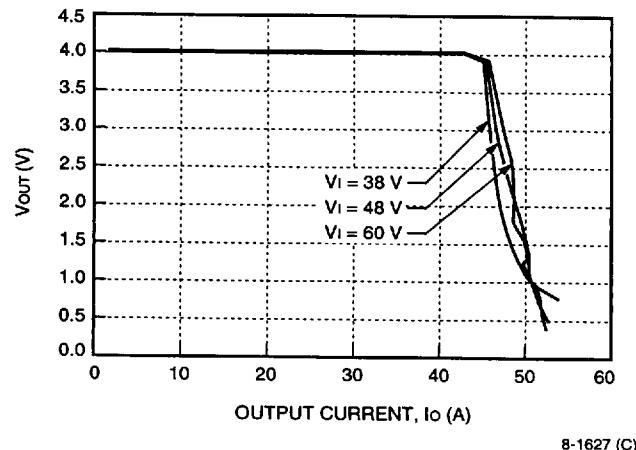
8-1626 (C)

Figure 1. Typical FE200S4R09 Input Characteristics at Room Temperature



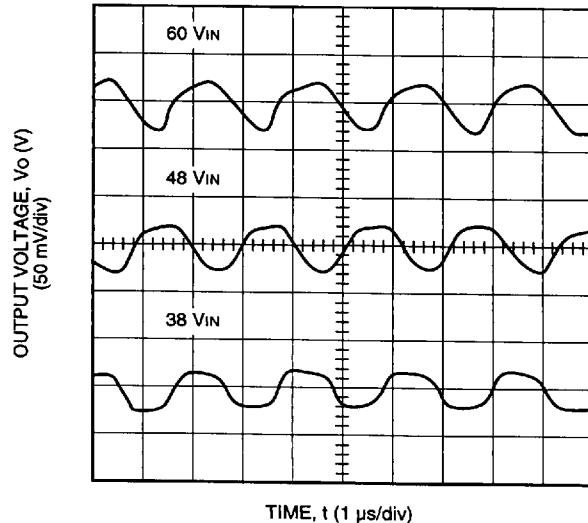
8-1628 (C)

Figure 3. Typical FE200S4R09 Efficiency vs. Output Current at Room Temperature



8-1627 (C)

Figure 2. Typical FE200S4R09 Output Characteristics at Room Temperature



8-1629 (C)

Figure 4. Typical FE200S4R09 Output Ripple Voltage at Room Temperature and 30 A Output

Characteristic Curves (continued)

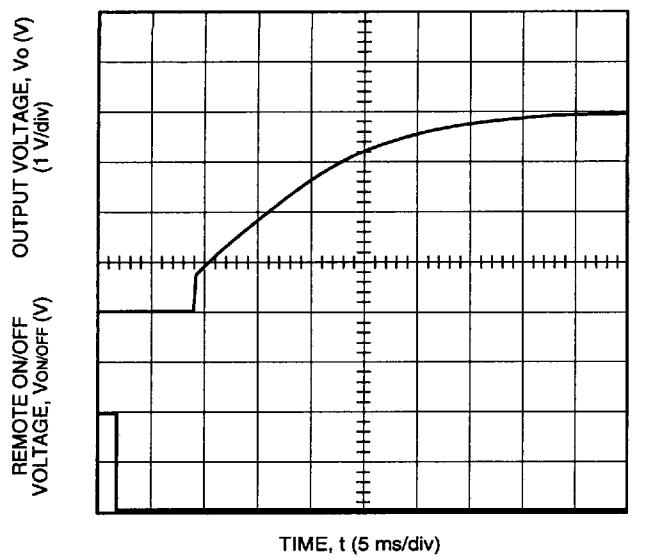
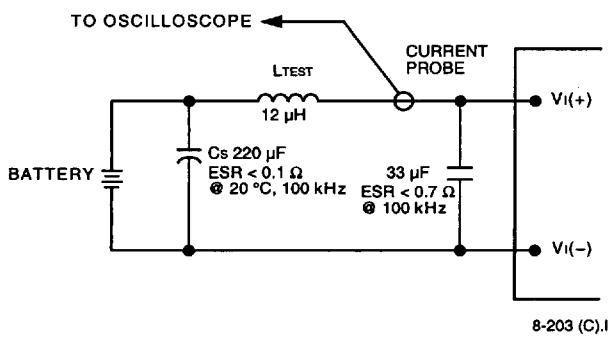


Figure 5. Typical FE200S4R09 Start-Up Transient at Room Temperature, 48 V Input, and 40 A Output

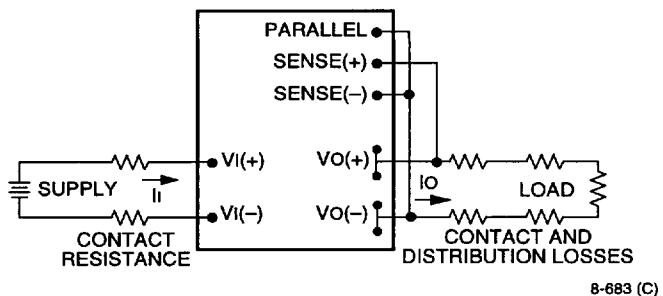
8-1630 (C)

Test Configurations



Note: Measure input reflected-ripple current with a simulated source inductance (LTEST) of 12 μ H. Capacitor Cs offsets possible battery impedance. Measure current as shown above.

Figure 6. Input Reflected-Ripple Test Setup

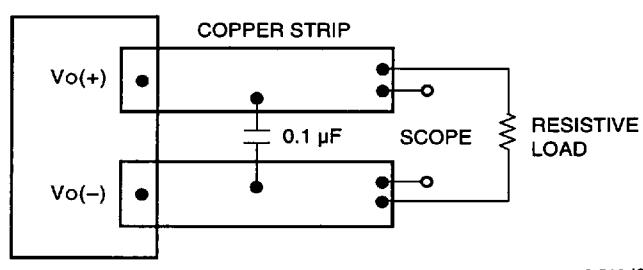


Note: All measurements are taken at the module terminals. When socketing, place Kelvin connections at module terminals to avoid measurement errors due to socket contact resistance.

$$\eta = \left(\frac{[V_o(+)] - [V_o(-)]}{[V_i(+)] - [V_i(-)]} \right) \times 100$$

Figure 7. Output Voltage and Efficiency Measurement Test Setup

8-683 (C)



8-513 (C)

Note: Use a 0.1 μ F ceramic capacitor. Scope measurement should be made using a BNC socket. Position the load between 50 mm (2 in.) and 80 mm (3 in.) from the module.

Figure 8. Peak-to-Peak Output Noise Measurement Test Setup

Design Considerations

Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. For the test configuration in Figure 6, a 33 μ F electrolytic capacitor (ESR < 0.7 Ω at 100 kHz) mounted close to the power module helps ensure stability of the unit. For other highly inductive source impedances, consult the factory for further application guidelines.

Design Considerations (continued)

Safety Considerations

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., UL-1950, CSA 22.2-950, and EN60950.

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements.

If the input meets extra-low voltage (ELV) requirements, then the converter's output is considered ELV.

The input to these units is to be provided with a maximum 20 A normal-blow fuse in the ungrounded lead.

Electrical Descriptions

Current Limit

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting for an unlimited duration. At the point of current-limit inception, the unit shifts from voltage control to current control. If the output voltage is pulled very low during a severe fault, the current-limit circuit can exhibit either foldback or tailout characteristics (output-current decrease or increase). The unit operates normally once the output current is brought back into its specified range.

Feature Descriptions

Remote On/Off

To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the $Vi(-)$ terminal ($V_{on/off}$). The switch can be an open collector or equivalent (see Figure 9). A logic low is $V_{on/off} = 0$ V to 1.2 V, during which the module is on. The maximum $I_{on/off}$ during a logic low is 1 mA. The switch should maintain a logic-low voltage while sinking 1 mA.

During a logic high, the maximum $V_{on/off}$ generated by the power module is 18 V. The maximum allowable leakage current of the switch at $V_{on/off} = 18$ V is 50 μ A.

If not using the remote on/off feature, short the ON/OFF pin to $Vi(-)$.

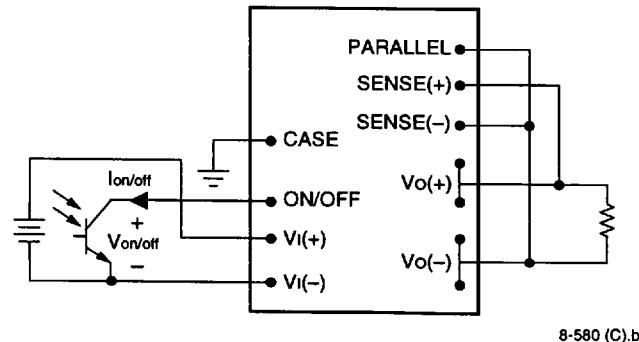


Figure 9. Remote On/Off Implementation

Remote Sense

Remote sense minimizes the effects of distribution losses by regulating the voltage at the remote-sense connections. For single-unit operation, the PARALLEL pin should be connected to SENSE(-). The voltage between the remote-sense pins and the output terminals must not exceed the output voltage sense range given in the Feature Specifications table, i.e.:

$$[Vo(+)-Vo(-)]-[SENSE(+)-SENSE(-)] \leq 0.5 \text{ V}$$

The voltage between the $Vo(+)$ and $Vo(-)$ terminals must not exceed the minimum output voltage clamp value as indicated in the Feature Specifications table. This limit includes any increase in voltage due to remote-sense compensation and output voltage set-point adjustment (trim), see Figure 10.

If not using the remote-sense feature to regulate the output at the point of load, connect SENSE(+) to $Vo(+)$ and SENSE(-) to $Vo(-)$ at the module.

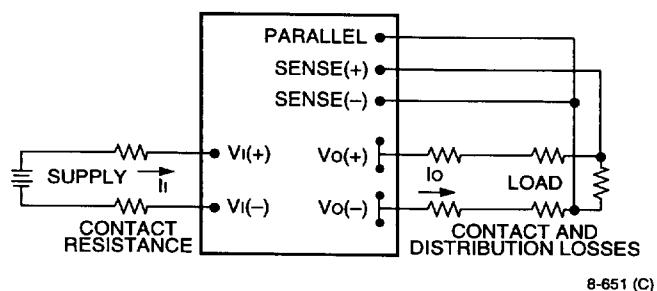


Figure 10. Effective Circuit Configuration for Single-Module Remote-Sense Operation

Feature Descriptions (continued)

Output Voltage Set-Point Adjustment (Trim)

When not using the trim feature, leave the TRIM pin open.

Adjustment with TRIM Pin

Output voltage adjustment allows the output voltage set point to be increased or decreased by adjusting an external resistor connected between the TRIM pin and either the SENSE(+) or SENSE(−) pins (see Figure 11 and Figure 12).

Connecting the external resistor ($R_{trim-up}$) between the TRIM and SENSE(−) pins ($V_{o, adj}$) increases the output voltage set point as defined in the following equation:

$$R_{trim-up} = \left(\frac{1.25 \times 4.22}{V_{o, adj} - 4} \right) \text{ k}\Omega$$

Connecting the external resistor ($R_{trim-down}$) between the TRIM and SENSE(+) pins ($V_{o, adj}$) decreases the output voltage set point as defined in the following equation:

$$R_{trim-down} = \left[\frac{(V_{o, adj} - 1.25) \times 4.22}{4 - V_{o, adj}} \right] \text{ k}\Omega$$

The voltage between the $V_o(+)$ and $V_o(-)$ terminals must not exceed the minimum output voltage clamp value as indicated in the Feature Specifications table. This limit includes any increase in voltage due to remote-sense compensation and output voltage set-point adjustment (trim), see Figure 10.

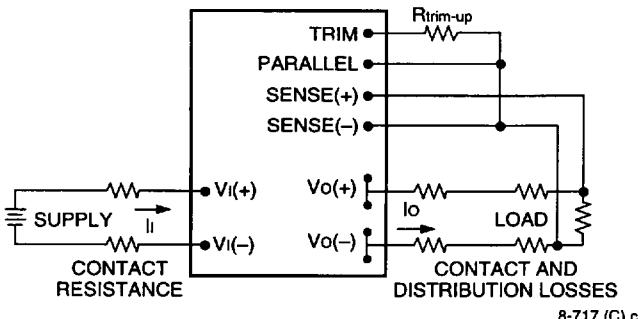


Figure 11. Circuit Configuration to Trim Up Output Voltage

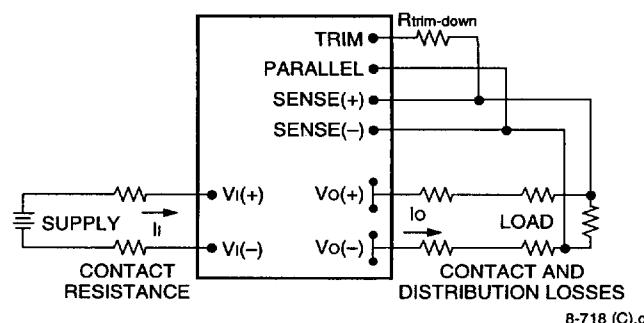


Figure 12. Circuit Configuration to Trim Down Output Voltage

Adjustment Without TRIM Pin

The output voltage can be adjusted by placing an external resistor (R_{adj}) between the SENSE(+) and $V_o(+)$ terminals (see Figure 13). By adjusting R_{adj} , the output voltage can be increased by 14% of the nominal output voltage. The equation below shows the resistance required to obtain the desired output voltage.

$$R_{adj} = (V_{o, adj} - V_{o, nom}) 807 \Omega$$

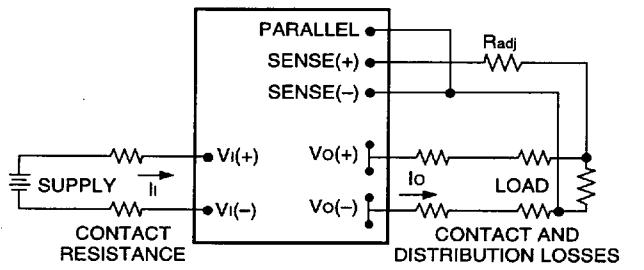


Figure 13. Circuit Configuration to Adjust Output Voltage

Forced Load Sharing (Parallel Operation)

For either redundant operation or additional power requirements, the power modules can be configured for parallel operation with forced load sharing (see Figure 14). For a typical redundant configuration, Schottky diodes or an equivalent should be used to protect against short-circuit conditions. Because of the remote sense, the forward-voltage drops across the Schottky diodes do not affect the set point of the voltage applied to the load. For additional power requirements, where multiple units are used to develop combined power in excess of the rated maximum, the Schottky diodes are not needed.

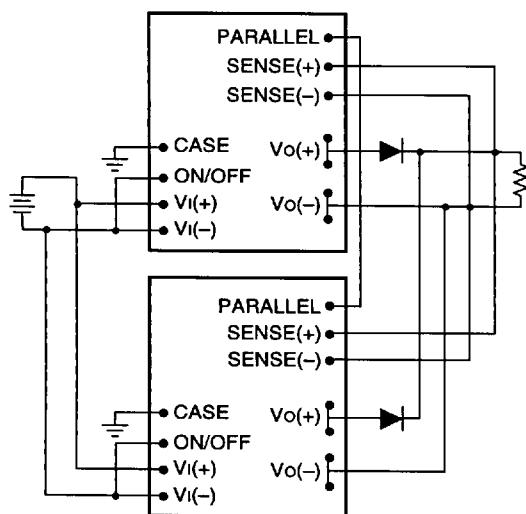
Feature Descriptions (continued)

Output Voltage Set-Point Adjustment (Trim) (continued)

Good layout techniques should be observed for noise immunity. To implement forced load sharing, the following connections must be made:

- The parallel pins of all units must be connected together. The paths of these connections should be as direct as possible.
- All remote-sense pins should be connected to the power bus at the same point, i.e., connect all SENSE(+) pins to the (+) side of the power bus at the same point and all SENSE(–) pins to the (–) side of the power bus at the same point. Close proximity and directness are necessary for good noise immunity.

When not using the parallel feature, short the PARALLEL pin to SENSE(–).



8-581 (C)

Figure 14. Wiring Configuration for Redundant Parallel Operation

Output Overvoltage Clamp

The output overvoltage clamp consists of control circuitry, independent of the primary regulation loop, that monitors the voltage on the output terminals. The control loop of the clamp has a higher voltage set point than the primary loop (see Feature Specifications table). This provides a redundant voltage-control that reduces the risk of output overvoltage.

Power Good Signal

The power good signal (MODPG pin) provides a pulsating voltage that must be appropriately rectified and filtered external to the power module. This filtered signal is used to determine if a module operating in parallel with other modules is delivering current or if it has failed. This signal is useful only when modules operate in parallel. Specific application questions can be directed to your Lucent Technologies Application Engineer.

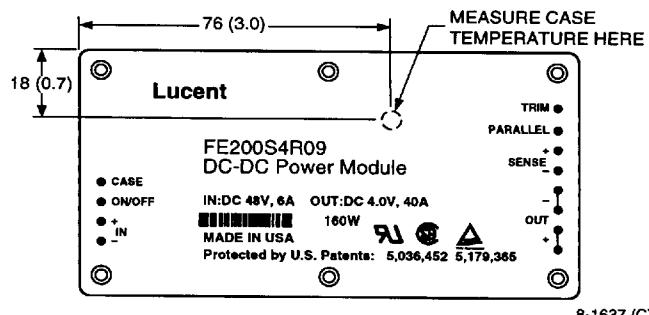
Overtemperature Shutdown

To provide protection in fault condition, the unit is equipped with an overtemperature shutdown circuit. The shutdown circuit will not engage unless the unit is operated above the maximum case temperature. If the overtemperature shutdown does engage, the unit will return to normal operation when the case has cooled sufficiently.

Thermal Considerations

Introduction

The power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat-dissipating components inside the unit are thermally coupled to the case. Heat is removed by conduction, convection, and radiation to the surrounding environment. Proper cooling can be verified by measuring the case temperature. Peak temperature occurs at the position indicated in Figure 15.



8-1637 (C)

Note: Top view, measurements shown in millimeters and (inches).

Figure 15. Case Temperature Measurement Location

Thermal Considerations (continued)

Introduction (continued)

The temperature at this location should not exceed 85 °C. The maximum case temperature can be limited to a lower value for extremely high reliability. The output power of the module should not exceed the rated power for the module as listed in the Ordering Information table.

For additional information about these modules, refer to the Lucent Technologies *Thermal Management for High-Power Board-Mounted Power Modules* Technical Note (TN97-009EPS).

Heat Transfer Without Heat Sinks

Derating curves for forced-air cooling without a heat sink are shown in Figure 16. These curves can be used to determine the appropriate airflow for a given set of operating conditions. For example, if the unit dissipates 20 W of heat, the correct airflow in a 30 °C environment is 1.0 m/s (200 ft./min.).

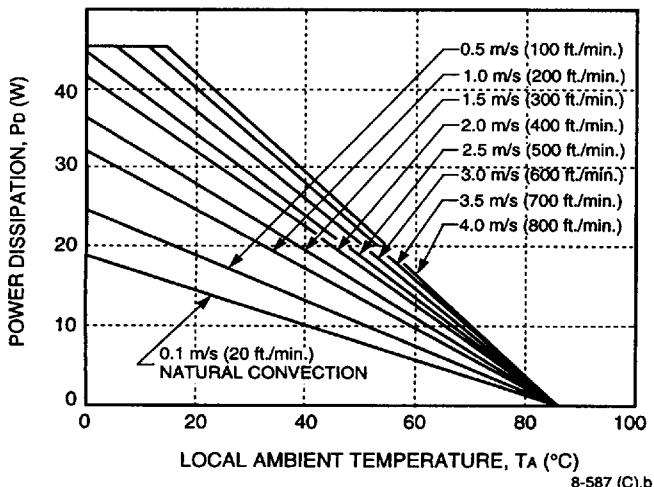


Figure 16. Power Derating vs. Local Ambient Temperature and Air Velocity

Heat Transfer with Heat Sinks

The power modules have threaded #4-40 fasteners, which enable heat sinks or cold plates to be attached to the module. The mounting torque must not exceed 0.56 N-m (5 in.-lb.).

Thermal derating with heat sinks is expressed by using the overall thermal resistance of the module. Total module thermal resistance (θ_{ca}) is defined as the maximum case temperature rise ($\Delta T_c, \text{max}$) divided by the module power dissipation (P_d).

$$\theta_{ca} = \left[\frac{\Delta T_c, \text{max}}{P_d} \right] = \left[\frac{(T_c - T_a)}{P_d} \right]$$

The location to measure case temperature (T_c) is shown in Figure 15. Case-to-ambient thermal resistance vs. airflow for various heat sink configurations is shown in Figure 17 and Figure 18. These curves were obtained by experimental testing of heat sinks, which are offered in the product catalog.

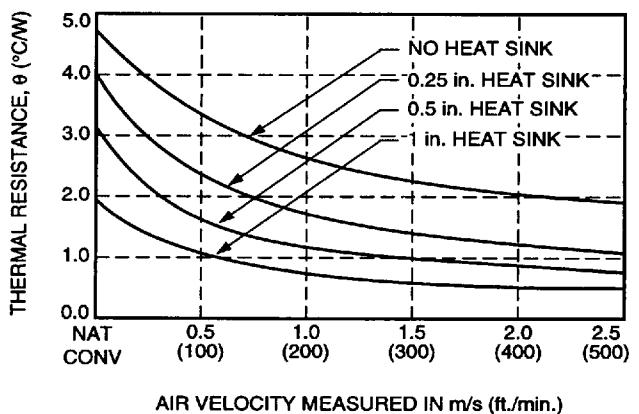


Figure 17. Heat Sink Resistance Curves; Fins Oriented Along Width

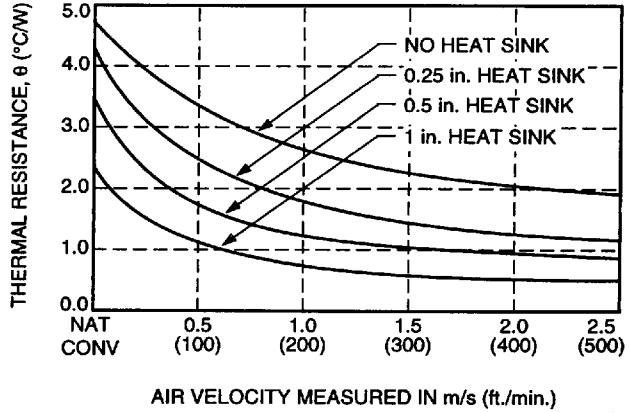


Figure 18. Heat Sink Resistance Curves; Fins Oriented Along Length

These measured resistances are from heat transfer from the sides and bottom of the module as well as the top side with the attached heat sink; therefore, the case-to-ambient thermal resistances shown are generally lower than the resistance of the heat sink by itself. The module used to collect the data in Figure 17 and Figure 18 had a thermal-conductive dry pad between the case and the heat sink to minimize contact resistance.

Thermal Considerations (continued)

Heat Transfer with Heat Sinks (continued)

To choose a heat sink, determine the power dissipated as heat by the unit for the particular application.

Figure 19 shows typical heat dissipation for a range of output currents and three voltages for the FE200S4R09.

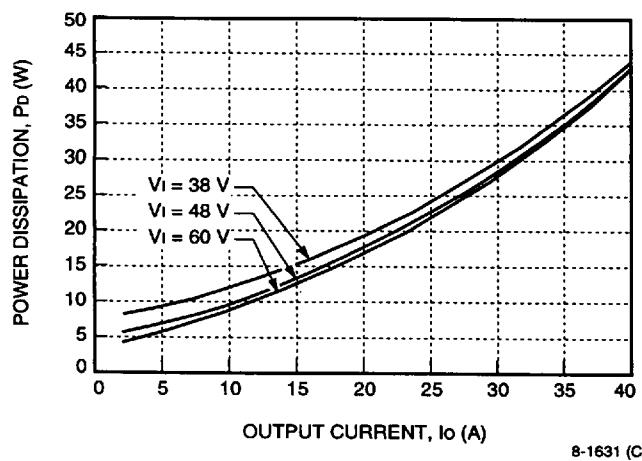


Figure 19. Power Dissipation as Heat vs. Output Current

Example

If an 85 °C case temperature is desired, what is the minimum airflow necessary? Assume the FE200S4R09 module is operating at nominal line and an output current of 26 A, maximum ambient air temperature of 40 °C, and the heat sink is 0.5 inch.

Solution

Given: $V_i = 48 V$
 $I_o = 26 A$
 $T_A = 40 ^\circ C$
 $T_c = 85 ^\circ C$
Heat sink = 0.5 inch.

Determine P_D by using Figure 19:

$$P_D = 24 W$$

Then solve the following equation:

$$\theta_{ca} = \left[\frac{(T_c - T_A)}{P_D} \right]$$

$$\theta_{ca} = \left[\frac{(85 - 40)}{24} \right]$$

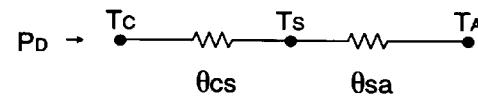
$$\theta_{ca} = 1.88 ^\circ C/W$$

Use Figure 17 and Figure 18 to determine air velocity for the 0.5 inch heat sink. The minimum airflow necessary for the FE200S4R09 module depends on heat sink fin orientation and is shown below:

- 0.4 m/s (80 ft./min.) (oriented along width)
- 0.45 m/s (90 ft./min.) (oriented along length)

Custom Heat Sinks

A more detailed model can be used to determine the required thermal resistance of a heat sink to provide necessary cooling. The total module resistance can be separated into a resistance from case-to-sink (θ_{cs}) and sink-to-ambient (θ_{sa}) shown below (Figure 20).



8-1304 (C)

Figure 20. Resistance from Case-to-Sink and Sink-to-Ambient

For a managed interface using thermal grease or foils, a value of $\theta_{cs} = 0.1 ^\circ C/W$ to $0.3 ^\circ C/W$ is typical. The solution for heat sink resistance is:

$$\theta_{sa} = \left[\frac{(T_c - T_A)}{P_D} \right] - \theta_{cs}$$

This equation assumes that all dissipated power must be shed by the heat sink. Depending on the user-defined application environment, a more accurate model, including heat transfer from the sides and bottom of the module, can be used. This equation provides a conservative estimate for such instances.

Layout Considerations

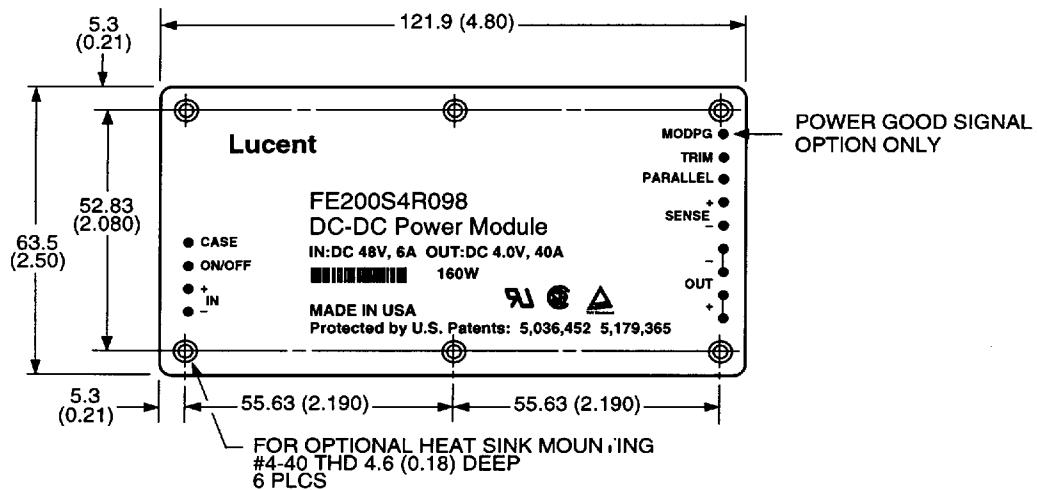
Copper paths must not be routed beneath the power module standoffs.

Outline Diagram

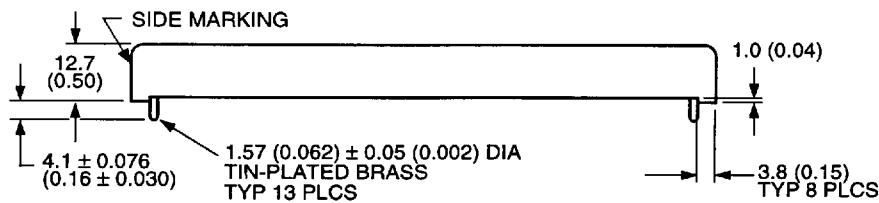
Dimensions are in millimeters and (inches).

Tolerances: $x.x$ mm ± 0.5 mm ($x.xx$ in. ± 0.02 in.),
 $x.xx$ mm ± 0.25 mm ($x.xxx$ in. ± 0.010 in.)

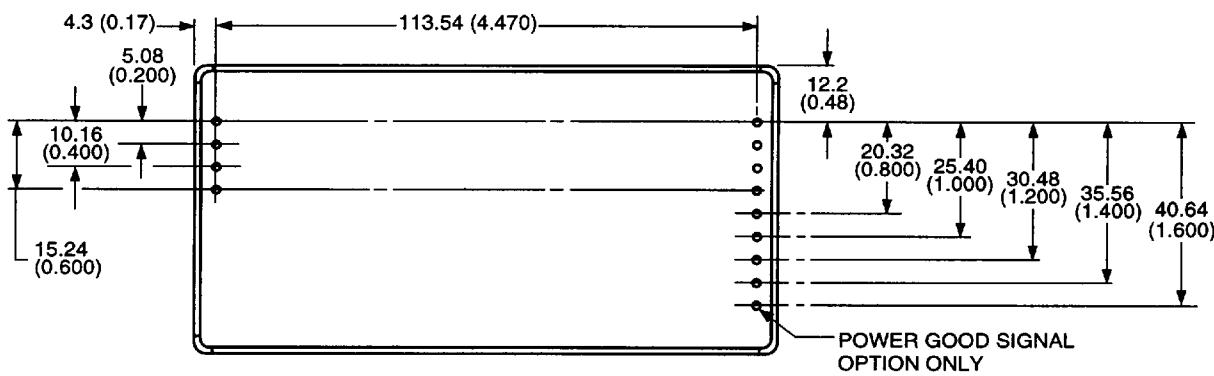
Top View



Side View



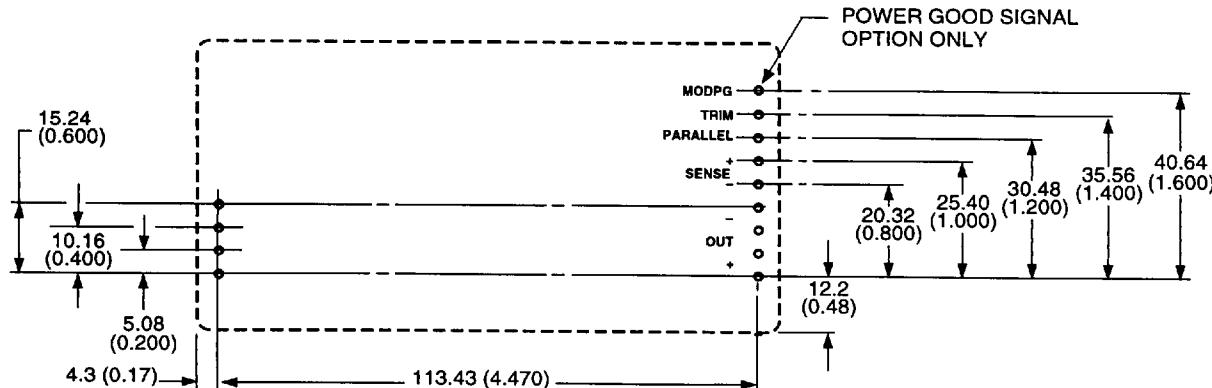
Bottom View



Recommended Hole Pattern

Component-side footprint.

Dimensions are in millimeters and (inches).



8-1635 (C)

Ordering Information

Optional Power Good signal is designated by the ending 8 in device code name. For assistance in ordering you may call the Lucent Technologies Power Systems Technical Hotline (1-800-526-7819 or 972-284-2626).

Input Voltage	Output Voltage	Output Power	Power Good Signal	Trim	Device Code	Comcode
48 V	4 V	160 W	No	Yes	FE200S4R09	107824534
48 V	4 V	160 W	Yes	Yes	FE200S4R098	TBD