



## Low-Jitter Precision LVPECL Oscillator

### General Description

The DSC1102 & DSC1122 series of high performance oscillators utilizes a proven silicon MEMS technology to provide excellent jitter and stability over a wide range of supply voltages and temperatures. By eliminating the need for quartz or SAW technology, MEMS oscillators significantly enhance reliability and accelerate product development, while meeting stringent clock performance criteria for a variety of communications, storage, and networking applications.

DSC1102 has a standby feature allowing it to completely power-down when EN pin is pulled low; whereas for DSC1122, only the outputs are disabled when EN is low. Both oscillators are available in industry standard packages, including the small 3.2x2.5 mm<sup>2</sup>, and are "drop-in" replacements for standard 6-pin LVPECL quartz crystal oscillators.

### Block Diagram



### Output Enable Modes

| EN Pin | DSC1102        | DSC1122          |
|--------|----------------|------------------|
| High   | Outputs Active | Outputs Active   |
| NC     | Outputs Active | Outputs Active   |
| Low    | Standby        | Outputs Disabled |

### Features

- **Low RMS Phase Jitter: <1 ps (typ)**
- **High Stability:  $\pm 10$ ,  $\pm 25$ ,  $\pm 50$  ppm**
- **Wide Temperature Range**
  - Industrial: -40° to 85° C
  - Ext. commercial: -20° to 70° C
- **High Supply Noise Rejection: -50 dBc**
- **Short Lead Time: 2 Weeks**
- **Wide Freq. Range: 2.3 to 460 MHz**
- **Small Industry Standard Footprints**
  - 2.5x2.0, 3.2x2.5, 5.0x3.2, & 7.0x5.0 mm
- **Excellent Shock & Vibration Immunity**
  - Qualified to MIL-STD-883
- **High Reliability**
  - 20x better MTF than quartz oscillators
- **Low Current Consumption**
- **Supply Range of 2.25 to 3.6 V**
- **Standby & Output Enable Function**
- **Lead Free & RoHS Compliant**
- **LVDS & HCSL Versions Available**

### Applications

- **Storage Area Networks**
  - SATA, SAS, Fibre Channel
- **Passive Optical Networks**
  - EPON, 10G-EPON, GPON, 10G-PON
- **Ethernet**
  - 1G, 10GBASE-T/KR/LR/SR, and FCoE
- **HD/SD/SDI Video & Surveillance**
- **PCI Express: Gen 1 & Gen 2**
- **DisplayPort**

## Absolute Maximum Ratings

| Item           | Min  | Max          | Unit | Condition  |
|----------------|------|--------------|------|------------|
| Supply Voltage | -0.3 | +4.0         | V    |            |
| Input Voltage  | -0.3 | $V_{DD}+0.3$ | V    |            |
| Junction Temp  | -    | +150         | °C   |            |
| Storage Temp   | -55  | +150         | °C   |            |
| Soldering Temp | -    | +260         | °C   | 40sec max. |
| ESD            | -    |              | V    |            |
| HBM            |      | 4000         |      |            |
| MM             |      | 400          |      |            |
| CDM            |      | 1500         |      |            |

## Ordering Code



Note: 1000+ years of data retention on internal memory

## Specifications

| Parameter                                                     |                      | Condition                                                                              | Min.                | Typ.                | Max.                | Unit              |
|---------------------------------------------------------------|----------------------|----------------------------------------------------------------------------------------|---------------------|---------------------|---------------------|-------------------|
| Supply Voltage <sup>1</sup>                                   | $V_{DD}$             |                                                                                        | 2.25                |                     | 3.6                 | V                 |
| Supply Current                                                | $I_{DD}$             | EN pin low – outputs are disabled<br>DSC1102<br>DSC1122                                |                     | 20                  | 0.095<br>22         | mA                |
| Frequency Stability                                           | $\Delta f$           | Includes frequency variations due to initial tolerance, temp. and power supply voltage |                     |                     | ±10<br>±25<br>±50   | ppm               |
| Aging                                                         | $\Delta f$           | 1 year @25°C                                                                           |                     |                     | ±5                  | ppm               |
| Startup Time <sup>2</sup>                                     | $t_{SU}$             | T=25°C                                                                                 |                     |                     | 5                   | ms                |
| Input Logic Levels<br>Input logic high<br>Input logic low     | $V_{IH}$<br>$V_{IL}$ |                                                                                        | 0.75x $V_{DD}$<br>- |                     | -<br>0.25x $V_{DD}$ | V                 |
| Output Disable Time <sup>3</sup>                              | $t_{DA}$             |                                                                                        |                     |                     | 5                   | ns                |
| Output Enable Time                                            | $t_{EN}$             | DSC1102<br>DSC1122                                                                     |                     |                     | 5<br>20             | ms<br>ns          |
| Enable Pull-Up Resistor <sup>4</sup>                          |                      | Pull-up resistor exist                                                                 |                     | 40                  |                     | kΩ                |
| <b>LVPECL Outputs</b>                                         |                      |                                                                                        |                     |                     |                     |                   |
| Supply Current                                                | $I_{DD}$             | Output Enabled, $R_L=50\Omega$                                                         |                     | 56.5                | 58                  | mA                |
| Output Logic Levels<br>Output logic high<br>Output logic low  | $V_{OH}$<br>$V_{OL}$ | $R_L=50\Omega$                                                                         | $V_{DD}-1.08$<br>-  |                     | -<br>$V_{DD}-1.55$  | V                 |
| Pk to Pk Output Swing                                         |                      | Single-Ended                                                                           |                     | 800                 |                     | mV                |
| Output Transition time <sup>3</sup><br>Rise Time<br>Fall Time | $t_R$<br>$t_F$       | 20% to 80%<br>$R_L=50\Omega$ , $C_L=0pF$                                               |                     | 250                 |                     | ps                |
| Frequency                                                     | $f_0$                | Single Frequency                                                                       | 2.3                 |                     | 460                 | MHz               |
| Output Duty Cycle                                             | SYM                  | Differential                                                                           | 48                  |                     | 52                  | %                 |
| Period Jitter                                                 | $J_{PER}$            |                                                                                        |                     | 2.5                 |                     | ps <sub>RMS</sub> |
| Integrated Phase Noise                                        | $J_{PH}$             | 200kHz to 20MHz @156.25MHz<br>100kHz to 20MHz @156.25MHz<br>12kHz to 20MHz @156.25MHz  |                     | 0.25<br>0.38<br>1.7 | 2                   | ps <sub>RMS</sub> |

### Notes:

1. Pin 6  $V_{DD}$  should be filtered with 0.1uF capacitor.
2.  $t_{SU}$  is time to 100ppm of output frequency after  $V_{DD}$  is applied and outputs are enabled.
3. Output Waveform and Test Circuit figures below define the parameters.
4. Output is enabled if pad is floated or not connected.

## Nominal Performance Parameters (Unless specified otherwise: $T=25^{\circ}\text{C}$ , $V_{DD}=3.3\text{V}$ )



## Output Waveform



## Typical Termination Scheme



## Test Circuit



## Solder Reflow Profile



| MSL 1 @ 260°C refer to JSTD-020C  |              |
|-----------------------------------|--------------|
| Ramp-Up Rate (200°C to Peak Temp) | 3°C/Sec Max. |
| Preheat Time 150°C to 200°C       | 60-180 Sec   |
| Time maintained above 217°C       | 60-150 Sec   |
| Peak Temperature                  | 255-260°C    |
| Time within 5°C of actual Peak    | 20-40 Sec    |
| Ramp-Down Rate                    | 6°C/Sec Max. |
| Time 25°C to Peak Temperature     | 8 min Max.   |

## Package Dimensions

### 7.0 x 5.0 mm Plastic Package

#### EXTERNAL DIMENSIONS

Units: mm [ inches]



| No. | Pin Terminal |
|-----|--------------|
| 1   | Enable       |
| 2   | N/C          |
| 3   | Ground       |
| 4   | OUT          |
| 5   | OUT-         |
| 6   | VDD          |
| PAD | TIE TO GND   |

#### RECOMMENDED SOLDER PAD LAYOUT

Units: mm [ inches]



### 5.0 x 3.2 mm Plastic Package

#### EXTERNAL DIMENSIONS

Units: mm [ inches]



| No. | Pin Terminal |
|-----|--------------|
| 1   | Enable       |
| 2   | N/C          |
| 3   | Ground       |
| 4   | OUT          |
| 5   | OUT-         |
| 6   | VDD          |

#### RECOMMENDED SOLDER PAD LAYOUT

Units: mm [ inches]



### 3.2 x 2.5 mm Plastic Package

#### EXTERNAL DIMENSIONS

Units: mm [ inches]



| No. | Pin Terminal |
|-----|--------------|
| 1   | Enable       |
| 2   | N/C          |
| 3   | Ground       |
| 4   | OUT          |
| 5   | OUT-         |
| 6   | VDD          |

#### RECOMMENDED SOLDER PAD LAYOUT

Units: mm [ inches]



### 2.5 x 2.0 mm Plastic Package

#### EXTERNAL DIMENSIONS

Units: mm [ inches]



| No. | Pin Terminal |
|-----|--------------|
| 1   | Enable       |
| 2   | N/C          |
| 3   | Ground       |
| 4   | OUT          |
| 5   | OUT-         |
| 6   | VDD          |

#### RECOMMENDED SOLDER PAD LAYOUT

Units: mm [ inches]



## 7.0 x 5.0 mm Plastic Package

## EXTERNAL DIMENSIONS

Units: mm [ inches]



### RECOMMENDED SOLDER PAD LAYOUT

Units: mm [ inches]



| No. | Pin Terminal |
|-----|--------------|
| 1   | Enable       |
| 2   | N/C          |
| 3   | Ground       |
| 4   | OUT          |
| 5   | OUT-         |
| 6   | VDD          |

**Disclaimer:**

Micrel makes no representations or warranties with respect to the accuracy or completeness of the information furnished in this data sheet. This information is not intended as a warranty and Micrel does not assume responsibility for its use. Micrel reserves the right to change circuitry, specifications and descriptions at any time without notice. No license, whether express, implied, arising by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Micrel's terms and conditions of sale for such products, Micrel assumes no liability whatsoever, and Micrel disclaims any express or implied warranty relating to the sale and/or use of Micrel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right.

Micro Products are not designed or authorized for use as components in life support appliances, devices or systems where malfunction of a product can reasonably be expected to result in personal injury. Life support devices or systems are devices or systems that (a) are intended for surgical implant into the body or (b) support or sustain life, and whose failure to perform can be reasonably expected to result in a significant injury to the user. A Purchaser's use or sale of Micro Products for use in life support appliances, devices or systems is a Purchaser's own risk and Purchaser agrees to fully indemnify Micro for any damages resulting from such use or sale.