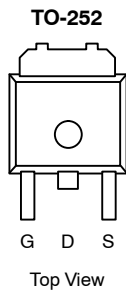




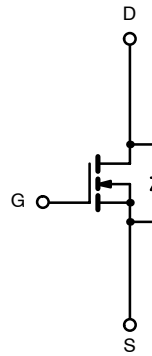
N-Channel 20-V (D-S) 175°C MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A) ^a
20	0.0033 @ $V_{GS} = 10$ V	39
	0.0053 @ $V_{GS} = 4.5$ V	31



Drain Connected to Tab



N-Channel MOSFET

Ordering Information: SUD70N02-03P

FEATURES

- TrenchFET® Power MOSFET
- 175°C Junction Temperature
- PWM Optimized for High-Efficiency
- 100% R_g Tested

APPLICATIONS

- Synchronous Buck Converter
 - Low-Side
 - Secondary Synchronous Rectifier

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^a	$T_A = 25^\circ\text{C}$	I_D	39 ^a	A
	$T_C = 25^\circ\text{C}$		70 ^b	
Pulsed Drain Current		I_{DM}	100	
Continuous Source Current (Diode Conduction) ^a		I_S	37	
Maximum Power Dissipation	$T_A = 25^\circ\text{C}$	P_D	8.3 ^a	W
	$T_C = 25^\circ\text{C}$		100	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	15	18	$^\circ\text{C/W}$
	Steady State		40	50	
Maximum Junction-to-Case		R_{thJC}	1.2	1.5	

Notes

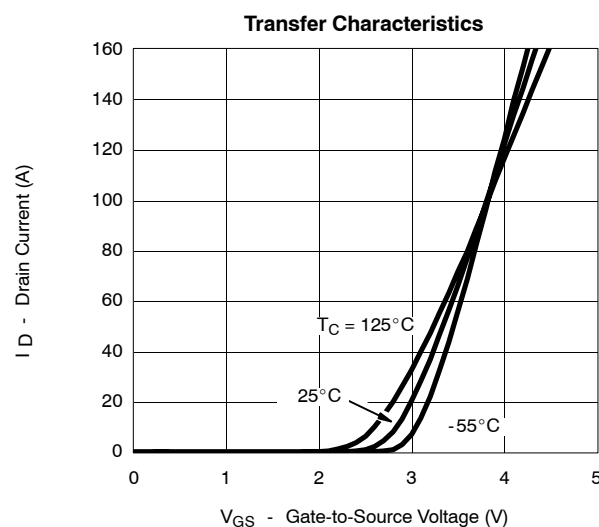
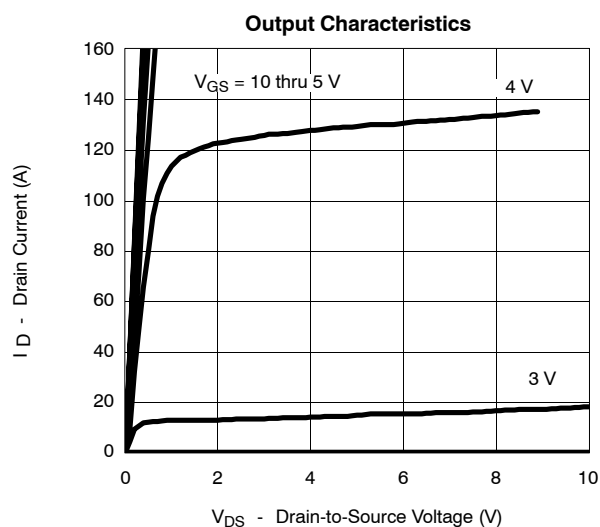
- a. Surface Mounted on FR4 Board, $t \leq 10$ sec.
b. Limited by package

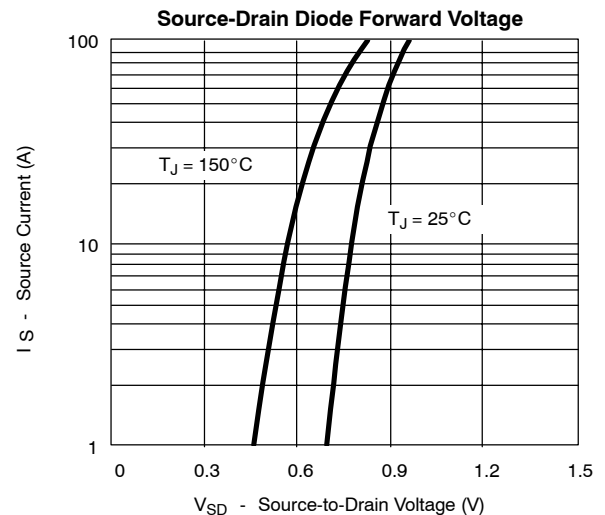
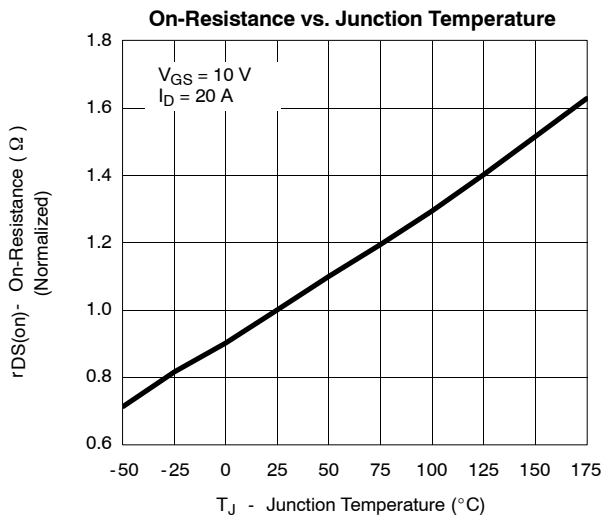
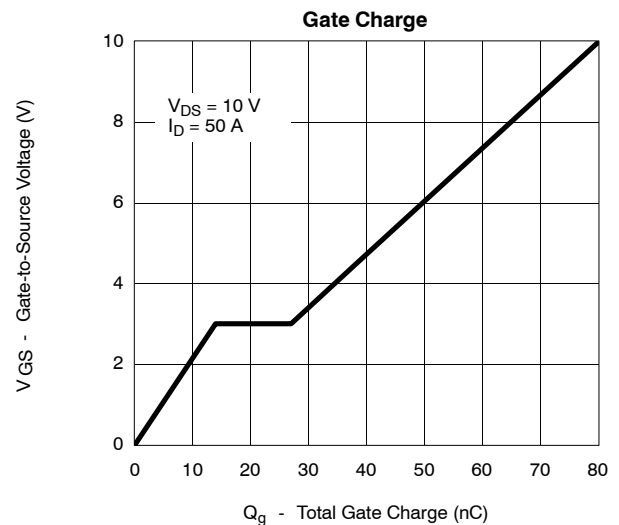
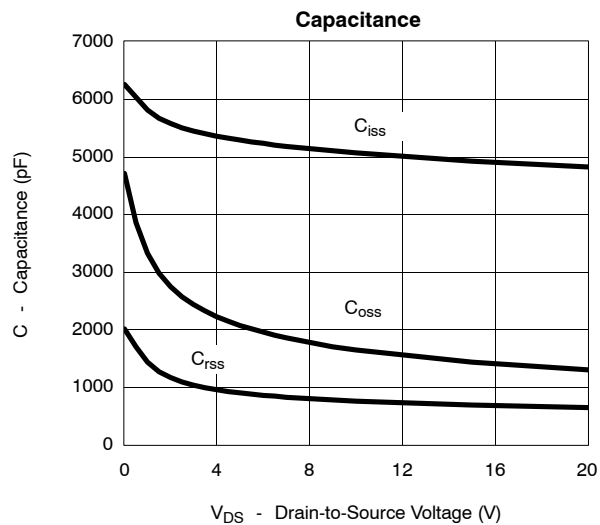
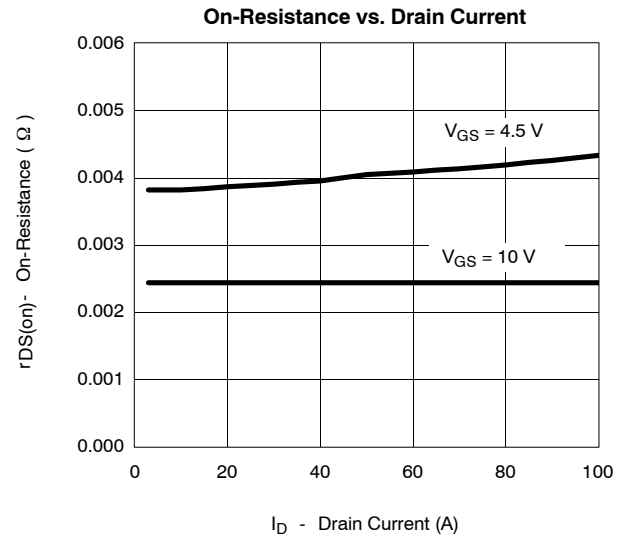
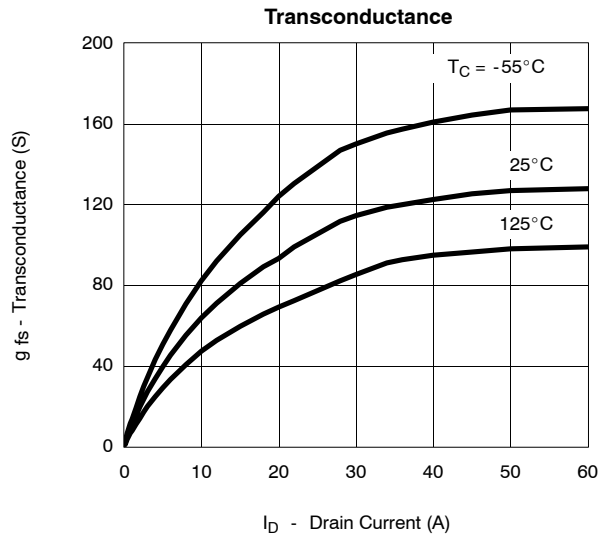
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA	20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.8		3.0	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 16 V, V _{GS} = 0 V			1	μA
		V _{DS} = 16 V, V _{GS} = 0 V, T _J = 125°C			50	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	50			A
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.0026	0.0033	Ω
		V _{GS} = 10 V, I _D = 20 A, T _J = 125°C			0.0047	
		V _{GS} = 4.5 V, I _D = 20 A		0.0042	0.0053	
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 20 A	15			S
Dynamic ^a						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 10 V, f = 1 MHz		5100		pF
Output Capacitance	C _{oss}			1650		
Reverse Transfer Capacitance	C _{rss}			800		
Gate Resistance	R _g	f = 1.0 MHz	0.5	1.1	1.8	Ω
Total Gate Charge ^c	Q _g	V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 50 A		40	60	nC
Gate-Source Charge ^c	Q _{gs}			14		
Gate-Drain Charge ^c	Q _{gd}			13		
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 10 V, R _L = 0.2 Ω I _D ≅ 50 A, V _{GEN} = 10 V, R _G = 2.5 Ω		15	25	ns
Rise Time ^c	t _r			11	20	
Turn-Off Delay Time ^c	t _{d(off)}			45	70	
Fall Time ^c	t _f			15	25	
Source-Drain Diode Ratings and Characteristic (T _C = 25°C)						
Pulsed Current	I _{SM}				100	A
Diode Forward Voltage ^b	V _{SD}	I _F = 50 A, V _{GS} = 0 V		1.2	1.5	V
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 50 A, di/dt = 100 A/μs		45	90	ns

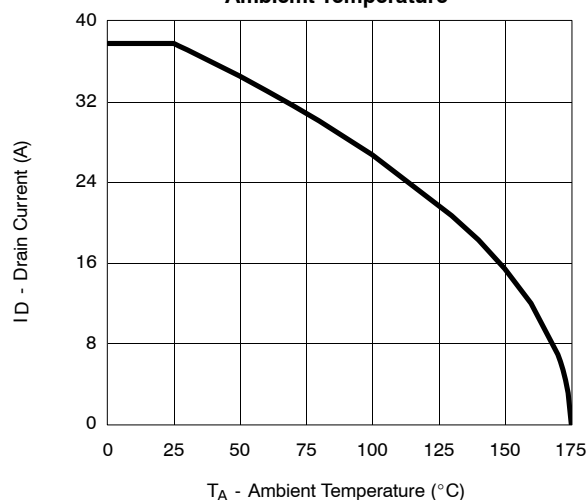
Notes

- a. Guaranteed by design, not subject to production testing.
b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
c. Independent of operating temperature.

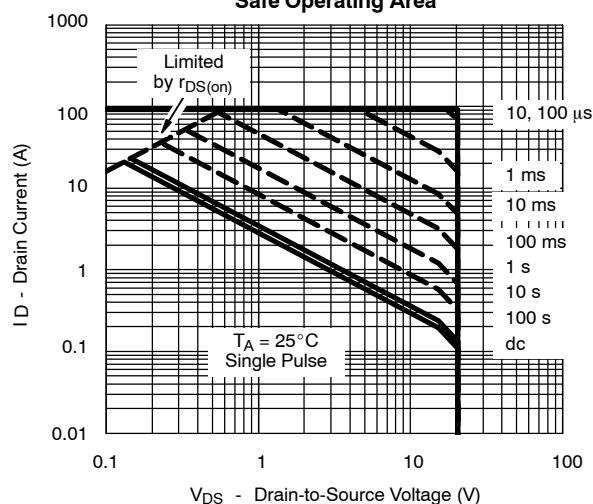
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

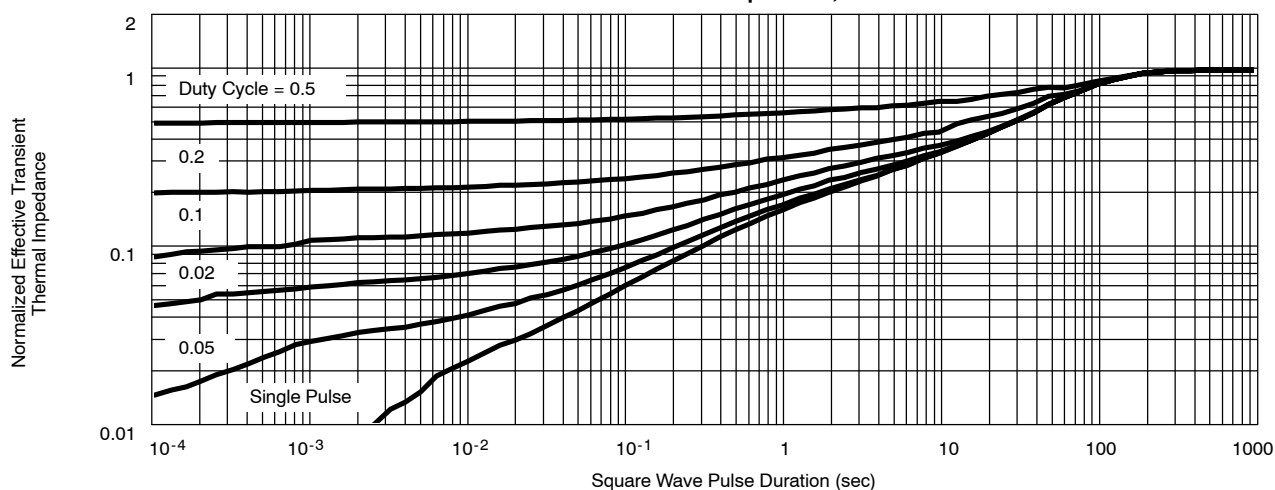
THERMAL RATINGS

Maximum Drain Current vs.
Ambient Temperature

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

