

Analog Signal Input Class D Amplifier with DC-DC Converter for Piezo Speaker

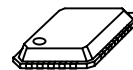
■ GENERAL DESCRIPTION

The **NJU8753** is an analog signal input class D power amplifier with DC-DC converter for Piezo speaker. Input part operates on 2.85V(typ) and a built-in DC-DC converter generates variable output voltage(up to 12V) with input voltage(2.6 to 4.2V). Therefore, it drives Piezo speaker with louder sound and high efficiency.

The **NJU8753** incorporates BTL amplifier, which eliminate AC coupling capacitors, capable of driving monaural Piezo speaker with simple external LC low-pass filters.

Class D operation achieves lower power operation for Piezo speaker, thus the NJU8753 is suited for battery-powered applications.

■ PACKAGE OUTLINE

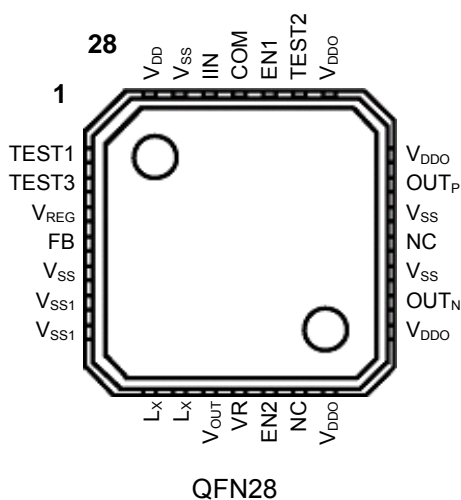


NJU8753KN1

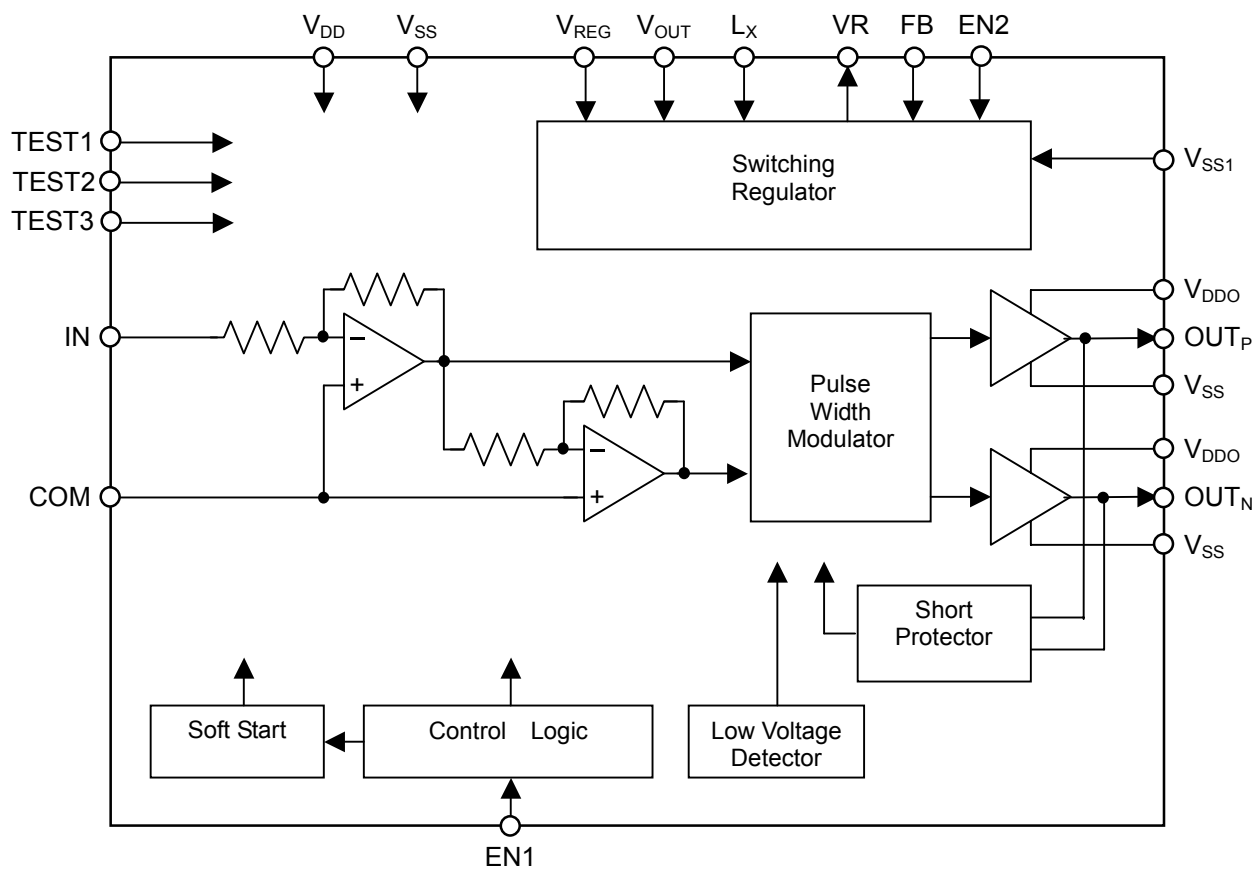
■ FEATURES

- Piezo Speaker Driving
- Output voltage: 8Vrms(Typ.) @ $V_{DDO}=10.0V$
- 1-channel Analog Signal Input, 1-channel BTL output
- Standby(Hi-Z),
- Built-in DC-DC Converter
- Built-in Low Voltage Detector
- Built-in Short Protector
- Operating Voltage :2.6~3.6V(V_{DD} , V_{REG})
:2.6~4.2V(V_{BAT})
- CMOS Technology
- Package Outline :QFN28

■ PIN CONFIGURATION



■ BLOCK DIAGRAM



■ PIN DESCRIPTION

| No. | SYMBOL | I/O | Function |
|--------|------------------|-----|--|
| 1 | TEST1 | I | Maker test 1 This pin must be connected to GND. |
| 2 | TEST3 | I | Maker test 3 This pin must be connected to GND. |
| 3 | V _{REG} | – | Switching regulator Power Supply : V _{REG} =2.85V |
| 4 | FB | I | Switching regulator Feedback resistor |
| 5 | V _{SS} | – | Power GND : V _{SS} =0V |
| 6,7 | V _{SS1} | – | Switching regulator Power GND : V _{SS1} =0V |
| 8,9 | L _X | I | Switching regulator voltage input |
| 10 | V _{OUT} | – | Switching regulator Power Supply : V _{out} =10.0V |
| 11 | VR | O | Switching regulator Output voltage |
| 12 | EN2 | I | Switching regulator Standby Control High : Step-up ON Low : Standby ON This pin must be connected to V _{REG} when step-up ON. |
| 13 | NC | – | Non connection |
| 14, 15 | V _{DDO} | – | Output Power supply |
| 16 | OUT _N | O | Negative Output |
| 17 | V _{SS} | – | Power GND : V _{SS} =0V |
| 18 | NC | – | Non connection |
| 19 | V _{SS} | – | Power GND : V _{SS} =0V |
| 20 | OUT _P | O | Positive output |
| 21, 22 | V _{DDO} | – | Output Power supply |
| 23 | TEST2 | I | Maker Test 2 This pin must be connected to GND. |
| 24 | EN1 | I | Power Amplifier Standby Control High : Standby OFF Low : Standby ON This pin must be connected to V _{DD} when Standby OFF. |
| 25 | COM | – | Analog common |
| 26 | IN | I | Audio Signal Input |
| 27 | V _{SS} | I | Power GND : V _{SS} =0V |
| 28 | V _{DD} | – | Power Supply : V _{DD} =2.85V |

*Pin No.5(V_{SS}), 6(V_{SS1}), 7(V_{SS1}), 17(V_{SS}), 19(V_{SS}), 27(V_{SS}) should be connected at the nearest point to the IC.

*Pin No.14(V_{DDO}) , 15(V_{DDO}), 21(V_{DDO}), 22(V_{DDO}) should be connected at the nearest point to the IC.

*Pin No.3(V_{REG}) and 28(V_{DD}) must be connected to V_{DD}, when these pins are not used.

■ FUNCTIONAL DESCRIPTION

(1) Signal Output

The OUT_P and OUT_N generate PWM output signals, which will be converted to analog signal via external 2nd-order or higher LC filter. A switching regulator with a high response against a voltage fluctuation is the best selection for the VDDO, which are the power supply for output drivers. To obtain better THD performance, the stabilization of the power is required.

(2) EN1

By setting the EN1 pin to "L", the standby mode is enabled. In the standby mode, the entire functions of the **NJU8753** enter a low-power state, and the output pins (OUT_P and OUT_N) are high impedance.

(3) Low Voltage Detector

When the power supply voltage drops down to below $V_{DD}(\text{MIN})$, the internal oscillation is halted for prevention to generate unwanted frequency, and the output pins (OUT_P , OUT_N) become in high impedance.

(4) Step-up switching regulator

The switching regulator is used as power supply(V_{DDO}) for power amplifier of class-D. The PFM controlled switching regulator works with external components, which are coil, capacitor, Schottky-diode and resistor for Step-up voltage.

By setting the EN2 pin to "H", the step-up operation is enabled, and in case of "L", standby mode is enabled. Step-up voltage is set by internal reference voltage($V_{REF} / 2$) and external resistors.

The step-up voltage can be calculated by the following methods:

<Calculation of the step-up voltage>

The step-up voltage is determined by internal reference voltage(V_{REF}), R_1 and R_2 . (See Figure.2)

ex. $V_{DD}=2.85V$, Internal reference voltage(V_{REF}) = $V_{REG} / 2 = 1.425[V]$, $R_1=2M\Omega$, $R_2=330k\Omega$
" Step-up voltage[V] = $V_{REF} \times ((R_1 + R_2) / R_2) = 1.425 \times ((2M + 330k) / 330k) = 10.06[V]$ "

Note 1) Apply V_{REG} first, next V_{BAT} . Otherwise, the voltage stress may cause a permanent damage to the IC.

(5) Short Circuit Protection

The short protector, which protects the **NJU8753** against high short-circuit current, turns off the output driver. After about 5 seconds from the protection, the NJU8753 returns to normal operation. The short protector functions at the following accidents.

- Short between OUT_P and OUT_N
- Short between OUT_P and V_{SS}
- Short between OUT_N and V_{SS}

Note 1) The detectable current and the period for the protection depend on the power supply voltage and ambient temperature.

Note 2) The short protector is not effective for a long term short-circuit current but for an instantaneous accident. Continuous high-current may cause permanent damage to the **NJU8753**.

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

| PARAMETER | SYMBOL | RATING | UNIT |
|------------------------------|------------------|-----------------------------|------|
| Supply Voltage | V _{DD} | -0.3 ~ +4.0 | V |
| | V _{REG} | -0.3 ~ +4.0 | V |
| | V _{DDO} | -0.3 ~ +15.0 | V |
| L _X Input Voltage | V _{LX} | -0.3 ~ +15.0 | V |
| FB Input Voltage | V _{FB} | -0.3 ~ +4.0 | V |
| Input Voltage | V _{in} | -0.3 ~ V _{DD} +0.3 | V |
| Operating Temperature | Topr | -40 ~ +85 | °C |
| Storage Temperature | Tstg | -40 ~ +125 | °C |
| Power Dissipation | P _D | 640* | mW |

* : Mounted on a glass epoxy PCB(50mm x 50mm x 1.6mm).

Note 1) All voltage are relative to “V_{SS}= 0V” reference.

Note 2) The LSI must be used inside of the “Absolute maximum ratings”. Otherwise, a stress may cause permanent damage to the LSI.

Note 3) De-coupling capacitors for V_{DD}(Pin 28)-V_{SS}(Pin 27), V_{REG}(Pin 3)-V_{SS}(Pin 5) and L_X(Pin 8,9)-V_{SS}(Pin 17,18,19) should be connected for stable operation.

■ ELECTRICAL CHARACTERISTIC

(Ta=25°C, V_{DD}=V_{REG}=2.85V, V_{BAT}=3.7V, V_{DDO}=10V, V_{SS}=0V, V_{SS1}=0V,
TEST1=TEST2=TEST3=0V, EN1=EN2=2.85V,

Input Signal=1kHz, Input Signal Level=150mVrms, Frequency Band=20Hz~20kHz,
Load Impedance=2.0μF 2nd-order 11.5kHz LC Filter(Q=0.72))

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNIT | Note |
|--|-------------------|--|--------------------|------|--------------------|------|------|
| V _{DD} Supply Voltage | V _{DD} | | 2.6 | 2.85 | 3.6 | V | |
| V _{REG} Supply Voltage | V _{REG} | | 2.6 | 2.85 | 3.6 | V | |
| V _{BAT} Input Voltage | V _{BAT} | | 2.6 | 3.7 | 4.2 | V | |
| V _{DDO} Supply Voltage | V _{DDO} | | 8.0 | 10.0 | 12.0 | V | |
| Input Impedance | Z _{IN} | IN pin | - | 20 | - | KΩ | |
| Voltage Gain | A _V | | - | 31 | - | dB | |
| Output THD | THD | Input Signal Level =200mVrms | - | 0.05 | 0.08 | % | 4 |
| Maximum Output | V _O | Output THD=10% | - | 8.0 | - | Vrms | |
| S/N | SN | A weight | - | 80 | - | dB | 4 |
| Operating Current(Standby) | I _{ST} | EN1=EN2=0V | - | - | 1 | μA | |
| Operating Current (No signal input) | I _{SS} | No-load operating No Signal Input | - | 10 | 15 | mA | |
| Input Voltage | V _{IH} | EN1, EN2 pins | 0.7V _{DD} | - | V _{DD} | V | |
| | V _{IL} | EN1, EN2 pins | 0 | - | 0.3V _{DD} | V | |
| Input Leakage Current | I _{LK} | EN1, EN2 pins | - | - | ±1.0 | μA | |
| Switching regulator Oscillating Frequency | f _{OSC} | | 220 | 300 | 380 | kHz | |
| Switching regulator Maximum Load Current | I _{OUT} | Step-up Voltage =10.0V V _{OUT} = 10.0V X 95% | 50 | - | - | mA | |
| Switching regulator Load Stability | ΔV _{OUT} | Step-up Voltage =10.0V I _{OUT} =10mA~50mA | - | 100 | - | mV | |

Note 4) Test system of the output THD and S/N

The output THD and S/N are tested in the system shown in Figure 1, where a 2nd-order LC LPF and another filter incorporated in an audio analyzer are used.

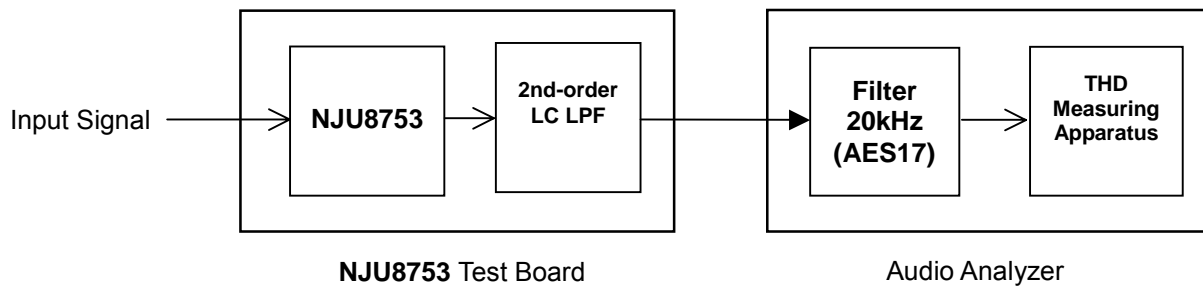


Figure 1. Output THD and S/N Test System

| | |
|------------------|---|
| 2nd-order LC LPF | : Refer to "Typical Application Circuit" |
| Filters | : 22Hz HPF + 20kHz LPF(AES17) (with the A-Weight filter for S/N and Dynamic-range tests) |

