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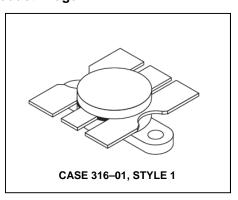
Designed primarily for wideband large-signal output amplifier stages in the 100 to 500 MHz frequency range.

- Guaranteed performance @ 400 MHz, 28 Vdc Output power = 80 W over 225 to 400 MHz Band Minimum gain = 7.3 dB @ 400 MHz
- Built-in matching network for broadband operation using double match technique
- 100% tested for load mismatch at all phase angles with 30:1 **VSWR**
- Gold metallization system for high reliability applications
- Characterized for 100 =8 500 MHz

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	33	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	60	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	4.0	Vdc
Collector Current — Continuous — Peak	I <sub>C</sub>	9.0 12	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C (1) Derate above 25°C	P <sub>D</sub>	250 1.43	Watts W/°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

### **Product Image**



### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>eJC</sub>	0.7	°C/W

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	'	•		•	•
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 80 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	33	_	_	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 80 mAdc, V <sub>BE</sub> = 0)	V <sub>(BR)CES</sub>	60	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 8.0 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	4.0	_	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 80 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)CBO</sub>	60	_	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0)	Ісво	_	_	5.0	mAdc
ON CHARACTERISTICS	, -				
DC Current Gain (I <sub>C</sub> = 4.0 Adc, V <sub>CE</sub> = 5.0 Vdc)	h <sub>FE</sub>	20	_	80	_

NOTE: (continued)

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.

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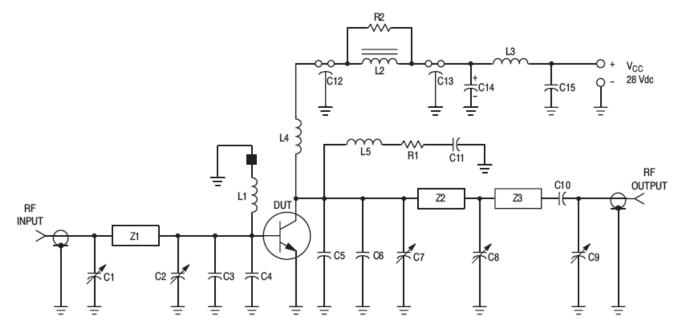
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### DYNAMIC CHARACTERISTICS

Output Capacitance	C <sub>ob</sub>	_	95	125	pF	l
(V <sub>CB</sub> = 28 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)						l

### ELECTRICAL CHARACTERISTICS - continued (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
FUNCTIONAL TESTS (Figure 1)			•	,	
Common–Emitter Amplifier Power Gain (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 80 W, f = 400 MHz)	G <sub>PE</sub>	7.3	9.0	_	dB
Collector Efficiency (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 80 W, f = 400 MHz)	η	50	60	_	%
Load Mismatch (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 80 W, f = 400 MHz, VSWR = 30:1 All Phase Angles)	Ψ	No Degradation in Output Power			



C1, C2, C7, C8, C9 — 1.0-20 pF Piston Trimmer (Johanson JMC 5501)

C3, C4 — 36 pF ATC 100 mil Chip Capacitor

C5, C6 - 43 pF ATC 100 mil Chip Capacitor

C10 — 100 pF UNELCO

C11, C15 — 0.1  $\mu F$  Erie Redcap

C12, C13 - 680 pF Feedthru

C14 — 1.0 µF 50 V Tantalum

L1 — 4 Turns #22 AWG Enameled, 3/16" ID Closewound with Ferroxcube Bead (#56–590–65/4B) on Ground End of Coil

L2 — Ferroxcube VK200-19/4B Ferrite Choke

L3 — 7 Turns #18 AWG, 11/16" Long, Wound on a 100 kΩ 2.0 Watt Resistor

L4 — 6 Turns #20 AWG Enameled, 3/16" ID Closewound

L5 - 4 Turns #22 AWG Enameled, 1/8" ID Closewound

Z1 - Microstrip 0.2" W x 1.5" L

Z2 - Microstrip 0.17" W x 1.16" L

Z3 - Microstrip 0.17" W x 0.63" L

R1, R2 — 10 Ω 2.0 Watt

Board — Glass Teflon  $\varepsilon_{\rm f}$  = 2.56, t = 0.062"

Input/Output Connectors Type N

DUT Socket Lead Frame Etched from 80-mil-Thick Copper

Figure 1. 400 MHz Test Circuit

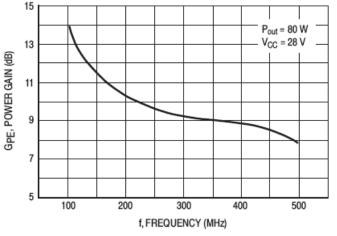
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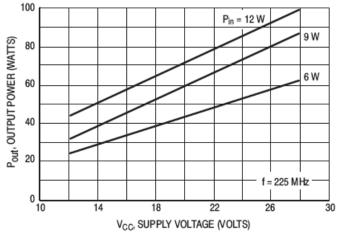
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120 100 80 80 10 W 10 W 7.5 W 10 V<sub>CC</sub> = 28 V 10 V<sub>CC</sub> = 28 V 10 MHz)

Figure 2. Power Gain versus Frequency

Figure 3. Output Power versus Frequency



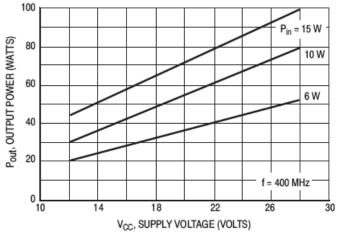


Figure 4. Output Power versus Supply Voltage

Figure 5. Output Power versus Supply Voltage



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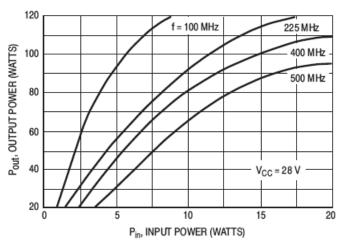


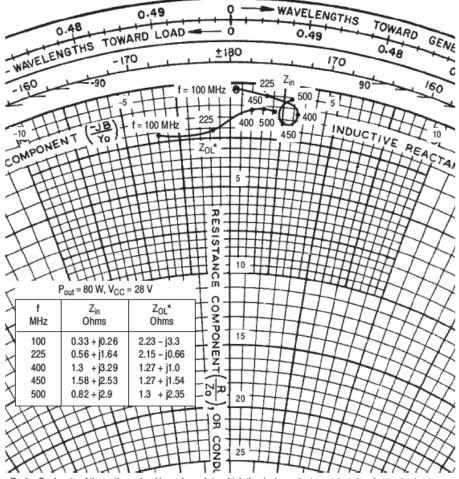
Figure 6. Output Power versus Input Power

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Z<sub>OL</sub>\* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

Figure 7. Series Equivalent Input-Output Impedance

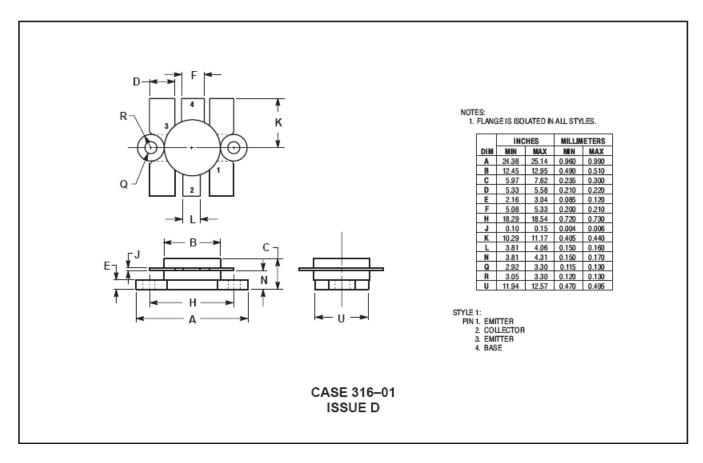
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### PACKAGE DIMENSIONS



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