

RECTIFIER DIODE

Double-diffused glass-passivated rectifier diode in hermetically sealed axial-leaded glass envelope, intended for use in general industrial applications where a high repetitive peak reverse voltage is required.

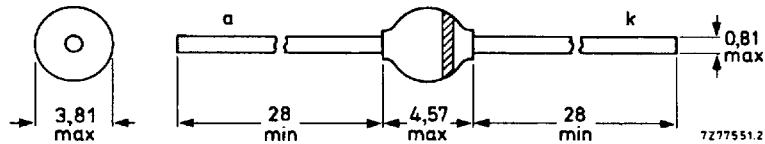
QUICK REFERENCE DATA

Crest working reverse voltage	V_{RWM}	max.	800 V
Repetitive peak reverse voltage	V_{RRM}	max.	1600 V
Average forward current	$I_F(AV)$	max.	1,2 A
Non-repetitive peak forward current	I_{FSM}	max.	25 A
Junction temperature	T_j	max.	175 °C

MECHANICAL DATA

Dimensions in mm

Fig. 1 SOD-57.



The marking band indicates the cathode.

RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Crest working reverse voltage	V_{RWM}	max.	800 V
Repetitive peak reverse voltage ($\delta \leq 1\%$)	V_{RRM}	max.	1600 V
Average forward current (averaged over any 20 ms period);			
$T_{tp} = 50\text{ }^{\circ}\text{C}$; lead length 10 mm	$I_{F(AV)}$	max.	1,2 A
$T_{amb} = 60\text{ }^{\circ}\text{C}$; see Fig. 2	$I_{F(AV)}$	max.	0,6 A
Repetitive peak forward current	I_{FRM}	max.	5 A
Non-repetitive peak forward current			
$t = 10\text{ ms}$, half-sine wave;			
$T_j = T_{jmax}$ prior to surge;			
$V_R = V_{RWMmax}$	I_{FSM}	max.	25 A
Storage temperature	T_{stg}		$-65\text{ to }+175\text{ }^{\circ}\text{C}$
Junction temperature	T_j	max.	175 $^{\circ}\text{C}$

THERMAL RESISTANCE

Influence of mounting method

1. Thermal resistance from junction to tie-point at a lead length of 10 mm
 $R_{th\ j-tp} = 46\text{ K/W}$
2. Thermal resistance from junction to ambient;
device mounted on an 1,5 mm thick epoxy-glass
printed-circuit board; Cu-thickness $\geq 40\text{ }\mu\text{m}$; Fig. 2
 $R_{th\ j-a} = 100\text{ K/W}$
(See „Thermal model“)

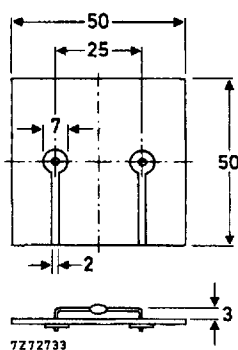


Fig. 2 Device mounted on a printed-circuit board.

CHARACTERISTICS

$T_j = 25\text{ }^{\circ}\text{C}$ unless otherwise specified

Forward voltage *

$$I_F = 2\text{ A}$$

Reverse current

$$V_R = V_{RWMmax}$$

$$V_R = V_{RWMmax}; T_j = 150\text{ }^{\circ}\text{C}$$

$$V_F < 1,5\text{ V}$$

$$I_R < 1\text{ }\mu\text{A}$$

$$I_R < 200\text{ }\mu\text{A}$$

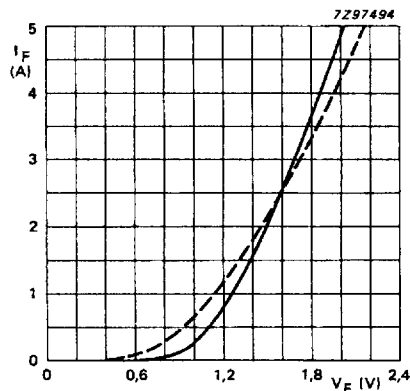


Fig. 3 Maximum forward voltage at
—— $T_j = 25\text{ }^{\circ}\text{C}$; ---- $T_j = 175\text{ }^{\circ}\text{C}$.

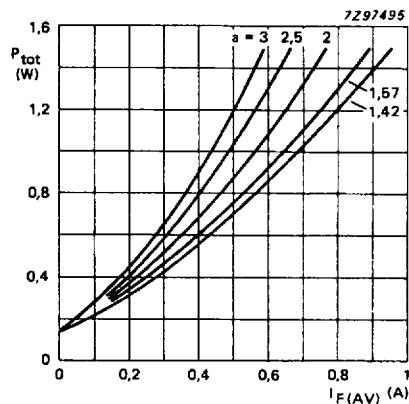


Fig. 4 Maximum steady state power dissipation
(forward plus leakage current) versus average
forward current.

$$a = I_F(RMS)/I_F(AV);$$

$$V_R = V_{RWMmax}, \delta = 0,5.$$

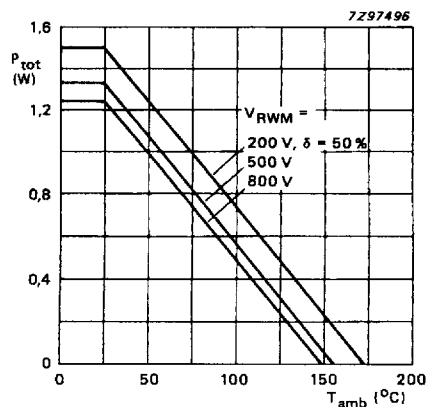


Fig. 5 Maximum steady state power dissipation
(forward plus leakage current) versus ambient
temperature.

$$a = I_F(RMS)/I_F(AV);$$

$$V_R = V_{RWMmax}, \delta = 0,5.$$

* Measured under pulse conditions to avoid excessive dissipation.

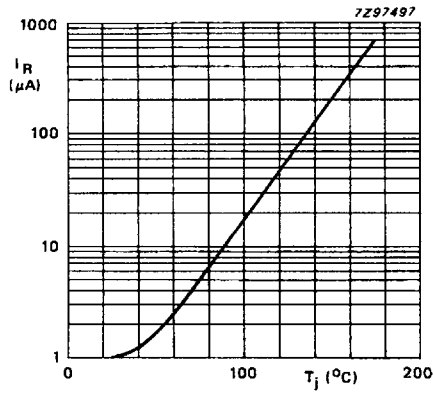


Fig. 6 Maximum reverse current versus junction temperature; $V_R = V_{RWMmax}$.

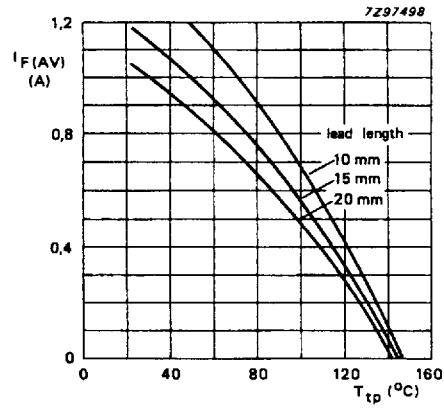


Fig. 7 Maximum average forward current vs. tie-point temperature; the curves include losses due to reverse leakage.
 $V_R = V_{RWMmax}$; $a = 1,57$.