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PATENTED:
THIS PRODUCT IS
COVERED BY U.S.
PATENT 5,736,910



DOCUMENT / PART NO.: SI-60102-F

TITLE : 10/100BT , SHIELDED

ISSUE	DESCRIPTION OF CHANGE							REVIEWED BY			APPROVED BY			ECN#
A0	Change the mechanical drawing and use new format							Bain Liu			Steve Weller			06380
PAGE	1	2	3	4	--	--	--	--	--	--	--	--	--	--
REV.	A0	A0	A0	A0	--	--	--	--	--	--	--	--	--	--
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REV.	--	--	--	--	--	--	--	--	--	--	--	--	24-0028-F REV A0	

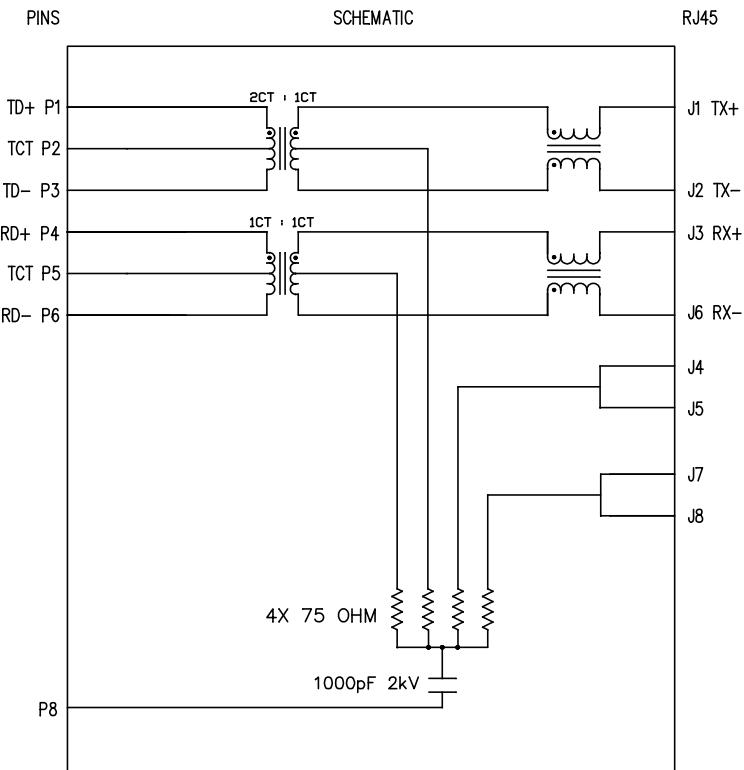
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ELECTRICAL CHARACTERISTICS @ 25°C

1.0 TURNS RATIO: (P6-P5-P4) : (J6-J3)	: 1CT : 1 ± 3%
(P3-P2-P1) : (J2-J1)	: 2CT : 1 ± 3%
2.0 INDUCTANCE: (P6-P4)	: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
(J3-J1)	: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
3.0 LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT)	: 0.3uH MAX. @ 1MHz
P3-P1 (WITH J2 AND J1 SHORT)	: 1.1uH MAX. @ 1MHz
4.0 INTERWINDING CAPACITANCE: (P6,P5,P4) TO (J6,J3)	: 30pF TYP @ 1MHz
(P3,P2,P1) TO (J2,J1)	: 30pF TYP @ 1MHz
5.0 DC RESISTANCE: (J6-J3)=(J2-J1)	: 1.2 ohms Max.
(P6-P4)	: 1.2 ohms Max.
(P3-P1)	: 1.5 ohms Max.
6.0 RETURN LOSS: 1MHz TO 30MHz	: 18dB MIN.
60MHz TO 80MHz	: 12dB MIN.
7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3)	: 1500 VAC
(J3, J6) TO (P4,P6)	: 1500 VAC
8.0 INSERTION LOSS: RS=RL=100 ohms	
100KHz TO 100MHz	: 1.1 dB TYP
9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS	
OUTPUT VOLTAGE = 1 V peak	: 3.0 nS TYP
PULSE WIDTH= 112nS	: 3.0 nS TYP
10.0 CROSS TALK: 1MHz TO 100MHz	: 35 dB TYP
11.0 COMMON TO COMMON MODE ATTENUATION:1MHz TO 100MHz	: 30 dB TYP



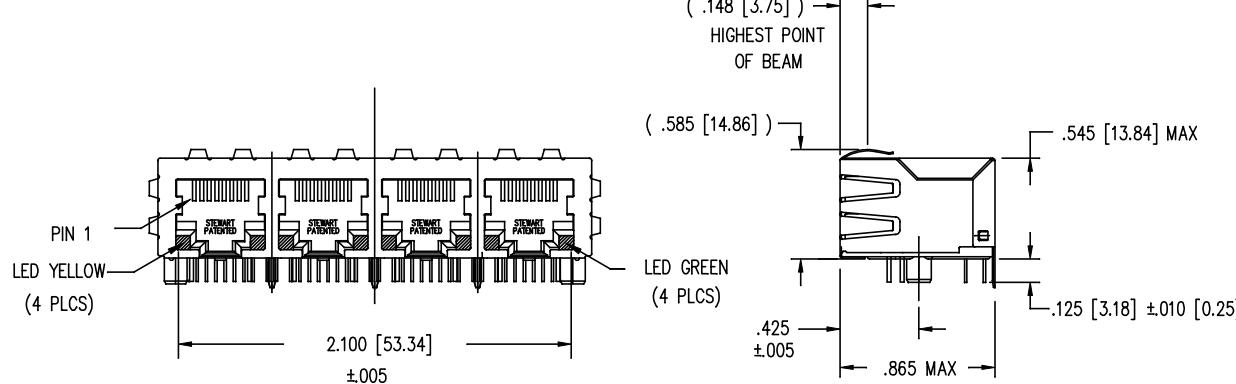
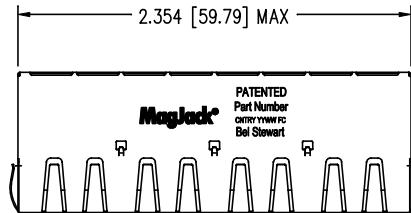
NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED

ORIGINATED BY Zeng Xiaochun	DATE 2011-12-08	TITLE 10/100BT , SHIELDED	PART NO. / DRAWING NO. SI-60102-F	STANDARD DIM. TOL. IN INCH	[] METRIC DIM. AS REFERENCE	
DRAWN BY Duan Jun	DATE 2011-12-08		FILE NAME SI-60102-F.DWG	.X	UNIT : INCH [mm] REV. : A0	
				.XX	SCALE : N/A	SIZE : A4
				XXX	±0.005	PAGE : 2

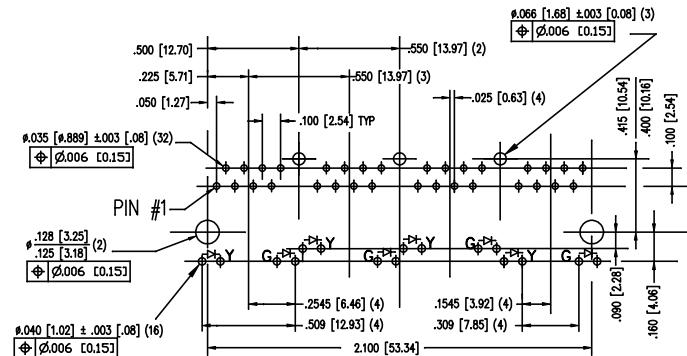
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LED SPECIFICATIONS			
STANDARD LED	FORWARD VOLTAGE (MAX)	* FORWARD VOLTAGE (TYP)	WAVELENGTH
YELLOW	2.5 V	2.1 V	590 nm
GREEN	2.6 V	2.25 V	555 nm

* WITH A FORWARD CURRENT OF 20 mA (TYP)



P.C.B. RECOMMENDED HOLE LAYOUT
SEEN FROM COMPONENT SIDE

ALL CENTERLINE DIMENSIONS ARE BASIC

NOTES:

1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: 50 MICRO-INCH SELECTIVE HARD GOLD PLATING OR EQUIVALENT IN CONTACT AREA
2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
3. TOLERANCES COMPLY WITH F.C.C.
DIMENSION REQUIREMENTS.
4. ALL TOLERANCES NOT OTHERWISE SPECIFIED
TO BE $\pm .005$ [0.13]
5. WAVE SOLDER COMPATIBLE – PREHEAT 125° C/90SECS.

ORIGINATED BY	DATE	TITLE
Zeng Xiaochun	2011-12-08	
DRAWN BY	DATE	10/100BT, SHIELDED PATENTED
Duan Jun	2011-12-08	

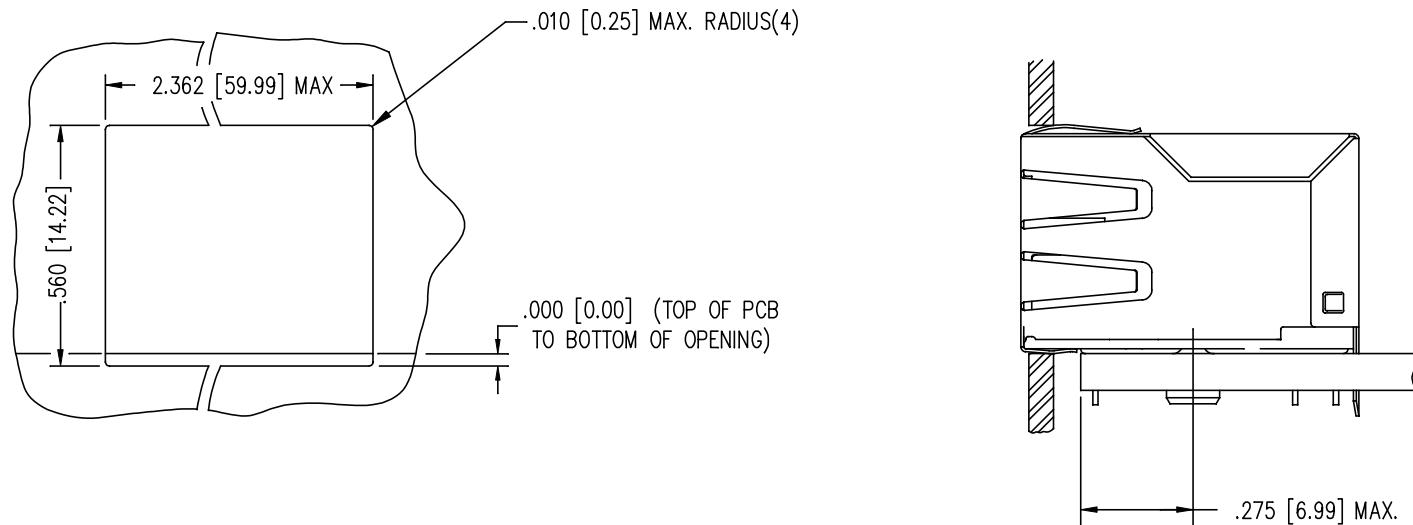
PART NO. / DRAWING NO.
SI-60102-F
FILE NAME
SI-60102-F.DWG

STANDARD DIM.
TOL. IN INCH
.X
.XX
.XXX
 $\pm .005$

[] METRIC DIM. AS REFERENCE
UNIT : INCH [mm] REV. : A0
SCALE : N/A SIZE : A4
PAGE : 3

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NOTES:

1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE USER THE ABILITY TO HAVE REASONABLE JACK/PANEL CLEARANCES, YET MAINTAIN GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]

ORIGINATED BY	DATE	TITLE	MagJack® 10/100BT, SHIELDED PATENTED	PART NO. / DRAWING NO. SI-60102-F	STANDARD DIM. TOL. IN INCH		[] METRIC DIM. AS REFERENCE	
DRAWN BY	DATE				.X	.XX	UNIT : INCH [mm]	REV. : A0
Zeng Xiaochun	2011-12-08						SCALE : N/A	SIZE : A4
Duan Jun	2011-12-08			SI-60102-F.DWG	.XXX	± 0.005		PAGE : 4

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COMPONENTS FOR A
CONNECTED
PLANET