

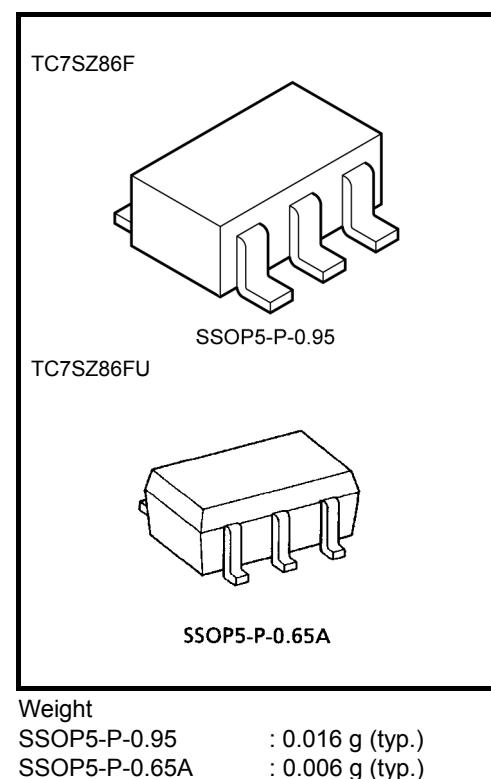
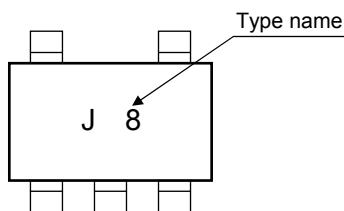
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ86F,TC7SZ86FU

EXCLUSIVE OR Gate

Features

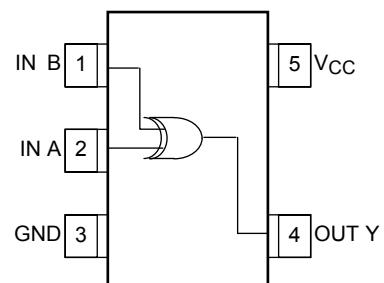
- High output drive: ± 24 mA (min) at $V_{CC} = 3$ V
- Super high speed operation: $t_{pd}=2.9$ ns (typ.)
at $V_{CC} = 5$ V, 50 pF
- Operation voltage range: V_{CC} (opr) = 1.8~5.5 V
- 5.5-V tolerant inputs
- 5.5-V power down protection output
- Matches the performance of TC74LCX series when operated at 3.3- V V_{CC}

**Marking**

Weight
 SSOP5-P-0.95 : 0.016 g (typ.)
 SSOP5-P-0.65A : 0.006 g (typ.)

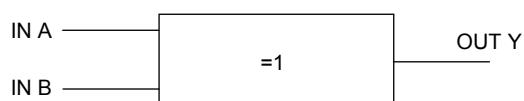
Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{CC}	-0.5~6	V
DC input voltage	V_{IN}	-0.5~6	V
DC output voltage	V_{OUT}	-0.5~6	V
Input diode current	I_{IK}	-20	mA
Output diode current	I_{OK}	-20	mA
DC output current	I_{OUT}	± 50	mA
DC V_{CC} /ground current	I_{CC}	± 50	mA
Power dissipation	P_D	200	mW
Storage temperature	T_{stg}	-65~150	$^\circ\text{C}$
Lead temperature (10s)	T_L	260	$^\circ\text{C}$

Pin Assignment (top view)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Logic Diagram**Truth Table**

Input		Output
A	B	Y
L	L	L
L	H	H
H	L	H
H	H	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.8~5.5	V
		1.5~5.5 (Note 1)	
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~5.5 (Note 2)	V
		0~V _{CC} (Note 3)	
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~20 (V _{CC} = 1.8 V, 2.5 V ± 0.2 V)	ns/V
		0~10 (V _{CC} = 3.3 V ± 0.3 V)	
		0~5 (V _{CC} = 5.5 V ± 0.5 V)	

Note 1: Data retention only

Note 2: V_{CC} = 0 V

Note 3: High or Low state

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition	V _{CC} (V)	Ta = 25°C			Ta = -40~85°C		Unit
				Min	Typ.	Max	Min	Max	
High-level input voltage	V _{IH}	—	1.8	0.75 × V _{CC}	—	—	0.75 × V _{CC}	—	V
			2.3-5.5	0.7 × V _{CC}	—	—	0.7 × V _{CC}	—	
Low-level input voltage	V _{IL}	—	1.8	—	—	0.25 × V _{CC}	—	0.25 × V _{CC}	V
			2.3-5.5	—	—	0.3 × V _{CC}	—	0.3 × V _{CC}	
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.8	1.7	1.8	—	1.7	V
				2.3	2.2	2.3	—	2.2	
				3.0	2.9	3.0	—	2.9	
				4.5	4.4	4.5	—	4.4	
				I _{OH} = -8 mA	2.3	1.9	2.15	—	1.9
			I _{OH} = -16 mA	3.0	2.4	2.8	—	2.4	V
				I _{OH} = -24 mA	3.0	2.3	2.68	—	2.3
				I _{OH} = -32 mA	4.5	3.8	4.2	—	3.8
				1.8	—	0	0.1	—	
				2.3	—	0	0.1	—	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 100 μA	3.0	—	0	0.1	—	V
				4.5	—	0	0.1	—	
				I _{OL} = 8 mA	2.3	—	0.1	0.3	
				I _{OL} = 16 mA	3.0	—	0.15	0.4	
			I _{OL} = 24 mA	I _{OL} = 24 mA	3.0	—	0.22	0.55	
				I _{OL} = 32 mA	4.5	—	0.22	0.55	
				I _{OL} = 32 mA	—	—	—	—	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND	0-5.5	—	—	±1	—	±10	μA
Power off leakage current	I _{OFF}	V _{IN} or V _{OUT} = 5.5 V	0.0	—	—	1	—	10	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND	5.5	—	—	2	—	20	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Ta = 25°C			Ta = -40~85°C		Unit
				Min	Typ.	Max	Min	Max	
Propagation delay time	t _{pLH}	C _L = 15 pF, R _L = 1 MΩ	1.8	2.0	5.7	11.5	2.0	12.0	ns
			2.5 ± 0.2	0.8	3.8	8.0	0.8	8.5	
			3.3 ± 0.3	0.5	3.0	5.7	0.5	6.0	
			5.0 ± 0.5	0.5	2.4	5.0	0.5	5.4	
			C _L = 50 pF, R _L = 500 Ω	3.3 ± 0.3	1.2	3.5	6.2	1.5	6.5
				5.0 ± 0.5	0.8	2.9	5.4	1.0	5.8
Input capacitance	C _{IN}	—	0~5.5	—	4	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 4)	3.3	—	21	—	—	—	pF
			5.5	—	24	—	—	—	

Note 4: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

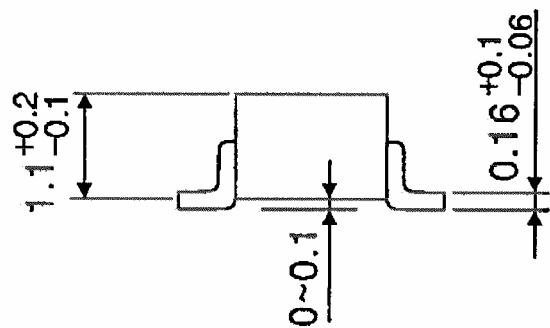
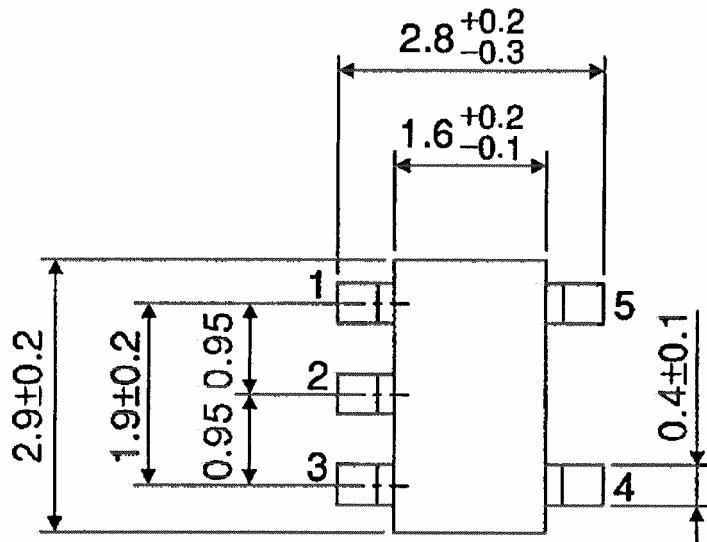
Average operating current can be obtained by the equation:

$$I_{CC} (\text{opr.}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SSOP5-P-0.95

Unit : mm

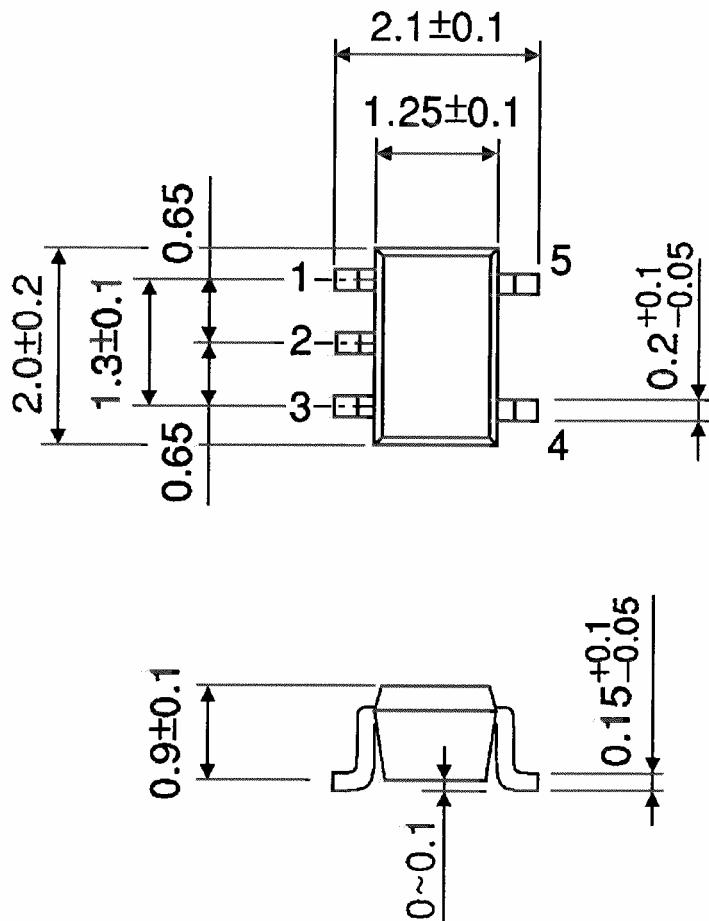


Weight: 0.016 g (typ.)

Package Dimensions

SSOP5-P-0.65A

Unit : mm



Weight: 0.006 g (typ.)

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20070701-EN GENERAL

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