

HEXFRED® Ultrafast Soft Recovery Diode, 60 A



SOT-227

PRODUCT SUMMARY					
V _R	1200 V				
V _F (typical)	2.2 V				
t _{rr} (typical)	145 ns				
I _{F(DC)} at T _C	30 A at 120 °C				
Package	SOT-227				
Circuit configuration Two separate diodes					

FEATURES

- Fast recovery time characteristic
- · Electrically isolated base plate
- Antiparallel diodes
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION/APPLICATIONS

This SOT-227 modules with HEXFRED® rectifier are in antiparallel configuration. The antiparallel configuration is used for simple series rectifier and high voltage application. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as HV power supplies, electronic welders, motor control and inverters.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		1200	V	
Continuous forward current	I _F	T _C = 120 °C	30		
Single pulse forward current	I _{FSM}	T _J = 25 °C	350	Α	
Maximum repetitive forward current	I _{FRM}	Rated $V_{R,}$ square wave, 20 kHz, T_{C} = 60 °C	130		
Maximum power dissipation	В	T _C = 25 °C	312	W	
Maximum power dissipation	P _D	T _C = 100 °C	125		
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500	V	
Operating junction and storage temperature range	T _J , T _{Stg}		- 55 to 150	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	1200	-	-	
		I _F = 30 A	-	2.2	3.0	V
Forward voltage	vard voltage V _{FM}	I _F = 60 A	-	2.7	3.8	
		I _F = 60 A, T _J = 150 °C	-	2.1	-	
Reverse leakage current I _{RM}		V _R = V _R rated	-	2.0	75	μΑ
		T _J = 150 °C, V _R = V _R rated	-	2.7	10	mA



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time t _{rr}	+	T _J = 25 °C	$I_F = 50 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	145	-	- ns
	L _{rr}	T _J = 125 °C		-	218	-	
Peak recovery current	I _{RRM}	T _J = 25 °C		-	13	=	Α
		T _J = 125 °C		-	19	=	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	910	-	nC
		T _J = 125 °C		-	1920	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	В		-	-	0.4	
Junction to case, both legs conducting	- R _{thJC}		-	-	0.2	°C/W
Case to heatsink	R _{thCS}	Flat, greased and surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	-	1.3	Nm
Case style			SOT-227			

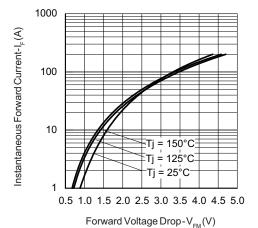


Fig. 1 - Typical Forward Voltage Drop Characteristics

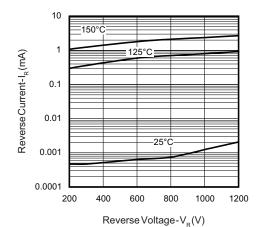


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

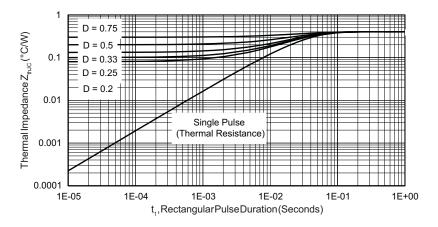


Fig. 3 - Maximum Thermal Impedance Z_{thJC} Characteristics

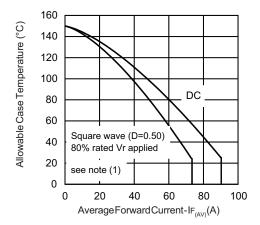


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

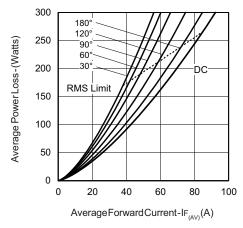


Fig. 5 - Forward Power Loss Characteristics

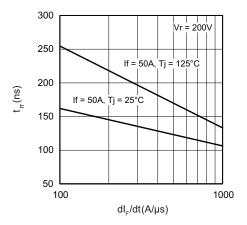


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

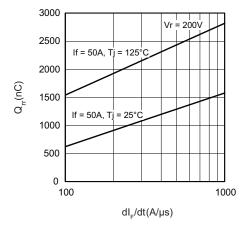


Fig. 7 - Typical Stored Charge vs. dl_F/dt

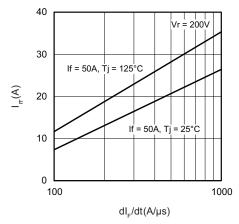


Fig. 8 - Typical Peak Recovery Current vs. dl_F/dt

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$

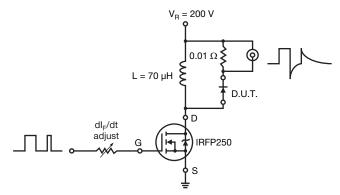
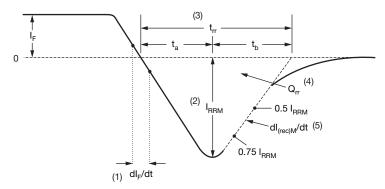


Fig. 9 - Reverse Recovery Parameter Test Circuit



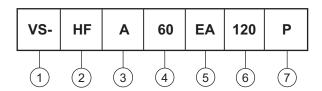
- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$
 - $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (5) dl_{(rec)M}/dt peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



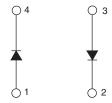
ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 HEXFRED® family
- 3 Process designator (A = Electron irradiated)
- 4 Average current (60 = 60 A)
- 5 Package outline (EA = SOT-227, 2 diodes antiparallel)
- 6 Voltage rating (120 = 1200 V)
- 7 P = Lead (Pb)-free

CIRCUIT CONFIGURATION

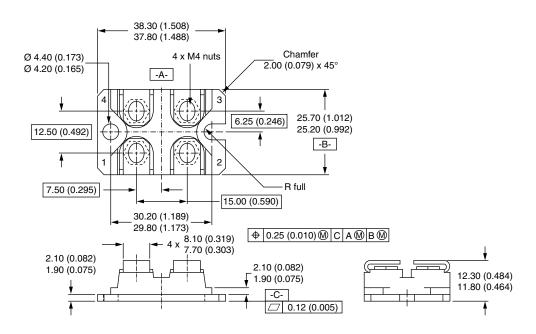


LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95036</u>				
Packaging information <u>www.vishay.com/doc?95037</u>				



SOT-227

DIMENSIONS in millimeters (inches)



Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
- · Controlling dimension: millimeter

Document Number: 95036 Revision: 28-Aug-07



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