

- * LVDS Output
- * Field Programmable
- * Industry Standard Packaging

Applications

- * Serial Communications
- * Routers
- * Switches
- * WAN Interfaces
- * Test Equipment

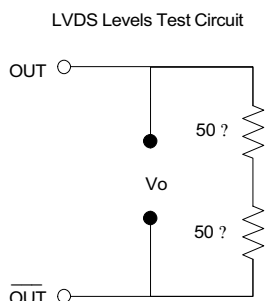


Part Numbering Example: CPPV7Z-A7BR-Freq.

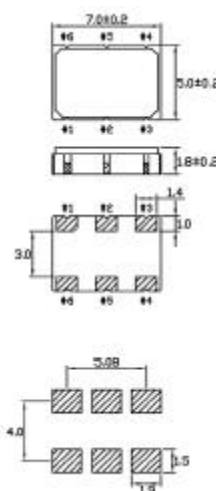
CPP	V	7	Z	A7	BR	XX.XXXX
SERIES	OUTPUT	PACKAGE STYLE	ADDED FEATURES	OPERATING TEMP.	STABILITY	FREQUENCY
CPP	V= LVDS	7 = 5X7 Ceramic	Blank = Bulk T = Tube Z = Tape and Reel	Blank = - 0°C + 70°C A5 = - 20°C + 70°C A7 = - 40°C + 85°C	B6 = ± 100 ppm BP = ± 50 ppm BR = ± 25 ppm	1 ~ 200 MHz

Specifications:	Min	Typ	Max	Unit
Frequency Range:	1		200	MHz
Stability:	-25		+ 25	ppm
Supply Voltage:	3.125	3.3	3.465	V
Operating Temperature:	-40		+ 85	°C
Storage Temperature:	-55		+ 125	°C
Duty Cycle:	45		55	%
Start-Up Time:		3	10	mS
Aging: (ppm/1st Year) Ta=25°C, Vdd=3.3V			5	ppm
Static Discharge Voltage:	2000			V
Supply Current:		40	50	mA
Short Circuit Current:		± 50		mA
Jitter (RMS) Period:		6	10	pS
Output Voltage Voh Vol	0.9		1.6	V V
Rise/Fall Time:		0.7	1.0	nS
Output Level:	LVDS			
Packaging:	Tape and Reel 1000 pcs per Reel			

Tristate internal pull up,
output active when high



Dimensions are in mm



Solder Pad Layout

- PIN Function
- 1 OE (CMOS)
 - 2 Do Not Connect
 - 3 Ground
 - 4 LVDS +
 - 5 LVDS -
 - 6 Vdd