

July 2008

TIP41/TIP41A/TIP41B/TIP41C NPN Epitaxial Silicon Transistor

Features

• Complementary to TIP42/TIP42A/TIP42B/TIP42C



1. Base 2. Collector 3. Emitter

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Emitter Voltage: TIP41 : TIP41A : TIP41B : TIP41C	40 60 80 100	V V V
V _{CEO}	Collector-Emitter Voltage: TIP41 : TIP41A : TIP41B : TIP41C	40 60 80 100	V V V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current (DC)	6	А
I _{CP}	Collector Current (Pulse)	10	Α
I _B	Base Current	2	А
P _C	Collector Dissipation (T _C =25°C)	65	W
	Collector Dissipation (T _a =25°C)	2	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 65 ~ 150	°C

Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage : TIP41 : TIP41A : TIP41B : TIP41C	I _C = 30mA, I _B = 0	40 60 80 100		V V V
I _{CEO}	Collector Cut-off Current : TIP41/41A : TIP41B/41C	V _{CE} = 30V, I _B = 0 V _{CE} = 60V, I _B = 0		0.7 0.7	mA mA
I _{CES}	Collector Cut-off Current : TIP41 : TIP41A : TIP41B : TIP41C	$V_{CE} = 40V, V_{EB} = 0$ $V_{CE} = 60V, V_{EB} = 0$ $V_{CE} = 80V, V_{EB} = 0$ $V_{CE} = 100V, V_{EB} = 0$		400 400 400 400	μΑ μΑ μΑ μΑ
I _{EBO}	Emitter Cut-off Current	V _{EB} = 5V, I _C = 0		1	mA
h _{FE}	* DC Current Gain	V _{CE} = 4V,I _C = 0.3A V _{CE} = 4V, I _C = 3A	30 15	75	
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = 6A, I _B = 600mA		1.5	V
V _{BE} (sat)	* Base-Emitter Saturation Voltage	V _{CE} = 4V, I _C = 6A		2.0	V
f _T	Current Gain Bandwidth Product	V _{CE} = 10V, I _C = 500mA, f = 1MHz	3.0		MHz

^{*} Pulse Test: PW≤300ms, Duty Cycle≤2%

Typical Characteristics

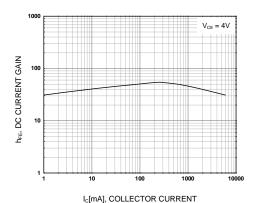


Figure 1. DC current Gain

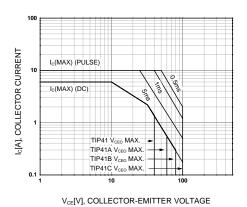


Figure 3. Safe Operating Area

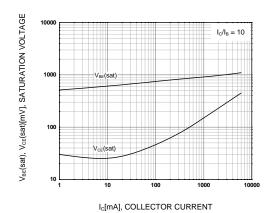


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

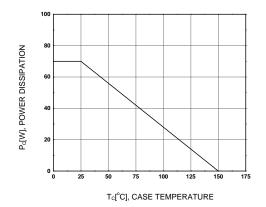
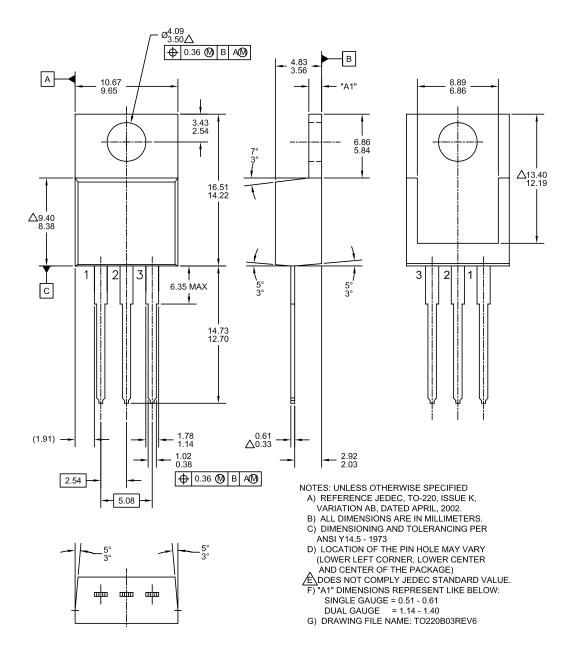


Figure 4. Power Derating

Mechanical Dimensions

TO220







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