SN54153, SN54LS153, SN54S153 SN74153, SN74LS153, SN74S153

DECEMBER 1972 - REVISED MARCH 1988

DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

- Permits Multiplexing from N lines to 1 line
- Performs Parallel-to-Serial Conversion
- Strobe (Enable) Line Provided for Cascading (N lines to n lines)
- High-Fan-Out, Low-Impedance, Totem-Pole Outputs
- Fully Compatible with most TTL Circuits

ТҮРЕ	•	TYPICAL AVERAGE PROPAGATION DELAY TIMES				
	FROM DATA	FROM STROBE	FROM SELECT	DISSIPATION		
153	14 ns	17 ns	22 ns	180 mW		
LS153	14 ns	19 ns	22 ns	31 mW		
'S153	6 ns	9.5 ns	12 ns	225 mW		

description

Each of these monolithic, data selectors/multiplexers contains inverters and drivers to supply fully complementary, on-chip, binary decoding data selection to the AND-OR gates. Separate strobe inputs are provided for each of the two four-line sections.

FUNCTION TABLE

F	ECT UTS		ATA	NPUT:	S	STROBE	ООТРОТ
В	Α	CO	C1	C2	C3	Ğ	Υ
×	×	x	X	X	×	Н	L
L	L	L	X	Х	×	Ł	L
L	L	Н	Х	X	х	L	н
L	Н	х	L	X	X	Ł	L
L	н	×	Н	X	×	L	H
н	L	х	X	L	x	L	L
Н	L	х	Х	Н	×	Ļ	н
H	н	×	х	Х	ᆫ	L	L
н	Н	х	Χ	х	н	L	н

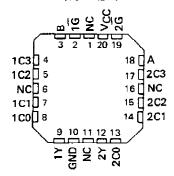
Select inputs A and B are common to both sections.

H = high level, L = low level, X = irrelevant

SN54153, SN54LS153, SN54S153 . . . J OR W PACKAGE SN74153 . . . N PACKAGE SN74LS153, SN74S153 . . . D OR N PACKAGE (TOP VIEW)

1 <u>G</u> [\bigcup_{16}	□ vcc
в[2	15	□ 2G
1C3[]3	14	ΠA
1C2[]4	13	2C3
1C1 []5	12] 2C2
100	a[11] 2C1
1Y 🛚	7	10] 2C0
GND [8	9] 2Y

SN54LS153, SN54S153 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

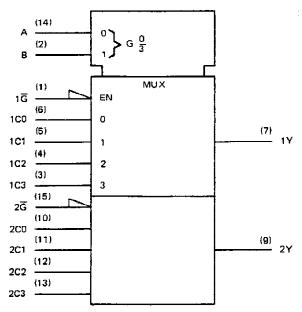
absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (See Note 1)		7 V
Input voltage: '153, '\$153		5.5 V

Operating free-air temperature range:	SN54'	-55°C to 125°C
	SN74'	0°C to 70°C
Storage temperature range		65 9C to 150 9C

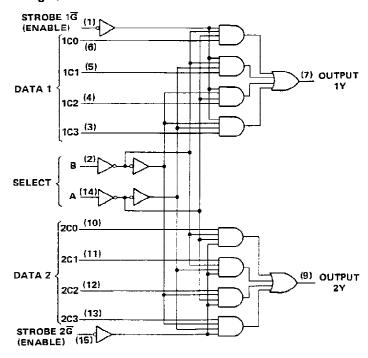
NOTE 1: Voltage values are with respect to network ground terminal.

logic symbol†



[†]This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

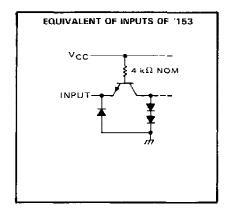
logic diagrams (positive logic)

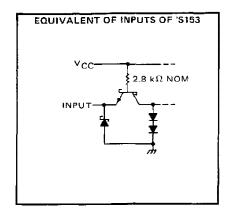


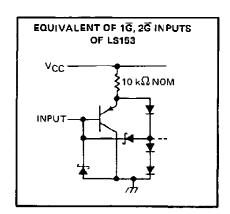
Pin numbers shown are for D, J, N, and W packages.

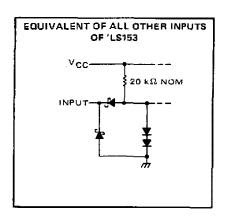


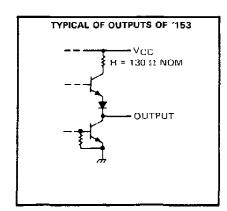
schematics of inputs and outputs

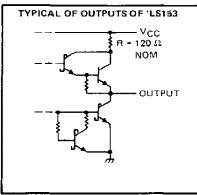


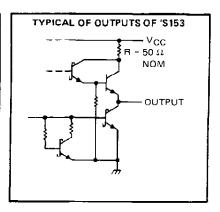












SN54153, SN74153 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

recommended operating conditions

		SN54153			SN74153		
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Supply voltage, V _{CC}	4.5	5	5.5	4.75	5	5.25	V
High-level output current, IOH			-800			-800	μД
Low-level output current, IOL			16		·	16	mA
Operating free-air temperature, TA	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

			SN54153			SN74153			
	PARAMETER	TEST CONDITIONS [†]		TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIH	High-level input voltage		2			2			٧
VIL	Low-level input voltage				8.0			8.0	V
Vik	Input clamp voltage	V _{CC} = MIN, I ₁ = -12 mA			-1.5			-1.5	V
Voн	High-level output voltage	V _{CC} = MIN, V _{IH} = 2 V, V _{IL} = 0.8 V, I _{OH} = -800 μA	2.4	3.4		2.4	3.4		٧
VOL	Low-level output voltage	V _{CC} = MIN, V _{IH} = 2 V, V _{IL} = 0.8 V. I _{OL} = 16 mA		0.2	0.4		0.2	0.4	٧
tı	Input current at maximum input voltage	V _{CC} = MAX, V _I = 5.5 V			1			1	mA
ЧН	High-level input current	V _{CC} = MAX, V _I = 2.4 V			40			40	μΑ
l _{IL}	Low-level input current	V _{CC} = MAX, V _I = 0.4 V			-1.6	ľ		-1.6	mA
los	Short-circuit output current §	V _{CC} = MAX	-20		55	-18		-57	mA
ICCL	Supply current, output fow	V _{CC} = MAX, See Note 2		36	52		36	60	mA

[†]For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, VCC = 5 V, TA = 25°C

PARAMETER¶	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
tPLH	Data	Y			12	18	ns
t _{PHL}	Data	Y	1		15	23	กร
tPLH	Select	Y	CL = 30 pF, RL = 400 Ω,		22	34	ns
[†] PHL	Select	Y	See Note 3		22	34	П\$
^t PLH	Strobe G	Y	1		19	30	กร
tPHL	Strobe G	Y	7		15	23	กร

 $[\]mathbf{1}_{\text{tPLH}}$ = propagation delay time, low-to-high-level output

 $[\]ddagger$ All typical values are at V_{CC} = 5 V, T_A = 25°C.

Not more than one output should be shorted at a time.

NOTE 2: I_{CCL} is measured with the outputs open and all inputs grounded.

tpHL = propagation delay time, high-to-low-level output

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

recommended operating conditions

		s	SN54LS153			N74L\$1	53	UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5,5	4.75	5	5,25	V
۷ін	High-level input voltage	2			2			٧
VIL	Low-level input voltage			0.7			8.0	V
ЮН	High-level output current			- 0.4			- 0.4	mΑ
loL	Low-level output current			4			8	mA
TA	Operating free-air temperature	55		125	O		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS †		S	N54LS1	53	s	N74LS1	53	
PANAMETER	TEST GONDITIONS		MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
٧ıĸ	V _{CC} = MIN, I ₁ = - 18 mA				- 1.5		-	– 1.5	٧
Voн	$V_{CC} = MIN$, $V_{IH} = 2V$, $V_{IL} = MAX$ $l_{OH} = -0.4 \text{ mA}$		2.5	3.4	·	2.7	3.4		٧
Va.	V _{CC} = MIN, V _{IH} = 2 V,	IOL = 4 mA		0.25	0.4		0.25	0.4	
VOL	VIL = MAX,	I _{OL} = 8 mA	-		•		0.35	0.5	٧
Ιq	VCC = MAX, VI = 7 V				0.1			0.1	mΑ
liH	V _{CC} = MAX, V ₁ = 2.7 V				20			20	μA
1G, 2G	V _{CC} = MAX, V _I = 0.4 V				- 0.2			-0.2	mΑ
All other	VCC - MAX, VI - 0.4 V				- 0.4			- 0.4	mA
lOs §	VCC = MAX		20		- 100	- 20	•	- 100	mΑ
₁ CCL	V _{CC} = MAX, See Note 2			6.2	10		6.2	10	mД

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

NOTE 2: ICCL is measured with the outputs open and all inputs grounded.

switching characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER¶	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
tрLH	Data	Y			10	15	ns
tpHL.	Data	Y	CL = 15 pF,		17	26	пѕ
tPLH .	Select	V	R _L = 2 kΩ,		19	29	пѕ
tPHL	Select	Υ	See Note 3		25	38	ns
t P LH	Strobe G	Y	300 NO.6 3		16	24	ns
^t PHL	Strabe G	Y			21	32	ns

 $[\]P_{\text{tpLH}}$ = propagation delay time, low-to-high-level output

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

[‡] All typical values are at V_{CC} = 5 V, T_A = 25°C.

[§]Not more than one output should be shorted at a time.

tpHL = propagation delay time, high-to-low-level output

SN54S153, SN74S153 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS

recommended operating conditions

	S	SN54S153			SN74S153		
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Supply voltage, VCC	4.5	5	5.5	4.75	5	5.25	٧
High-level output current, IOH			-1			-1	mΑ
Low-level output current, IOL			20			20	mA
Operating free-air temperature, TA	-55		125	0		70	,C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	PARAMETER	TEST CONDITIONS†	MIN	TYP‡	MAX	UNIT
VIH	High-level input voltage		2			٧
VIL	Low-level input voltage				8.0	٧
ViK	Input clamp voltage	V _{CC} = MIN, I ₁ = -18 mA			-1.2	٧
	111-billoud output voltage	V _{CC} = MIN, V _{IH} = 2 V, Series 545	2.5	3.4		V
∨он	High-level output voltage	V _{IL} = 0.8 V, I _{OH} = -1 mA Series 745	2.7	3.4		v
V	Low-level output voltage	V _{CC} = MIN, V _{IH} = 2 V,	1		0.5	V
VOL	Low-level output voltage	V _{IL} = 0.8 V, I _{OL} = 20 mA			0.5	
ų į	Input current at maximum input voltage	V _{CC} = MAX, V _I = 5.5 V			1	mΑ
ΊΗ	High-level input current	V _{CC} = MAX, V ₁ = 2.7 V			50	μA
11L	Low-level input current	V _{CC} = MAX, V _I = 0.5 V			-2	mA
los	Short-circuit output current	V _{CC} = MAX	-40		-100	mΑ
logi.	Supply current, low-level output	V _{CC} = MAX, See Note 2		45	70	mΑ

[†]For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, VCC = 5 V, TA = 25°C

PARAMETER¶	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
^t PLH	Data	Y	C _L = 15 pF, R _L = 280 Ω, See Note 3]	6	9	ns
†PHL	Data	Y			6	9	ns
tPLH .	Select	Y			11.5	18	пѕ
tPHL.	Select	Y			12	18	ns
tРLН	Strobe G	Y			10	15	nş
tpHL _	Strobe Ĝ	Y			9	13.5	ns

¹tpLH = propagation delay time, low-to-high-level output

[‡]All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25 ^{\circ}\text{C}$.

 $[\]S$ Not more than one output should be shorted at a time and duration of short-circuit should not exceed one second.

NOTE 2: ${}^{\dagger}_{CCL}$ is measured with the outputs open and all inputs grounded,

tpHL = propagation delay time, high to-low level output

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.

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