

TC74HC03AFN

Quad 2-Input NAND Gate (open drain)

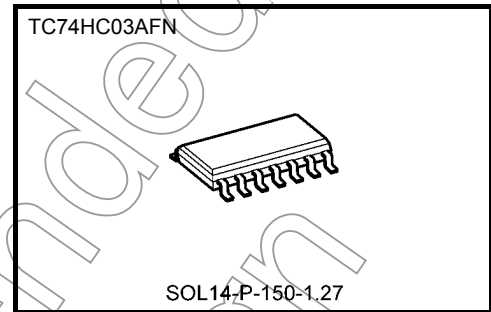
The TC74HC03A is a high speed CMOS 2-INPUT NAND GATE fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

Pin configuration and function are the same as the TC74HC00A. But the TC74HC03A has, as its outputs, high performance MOS N-channel transistors. (open-drain outputs) This device can, therefore, with a suitable pull-up resistors, be used in wired-AND, LED driver and other application.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Note: xxxFN (JEDEC SOP) is not available in Japan.

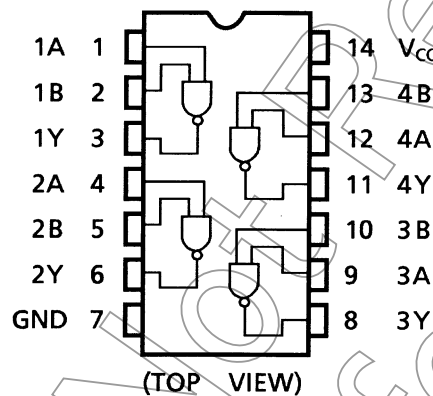


Weight
SOL14-P-150-1.27 : 0.12 g (typ.)

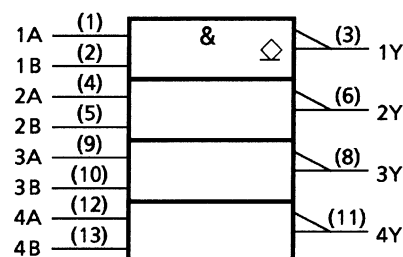
Features

- High speed: $t_{pz} = 5 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 1 \mu\text{A}$ (max) at $T_a = 25^\circ\text{C}$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Output drive capability: 10 LSTTL loads
- Wide operating voltage range: $V_{CC} \text{ (opr)} = 2 \sim 6 \text{ V}$
- Open drain structure
- Pin and function compatible with 74LS03

Pin Assignment



IEC Logic Symbol

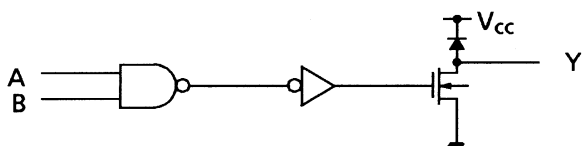


Truth Table

A	B	Y
L	L	Z
L	H	Z
H	L	Z
H	H	L

Z: High impedance

System Diagram (per gate)



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7	V
DC input voltage	V _{IN}	-0.5~V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	I _{OK}	±20	mA
DC output current	I _{OUT}	+25	mA
DC V _{CC} /ground current	I _{CC}	±50	mA
Power dissipation	P _D	180	mW
Storage temperature	T _{stg}	-65~150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 2: 500 mW in the range of T_a = -40°C~65°C. From T_a = 65°C to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2~6	V
Input voltage	V _{IN}	0~V _{CC}	V
Output voltage	V _{OUT}	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	t _r , t _f	0~1000 (V _{CC} = 2.0 V) 0~500 (V _{CC} = 4.5 V) 0~400 (V _{CC} = 6.0 V)	ns

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
				V _{CC} (V)	Min	Typ.	Max	Min	Max
High-level input voltage	V _{IH}	—		2.0	1.50	—	—	1.50	—
				4.5	3.15	—	—	3.15	—
				6.0	4.20	—	—	4.20	—
Low-level input voltage	V _{IL}	—		2.0	—	—	0.50	—	0.50
				4.5	—	—	1.35	—	1.35
				6.0	—	—	1.80	—	1.80
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 20 μA	2.0	—	0.0	0.1	—	0.1
				4.5	—	0.0	0.1	—	0.1
				6.0	—	0.0	0.1	—	0.1
			I _{OL} = 4 mA	4.5	—	0.17	0.26	—	0.33
				6.0	—	0.18	0.26	—	0.33
			I _{OL} = 5.2 mA	6.0	—	—	—	—	—
Output off-state current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC}		6.0	—	—	±0.5	—	±5.0
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	—	—	±0.1	—	±1.0
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	—	—	1.0	—	10.0

AC Characteristics (C_L = 15 pF, V_{CC} = 5 V, Ta = 25°C, input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output transition time	t _{THL}	—	—	4	8	ns
Propagation delay time	t _{pLZ}	R _L = 1 kΩ	—	5	12	ns
Propagation delay time	t _{pZL}	R _L = 1 kΩ	—	5	12	ns

AC Characteristics ($C_L = 50 \text{ pF}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition	V_{CC} (V)	$T_a = 25^\circ\text{C}$			$T_a = -40 \sim 85^\circ\text{C}$		Unit
				Min	Typ.	Max	Min	Max	
Output transition time	t_{THL}	—	2.0	—	30	75	—	95	ns
			4.5	—	8	15	—	19	
			6.0	—	7	13	—	16	
Propagation delay time	t_{pLZ}	$R_L = 1 \text{ k}\Omega$	2.0	—	20	75	—	95	ns
			4.5	—	10	15	—	19	
			6.0	—	9	13	—	16	
Propagation delay time	t_{pZL}	$R_L = 1 \text{ k}\Omega$	2.0	—	24	75	—	95	ns
			4.5	—	8	15	—	19	
			6.0	—	7	13	—	16	
Input capacitance	C_{IN}	—	—	—	5	10	—	10	pF
Output capacitance	C_{OUT}	—	—	—	3	—	—	—	pF
Power dissipation capacitance	C_{PD} (Note)	—	—	—	5	—	—	—	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

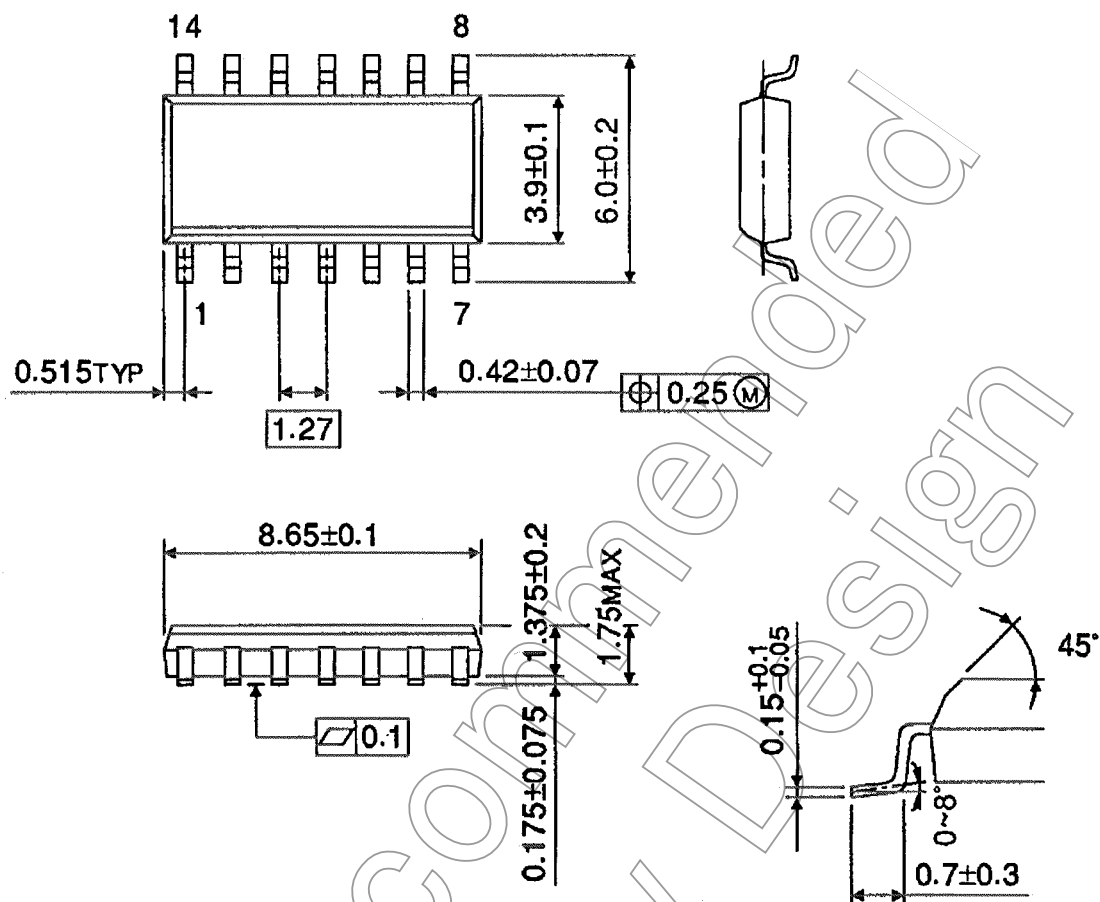
Average operating current can be obtained by the equation:

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$$

Package Dimensions (Note)

SOL14-P-150-1.27

Unit : mm



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

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